

The Knowledge-of-Exponent Assumptions and 3-Round Zero-Knowledge Protocols

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Abstract

Hada and Tanaka [10, 11] showed the existence of 3-round, negligible-error zero-knowledge arguments for NP based on a pair of non-standard assumptions, here called KEA1 and KEA2. In this paper we show that KEA2 is false. This renders vacuous the results of [10, 11]. However we recover these results under a suitably modified new assumption called KEA3. What we believe is most interesting is that we show that it is possible to “falsify” assumptions like KEA2 which, by their nature and quantifier-structure, do not lend themselves easily to “efficient falsification” [14].

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1 Introduction

A classical question in the theory of zero knowledge (ZK) [9] is whether there exist 3-round, negligible-error ZK proofs or arguments for NP. The difficulty in answering this question stems from the fact that such protocols would have to be non-black-box simulation ZK [8], and there are few approaches or techniques to this end. A positive answer has, however, been provided, by Hada and Tanaka [10, 11]. Their result (a negligible-error, 3-round ZK argument for NP) requires a pair of non-standard assumptions that we will denote by KEA1 and KEA2 in this paper.

THE ASSUMPTIONS, ROUGHLY. Let q be a prime such that $2q+1$ is also prime, and let g be a generator of the order q subgroup of Z_{2q+1}^* . Suppose we are given input q, g, g^a and want to output a pair (C, Y) such that $Y = C^a$. One way to do this is to pick some $c \in \mathbb{Z}_q$, let $C = g^c$, and let $Y = (g^a)^c$. Intuitively, KEA1 can be viewed as saying that this is the “only” way to produce such a pair. The assumption captures this by saying that any adversary outputting such a pair must “know” an exponent c such that $g^c = C$. The formalization asks that there be an “extractor” that can return c . Roughly:

KEA1: For any adversary \mathbf{A} , that takes input q, g, g^a and returns (C, Y) with $Y = C^a$, there exists an “extractor” $\bar{\mathbf{A}}$, which given the same inputs as \mathbf{A} returns c such that $g^c = C$.

Suppose we are given input q, g, g^a, g^b, g^{ab} and want to output a pair (C, Y) such that $Y = C^b$. One way to do this is to pick some $c \in \mathbb{Z}_q$, let $C = g^c$, and let $Y = (g^b)^c$. Another way is to pick some $c \in \mathbb{Z}_q$, let $C = (g^a)^c$, and let $Y = (g^{ab})^c$. Intuitively, KEA2 can be viewed as saying that these are the “only” ways to produce such a pair. The assumption captures this by saying that any adversary outputting such a pair must “know” an exponent c such that either $g^c = C$ or $(g^a)^c = C$. The formalization asks that there be an “extractor” that can return c . Roughly:

KEA2: For any adversary \mathbf{A} , that takes input q, g, g^a, g^b, g^{ab} and returns (C, Y) with $Y = C^b$, there exists an “extractor” $\bar{\mathbf{A}}$, which given the same inputs as \mathbf{A} returns c such that either $g^c = C$ or $(g^a)^c = C$.

As per [10, 11], adversaries and extractors are poly-sized families of (deterministic) circuits. See Definition 3.1 for a formalization of KEA2. (We do not formalize KEA1 in this paper since we do not need it. It is discussed here only as an introduction to KEA2 and because it plays a role in the results of [10, 11].)

HISTORY AND NOMENCLATURE OF THE ASSUMPTIONS. KEA1 is due to Damgård [6], and is used by [10, 11] to prove their protocol is ZK. To prove soundness of their protocol, Hada and Tanaka [10, 11] introduce and use KEA2. (In addition, they make the Discrete Logarithm Assumption, DLA.) The preliminary version of their work [10] referred to the assumptions as SDHA1 and SDHA2 (Strong Diffie-Hellman Assumptions 1 and 2), respectively. However, the full version [11] points out that the formalizations in the preliminary version are flawed, and provides corrected versions called non-uniform-DA1 and non-uniform-DA2. We refer to the latter, but use the terminology of Naor [14] which we feel is more reflective of the content of the assumption: “KEA” stands for “Knowledge of Exponent Assumption”, the exponent being the value c above.

FALSIFYING KEA2. In this paper we show that KEA2 is false. What is interesting about this—besides the fact that it renders the results of [10, 11] vacuous—is that we are able to “falsify” an assumption whose nature, as pointed out by Naor [14], does not lend itself easily to “efficient falsification.” Let us explain this issue before expanding more on the result itself.

The most standard format for an assumption is to ask that the probability that an adversary produces a certain output on certain inputs is negligible. (For example, the Factoring assumption is of this type.) To show such an assumption is false, we can present an “attack,” in the form of an adversary whose success probability is not negligible. (For example, an efficient factoring algorithm.)

KEA1 and KEA2 are not of this standard format. They involve a more complex quantification: “For every adversary there exists an extractor such that ...”. To show KEA2 is false, we must show there exists an adversary *for which there exists no extractor*. As we will see later, it is relatively simple to identify an adversary for which there does not *appear* to exist an extractor, but how can we actually show that none of the infinite number of possible extractors succeeds?

AN ANALOGY. The difficulty of falsifying an assumption with the quantifier format of KEA2 may be better appreciated via an analogy. The definition of ZK has a similar quantifier format: “For every (cheating) verifier there exists a simulator such that ...”. This makes it hard to show a protocol is not ZK, for, even though we may be able to identify a cheating verifier strategy that appears hard to simulate, it is not clear how we can actually show no simulator exists.¹ Indeed, this has usually kept us from showing protocols are not ZK.² (It has been possible to show protocols are not black-box simulation ZK [8], taking advantage of the fact that the quantification in this definition is different from that of ZK itself.)

FALSIFICATION RESULT. At an intuitive level, the weakness in KEA2 is easy to see, and indeed it is surprising this was not noted before. Namely consider an adversary \mathbf{A} that on input q, g, g^a, g^b, g^{ab} picks c_1, c_2 in some fashion, and outputs (C, Y) where $C = g^{c_1}(g^a)^{c_2}$ and $Y = (g^b)^{c_1}(g^{ab})^{c_2}$. This adversary does not appear to “know” c such that either $g^c = C$ or $(g^a)^c = C$. The difficulty, however, as indicated above, is to prove that there does not exist an extractor. We do this by first specifying a particular strategy for choosing c_1 and c_2 and then showing that if there exists an extractor for the resulting adversary then this extractor can be used to solve the discrete logarithm problem (DLP). Thus, our result (cf. Theorem 3.2) is that if the DLP is hard then KEA2 is false. (Note that if the DLP is easy, then KEA2 is true, for the extractor can simply compute a discrete logarithm of C and output it, and thus the assumption that it is hard is necessary to falsify KEA2.)

KEA3. Providing a 3-round, negligible-error ZK protocol for NP is a challenging problem that has attracted considerable research effort. The fact that KEA2 is false means that we “lose” one of the only positive results [10, 11] that we had on this subject. Accordingly, we would like to “recover” it. To this end, we propose a modification of KEA2 that addresses the weakness we found. The new assumption is, roughly, as follows:

KEA3: For any adversary \mathbf{A} , that takes input q, g, g^a, g^b, g^{ab} and returns (C, Y) with $Y = C^b$, there exists an “extractor” $\bar{\mathbf{A}}$, which given the same inputs as \mathbf{A} returns c_1, c_2 such that $g^{c_1}(g^a)^{c_2} = C$.

RECOVERING THE ZK RESULT. The falsity of KEA2 invalidates Hada and Tanaka’s proof of the soundness of their protocol, which we denote by HTP [11]. However, this does not mean that HTP is not sound: perhaps it is and this could be proved under another assumption, such as KEA3. This turns out to be almost but not quite true. We identify a small bug in HTP based on which we can present a successful cheating prover strategy, showing that HTP is not sound. This is easily fixed, however, to yield a protocol we call pHTP (patched HTP). This protocol is close enough to HTP that the proof of ZK (based on KEA1) is unchanged. On the other hand, we are able to prove its soundness based on KEA3 and DLA (cf. Theorem 5.3). In summary, we are able to show that assuming KEA1, KEA3 and DLA, there exists a 3-round, negligible error ZK argument for NP.

¹ For example, it is hard to imagine how one could find a simulator for the cheating verifier, for Blum’s ZK proof of Hamiltonian Cycle [4], that produces its challenges by hashing the permuted graphs sent by the prover in the first step. However, no proof that such a simulator does not exist has ever emerged.

² An interesting conditional result [15] is that the parallel version of the Fiat-Shamir [7] protocol is not ZK, unless there is no hash function that, when applied to collapse this protocol, results in a secure signature scheme. Our result too is conditional.

REMARK. We emphasize that we have not found any weaknesses in KEA1, an assumption used not only in [10, 11] but also elsewhere.

RELATED WORK. Since [10, 11] there has been more progress with regard to the design of non-black-box simulation ZK protocols [1]. However, this work does not provide a 3-round, negligible-error ZK protocol for NP. To date, there have been only two positive results. One is that of [10, 11], broken and recovered in this paper. The other, which builds a proof system rather than an argument, is reported in [13] and further documented in [12]. It also relies on non-standard assumptions, but different from the Knowledge of Exponent type ones. Roughly, they assume the existence of a hash function such that a certain discrete-log based protocol related to the non-interactive OT of [3] is a proof of knowledge.

2 Preliminaries

If x is a binary string, then $|x|$ denotes its length, and if $n \geq 1$ is an integer, then $|n|$ denotes the length of its binary encoding, meaning the unique integer ℓ such that $2^{\ell-1} \leq n < 2^\ell$. The empty string is denoted ε . We let $\mathbb{N} = \{1, 2, 3, \dots\}$ be the set of positive integers. If q is a prime number such that $2q + 1$ is also prime, then we denote by G_q the subgroup of quadratic residues of \mathbb{Z}_{2q+1}^* . (Operations are modulo $2q + 1$ but we will omit writing “mod $2q + 1$ ” for simplicity.) Recall this is a cyclic subgroup of order q . If g is a generator of G_q then we let $\text{DLog}_{q,g}: G_q \rightarrow \mathbb{Z}_q$ denote the associated discrete logarithm function, meaning $\text{DLog}_{q,g}(g^a) = a$ for any $a \in \mathbb{Z}_q$. We let

$$GL = \{ (q, g) : q, 2q + 1 \text{ are primes and } g \text{ is a generator of } G_q \} .$$

For any $n \in \mathbb{N}$ we let GL_n be the set of all $(q, g) \in GL$ such that the length of the binary representation of $2q + 1$ is n bits, i.e.,

$$GL_n = \{ (q, g) \in GL : |2q + 1| = n \} .$$

Assumptions and problems in [10, 11] involve circuits. A family of circuits $\mathbf{C} = \{\mathbf{C}_n\}_{n \in \mathbb{N}}$ contains one circuit for each value of $n \in \mathbb{N}$. It is poly-size if there is a polynomial p such that the size of \mathbf{C}_n is at most $p(n)$ for all $n \in \mathbb{N}$. Unless otherwise stated, *circuits are deterministic*. If they are randomized, we will say so explicitly. We now recall the DLA following [11].

Definition 2.1 Let $\mathbf{I} = \{\mathbf{I}_n\}_{n \in \mathbb{N}}$ be a family of randomized circuits, and $\nu: \mathbb{N} \rightarrow [0, 1]$ a function. We associate to any $n \in \mathbb{N}$ and any $(q, g) \in GL_n$ the following experiment:

Experiment $\mathbf{Exp}_{\mathbf{I}}^{\text{dl}}(n, q, g)$
 $a \xleftarrow{\$} \mathbb{Z}_q; A \leftarrow g^a; \bar{a} \xleftarrow{\$} \mathbf{I}_n(q, g, A)$
 If $a = \bar{a}$ then return 1 else return 0

We let

$$\mathbf{Adv}_{\mathbf{I}}^{\text{dl}}(n, q, g) = \Pr \left[\mathbf{Exp}_{\mathbf{I}}^{\text{dl}}(n, q, g) = 1 \right]$$

denote the *advantage* of \mathbf{I} on inputs n, q, g , the probability being over the random choice of a and the coins of \mathbf{I}_n , if any. We say that \mathbf{I} has *success bound* ν if

$$\forall n \in \mathbb{N} \forall (q, g) \in GL_n : \mathbf{Adv}_{\mathbf{I}}^{\text{dl}}(n, q, g) \leq \nu(n) .$$

We say that the Discrete Logarithm Assumption (DLA) holds if for every poly-size family of circuits \mathbf{I} there exists a negligible function ν such that \mathbf{I} has success bound ν . ■

The above formulation of the DLA, which, as we have indicated, follows [11], has some non-standard features that are important for their results. Let us discuss these briefly.

First, we note that the definition of the success bound is not with respect to (q, g) being chosen according to some distribution as is standard, but rather makes the stronger requirement that the advantage of \mathbf{I} is small for all (q, g) .

Second, we stress that the assumption only requires poly-size families of *deterministic* circuits to have a negligible success bound. However, in their proofs, which aim to contradict the DLA, Hada and Tanaka [10, 11] build adversaries that are poly-size families of randomized circuits, and then argue that these can be converted to related poly-size families of deterministic circuits that do not have a negligible success bound. We will also need to build such randomized adversaries, but, rather than using ad hoc conversion arguments repeated across proofs, we note the following more general Proposition, which simply says that DLA, as per Definition 2.1, implies that poly-size families of randomized circuits also have a negligible success bound. We will appeal to this in several later places in this paper.

Proposition 2.2 Assume the DLA, and let $\mathbf{J} = \{\mathbf{J}_n\}_{n \in \mathbb{N}}$ be a poly-size family of *randomized* circuits. Then there exists a negligible function ν such that \mathbf{J} has success bound ν . \blacksquare

As is typical in such claims, the proof proceeds by showing that for every n there exists a “good” choice of coins for \mathbf{J}_n , and by embedding these coins we get a deterministic circuit. However, which coins are “good” can depend not only on q, g but also on the random challenge A , and thus it may not be clear a priori that a single choice depending only on n exists. For completeness we provide a proof of Proposition 2.2 in Appendix A.

3 KEA2 is false

We begin by recalling the assumption. Our presentation of it is slightly different, but clearly equivalent to, that of [11]: we have merged the two separate conditions of their formalization into one. Recall that they refer to this assumption as “non-uniform-DA2”, and it was referred to, under a different and incorrect formalization, as SDHA2 in [10].

Definition 3.1 Let $\mathbf{A} = \{\mathbf{A}_n\}_{n \in \mathbb{N}}$ and $\bar{\mathbf{A}} = \{\bar{\mathbf{A}}_n\}_{n \in \mathbb{N}}$ be families of circuits, and $\nu: \mathbb{N} \rightarrow [0, 1]$ a function. We associate to any $n \in \mathbb{N}$, any $(q, g) \in GL_n$, and any $A \in G_q$ the following experiment:

Experiment $\mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A)$
 $b \xleftarrow{\$} \mathbb{Z}_q; B \leftarrow g^b; X \leftarrow A^b$
 $(C, Y) \leftarrow \mathbf{A}_n(q, g, A, B, X); c \leftarrow \bar{\mathbf{A}}_n(q, g, A, B, X)$
 If $(Y = C^b \text{ AND } g^c \neq C \text{ AND } A^c \neq C)$ then return 1 else return 0

We let

$$\mathbf{Adv}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A) = \Pr \left[\mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A) = 1 \right]$$

denote the *advantage* of \mathbf{A} relative to $\bar{\mathbf{A}}$ on inputs n, q, g, A . We say that $\bar{\mathbf{A}}$ is a *kea2-extractor* for \mathbf{A} with error bound ν if

$$\forall n \in \mathbb{N} \forall (q, g) \in GL_n \forall A \in G_q : \mathbf{Adv}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A) \leq \nu(n).$$

We say that KEA2 holds if for every poly-size family of circuits \mathbf{A} there exists a poly-size family of circuits $\bar{\mathbf{A}}$ and a negligible function ν such that $\bar{\mathbf{A}}$ is a kea2-extractor for \mathbf{A} with error bound ν . \blacksquare

We stress again that in this formulation, following [11], both the adversary and the extractor are families of *deterministic* circuits. One can consider various variants of the assumption, including an extension to families of randomized circuits, and we discuss these variants following the theorem below.

$\mathbf{A}_n(q, g, A, B, X)$ $C \leftarrow gA$ $Y \leftarrow BX$ Return (C, Y)	$\mathbf{J}_n(q, g, A)$ $b \xleftarrow{\$} \mathbb{Z}_q; B \leftarrow g^b; X \leftarrow A^b$ $c \leftarrow \bar{\mathbf{A}}_n(q, g, A, B, X)$ $C \leftarrow gA$ If $g^c = C$ then $\bar{a} \leftarrow (c - 1) \bmod q$ EndIf If $(A^c = C \text{ AND } c \neq 1)$ then $\bar{a} \leftarrow (c - 1)^{-1} \bmod q$ EndIf Return \bar{a}
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Figure 1: Adversary $\mathbf{A} = \{\mathbf{A}_n\}_{n \in \mathbb{N}}$ for the KEA2 problem and adversary $\mathbf{J} = \{\mathbf{J}_n\}_{n \in \mathbb{N}}$ for the DLP, for the proof of Theorem 3.2.

Theorem 3.2 If the DLA holds then KEA2 is false. \blacksquare

The basic idea behind the failure of the assumption, as sketched in Section 1, is simple. Consider an adversary given input q, g, A, B, X , where $A = g^a, B = g^b$ and $X = g^{ab}$. The assumption says that there are only two ways for the adversary to output a pair C, Y satisfying $Y = C^b$. One way is to pick some c , let $C = g^c$ and let $Y = B^c$. The other way is to pick some c , let $C = A^c$ and let $Y = X^c$. The assumption thus states that the adversary “knows” c such that either $C = g^c$ (i.e., $c = \text{DLog}_{q,g}(C)$) or $C = A^c$ (i.e., $c = \text{DLog}_{q,A}(C)$). This ignores the possibility of performing a linear combination of the two steps above. In other words, an adversary might pick c_1, c_2 , let $C = g^{c_1} A^{c_2}$ and $Y = B^{c_1} X^{c_2}$. In this case, $Y = C^b$ but the adversary does not appear to necessarily know $\text{DLog}_{q,g}(C) = c_1 + c_2 \text{DLog}_{q,g}(A)$, and neither does it necessarily know $\text{DLog}_{q,A}(C) = c_1 \text{DLog}_{q,A}(g) + c_2$.

However, going from this intuition to an actual proof that the assumption is false takes some work, for several reasons. The above may be intuition that there exists an adversary for which there would not exist an extractor, but we need to *prove* that there is no extractor. This cannot be done unconditionally, since certainly if the discrete logarithm problem (DLP) is easy, then in fact there is an extractor: it simply computes $\text{DLog}_{q,g}(C)$ and returns it. Accordingly, our strategy will be to present an adversary \mathbf{A} for which we can prove that if there exists an extractor $\bar{\mathbf{A}}$ then there is a method to efficiently compute the discrete logarithm of A .

An issue in implementing this is that the natural adversary \mathbf{A} arising from the above intuition is randomized, picking c_1, c_2 at random and forming C, Y as indicated, but our adversaries must be deterministic. We resolve this by designing an adversary that makes certain specific choices of c_1, c_2 . We now proceed to the formal proof.

PROOF OF THEOREM 3.2. Assume to the contrary that KEA2 is true. We show that the DLP is easy.

The outline of the proof is as follows. We first construct an adversary \mathbf{A} for the KEA2 problem. By assumption, there exists for it an extractor $\bar{\mathbf{A}}$ with negligible error bound. Using $\bar{\mathbf{A}}$, we then present a poly-size family of randomized circuits $\mathbf{J} = \{\mathbf{J}_n\}_{n \in \mathbb{N}}$ and show that it does not have a negligible success bound. By Proposition 2.2, this contradicts the DLA.

The poly-size family of circuits $\mathbf{A} = \{\mathbf{A}_n\}_{n \in \mathbb{N}}$ is presented in Figure 1. Now, under KEA2, there exists a poly-size family of circuits $\bar{\mathbf{A}} = \{\bar{\mathbf{A}}_n\}_{n \in \mathbb{N}}$ and a negligible function ν such that $\bar{\mathbf{A}}$ is an extractor for \mathbf{A} with error bound ν . Using $\bar{\mathbf{A}}$, we define the poly-size family of circuits $\mathbf{J} = \{\mathbf{J}_n\}_{n \in \mathbb{N}}$ shown in Figure 1.

Claim 3.3 For all $n \in \mathbb{N}$, all $(q, g) \in GL_n$ and all $A \in G_q$

$$\Pr \left[\bar{a} \xleftarrow{\$} \mathbf{J}_n(q, g, A) : g^{\bar{a}} \neq A \right] \leq \nu(n).$$

Note the claim shows much more than we need. Namely, \mathbf{J} does not merely have a success bound that is not negligible. In fact, it succeeds with probability almost one.

Proof of Claim 3.3: We let $\Pr[\cdot]$ denote the probability in the experiment of executing $\mathbf{J}_n(q, g, A)$. We first write some inequalities leading to the claim and then justify them:

$$\Pr[g^{\bar{a}} \neq A] \leq \Pr[g^c \neq C \wedge A^c \neq C] \quad (1)$$

$$\leq \mathbf{Adv}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A) \quad (2)$$

$$\leq \nu(n). \quad (3)$$

We justify Equation (1) by showing that if $g^c = C$ or $A^c = C$ then $g^{\bar{a}} = A$. First assume $g^c = C$. Since $C = gA$, we have $g^c = gA$, whence $A = g^{c-1}$. Since we have set $\bar{a} = (c-1) \bmod q$, we have $A = g^{\bar{a}}$. Next assume $A^c = C$. Since $C = gA$, we have $A^c = gA$, whence $A^{c-1} = g$. Now observe that $c \neq 1$, because otherwise $A^c = A \neq gA$. Since $c \neq 1$ and q is prime, $c-1$ has an inverse modulo q which we have denoted by \bar{a} . Raising both sides of the equation “ $A^{c-1} = g$ ” to the power \bar{a} we get $A = g^{\bar{a}}$.

$\mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A)$ returns 1 exactly when $Y = C^b$ and $g^c \neq C$ and $A^c \neq C$. By construction of \mathbf{A} , we have $C = gA$ and $Y = BX$, and thus $Y = C^b$, so $\mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea2}}(n, q, g, A)$ returns 1 exactly when $g^c \neq C$ and $A^c \neq C$. This justifies Equation (2).

Equation (3) is justified by the assumption that $\bar{\mathbf{A}}$ is an extractor for \mathbf{A} with error bound ν . ■

Claim 3.3 implies that \mathbf{J} does not have a negligible success bound, which, by Proposition 2.2, shows that the DLP is not hard, contradicting the assumption made in this Proposition. This completes the proof of Theorem 3.2.

EXTENSIONS AND VARIANTS. There are many ways in which the formalization of Definition 3.1 can be varied to capture the same basic intuition. However, Theorem 3.2 extends to these variants as well. Let us discuss this briefly.

As mentioned above, we might want to allow the adversary to be randomized. (In that case, it is important that the extractor get the coins of the adversary as an additional input, since otherwise the assumption is clearly false.) Theorem 3.2 remains true for the resulting assumption, in particular because it is stronger than the original assumption. (Note however that the proof of the theorem would be easier for this stronger assumption.)

Another variant is that adversaries and extractors are uniform, namely standard algorithms, not circuits. (In this case we should certainly allow both to be randomized, and should again give the extractor the coins of the adversary.) Again, it is easy to see that Theorem 3.2 extends to show that the assumption remains false.

4 The KEA3 assumption

The obvious fix to KEA2 is to take into account the possibility of linear combinations by saying this is the only thing the adversary can do. This leads to the following.

Definition 4.1 Let $\mathbf{A} = \{\mathbf{A}_n\}_{n \in \mathbb{N}}$ and $\bar{\mathbf{A}} = \{\bar{\mathbf{A}}_n\}_{n \in \mathbb{N}}$ be families of circuits, and $\nu: \mathbb{N} \rightarrow [0, 1]$ a function. We associate to any $n \in \mathbb{N}$, any $(q, g) \in GL_n$, and any $A \in G_q$ the following experiment:

$$\begin{aligned} &\text{Experiment } \mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A) \\ &\quad b \xleftarrow{\$} \mathbb{Z}_q; B \leftarrow g^b; X \leftarrow A^b \end{aligned}$$

$(C, Y) \leftarrow \mathbf{A}_n(q, g, A, B, X); (c_1, c_2) \leftarrow \bar{\mathbf{A}}_n(q, g, A, B, X)$
 If $(Y = C^b \text{ AND } g^{c_1} A^{c_2} \neq C)$ then return 1 else return 0

We let

$$\mathbf{Adv}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A) = \Pr \left[\mathbf{Exp}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A) = 1 \right]$$

denote the *advantage* of \mathbf{A} relative to $\bar{\mathbf{A}}$ on inputs n, q, g, A . We say that $\bar{\mathbf{A}}$ is a *kea3-extractor* for \mathbf{A} with error bound ν if

$$\forall n \in \mathbb{N} \forall (q, g) \in GL_n \forall A \in G_q : \mathbf{Adv}_{\mathbf{A}, \bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A) \leq \nu(n).$$

We say that KEA3 holds if for every poly-size family of circuits \mathbf{A} there exists a poly-size family of circuits $\bar{\mathbf{A}}$ and a negligible function ν such that $\bar{\mathbf{A}}$ is a kea3-extractor for \mathbf{A} with error bound ν . ■

We have formulated this assumption in the style of the formalization of KEA2 of [11] given in Definition 3.1. Naturally, variants such as discussed above are possible. Namely, we could strengthen the assumption to allow the adversary to be a family of randomized circuits, of course then giving the extractor the adversary's coins as an additional input. We do not do this because we do not need it for what follows. We could also formulate a uniform-complexity version of the assumption. We do not do this because it does not suffice to prove the results that follow. However, these extensions or variations might be useful in other contexts.

5 Three-round zero-knowledge

The falsity of KEA2 renders vacuous the result of [10, 11] saying that there exists a negligible-error, 3-round ZK arguments for NP. In this section we look at recovering this result.

We first consider the protocol of [10, 11], here called HTP. What has been lost is the proof of soundness (i.e. of negligible error). The simplest thing one could hope for is to re-prove soundness of HTP under KEA3 without modifying the protocol. However, we identify a bug in HTP that renders it unsound. This bug has nothing to do with the assumptions on which the proof of soundness was or can be based.

The bug is however small and easily fixed. We consider a modified protocol called pHTP, for patched-HTP. We are able to show it is sound (i.e. has negligible error) under KEA3. Since we have modified the protocol we need to re-establish ZK under KEA1 as well, but this is easily done.

ARGUMENTS. We begin by recalling some definitions. An argument for an NP language L [5] is a two-party protocol in which a polynomial-time prover tries to “convince” a polynomial-time verifier that their common input x belongs to L . (A party is said to be polynomial time if its running time is polynomial in the length of the common input.) In addition to x , the prover has an auxiliary input a . The protocol is a message exchange at the end of which the verifier outputs a bit indicating its decision to accept or reject. The probability (over the coin tosses of both parties) that the verifier accepts is denoted $\mathbf{Acc}_V^{P,a}(x)$. The formal definition follows.

Definition 5.1 A two-party protocol (P, V) , where P and V are both polynomial time, is an *argument* for L with error probability $\delta : \mathbb{N} \rightarrow [0, 1]$, if the following conditions are satisfied:

Completeness: For all $x \in L$ there exists $w \in \{0, 1\}^*$ such that $\mathbf{Acc}_V^{P,w}(x) = 1$.

Soundness: For all probabilistic polynomial-time algorithms \hat{P} , all sufficiently long $x \notin L$, and all $a \in \{0, 1\}^*$, $\mathbf{Acc}_V^{\hat{P},a}(x) \leq \delta(|x|)$.

We say (P, V) is a *negligible-error argument* for L if there exists a negligible function $\delta : \mathbb{N} \rightarrow [0, 1]$ such that (P, V) is an argument for L with error probability δ . ■

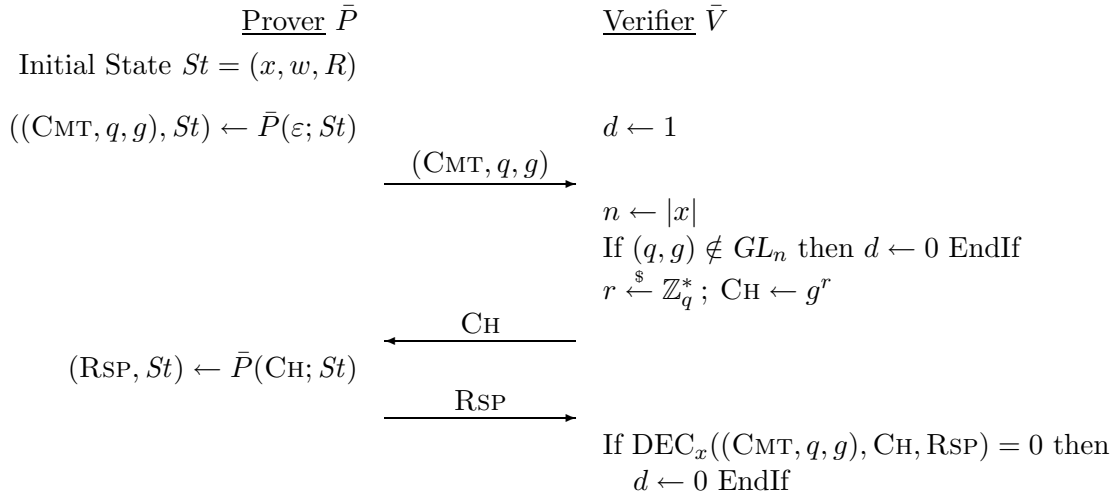


Figure 2: **A 3-round argument.** The common input is x . Prover \bar{P} has auxiliary input w and random tape R , and maintains state St . Verifier \bar{V} returns boolean decision d .

CANONICAL PROTOCOLS. The 3-round protocol proposed by [10, 11], which we call HTP, is based on a 3-round argument (\bar{P}, \bar{V}) for an NP-complete language L with the following properties:

- (1) The protocol is of the form depicted in Figure 2. The prover is identified with a function \bar{P} that given an incoming message M_{in} (this is ε when the prover is initiating the protocol) and its current state St , returns an outgoing message M_{out} and an updated state. The initial state of the prover is (x, w, R) , where x is the common input, w is an auxiliary input and R is a random tape. The prover's first message is called its *commitment*. This is a tuple consisting of a string CMT, a prime number q and an element g , where $(q, g) \in GL_{|x|}$. The verifier selects a *challenge* CH uniformly at random from G_q , and, upon receiving a *response* RSP from the prover, applies a deterministic *decision predicate* $\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP})$ to compute a boolean decision.
- (2) For any $x \notin L$ and any commitment (CMT, q, g) , where $(q, g) \in GL_{|x|}$, there is at most one challenge $\text{CH} \in G_q$ for which there exists a response $\text{RSP} \in \{0, 1\}^*$ such that $\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1$. This property is called *strong soundness*.
- (3) The protocol is honest-verifier zero knowledge (HVZK), meaning there exists a probabilistic polynomial-time *simulator* S such that the following two ensembles are computationally indistinguishable:

$$\{S(x)\}_{x \in L} \quad \text{and} \quad \{\mathbf{View}_{\bar{V}}^{\bar{P}, W(x)}(x)\}_{x \in L},$$

where W is any function that given an input in L returns a witness to its membership in L , and $\mathbf{View}_{\bar{V}}^{\bar{P}, w}(x)$, is a random variable taking value \bar{V} 's internal coin tosses and the sequence of messages it receives during an interaction between prover \bar{P} (with auxiliary input w) and verifier \bar{V} on common input x .

If (\bar{P}, \bar{V}) is a 3-round argument for an NP-complete language, meeting the three conditions above, then we refer to (\bar{P}, \bar{V}) as a *canonical argument*. In what follows, we assume that we have such canonical arguments. They can be constructed in various ways. For example, a canonical argument

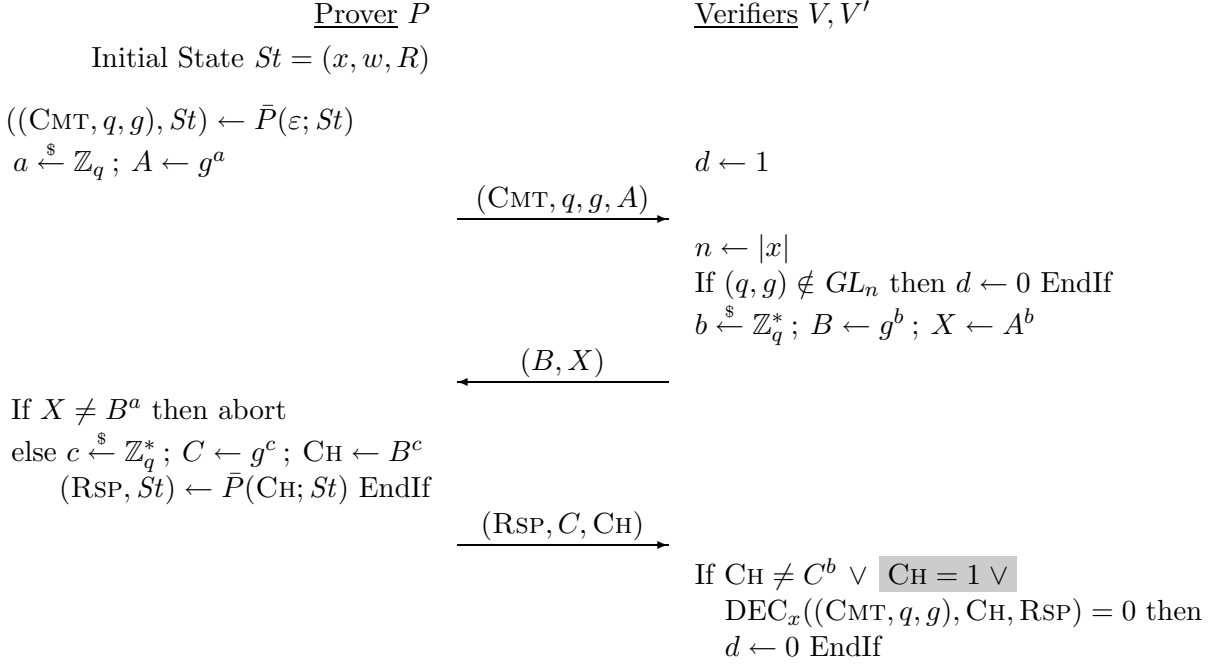


Figure 3: **HTP and pHTP**. Verifier V of protocol $\text{HTP} = (P, V)$ does not include the highlighted portion. Verifier V' of protocol $\text{pHTP} = (P, V')$ does.

can be constructed by modifying the parallel composition of Blum’s zero-knowledge protocol for the Hamiltonian circuit problem [4], as described in [10, 11].

THE HADA-TANAKA PROTOCOL. Let (\bar{P}, \bar{V}) be a canonical argument for an NP-complete language L , and let DEC be the verifier’s decision predicate. The Hada-Tanaka protocol $\text{HTP} = (P, V)$ is described in Figure 3. Note V ’s decision predicate does not include the highlighted portion of its code.

We now observe that the HTP protocol is unsound. More precisely, there exist canonical arguments such that the HTP protocol based on them does not have negligible error. This is true for any canonical argument (\bar{P}, \bar{V}) satisfying the extra condition that for infinitely many $x \notin L$ there exists a commitment (CMT_x, q_x, g_x) for which there is a response RSP_x to challenge 1 that will make the verifier accept. There are many such canonical arguments. For instance, a canonical argument satisfying this condition results from using an appropriate encoding of group elements in Hada and Tanaka’s modification of the parallel composition of Blum’s zero-knowledge protocol for the Hamiltonian circuit problem.

Proposition 5.2 Let HTP be the Hada-Tanaka protocol based on a canonical argument satisfying the condition stated above. Then there exists a polynomial-time prover for HTP that can make the verifier accept with probability one for infinitely many common inputs not in L . \blacksquare

Proof of Proposition 5.2: Let (\bar{P}, \bar{V}) be the canonical protocol and let V be the verifier of the corresponding protocol HTP. Consider a cheating prover \hat{P} that on initial state $(x, ((\text{CMT}_x, q_x, g_x), \text{RSP}_x), \varepsilon)$ selects an exponent $a \in \mathbb{Z}_{q_x}$ uniformly at random, and sends $(\text{CMT}_x, q_x, g_x, g_x^a)$ as its commitment to verifier V . Upon receiving a challenge (B, X) , it checks if $X = B^a$. If not, it aborts. Otherwise, it sends $(\text{RSP}_x, 1, 1)$ as its response to V . By the assumption about protocol (\bar{P}, \bar{V}) , for infinitely many

$x \notin L$ there exists an auxiliary input $y = ((\text{CMT}_x, q_x, g_x), \text{RSP}_x) \in \{0, 1\}^*$ such that $\mathbf{Acc}_{V'}^{\hat{P}, y}(x) = 1$. \blacksquare

PATCHED HTP. The above attack can be avoided by modifying the verifier to include the highlighted portion of the code in Figure 3. We call the resulting verifier V' . The following guarantees that the protocol pHTP = (P, V') is sound under KEA3, if the DLP is hard.

Theorem 5.3 If KEA3 holds, the DLA holds, and (\bar{P}, \bar{V}) is a canonical 3-round argument for an NP-complete language L , then pHTP = (P, V') as defined in Figure 3 is a negligible-error argument for L . \blacksquare

PROOF OF THEOREM 5.3. Completeness follows directly from the completeness of protocol (\bar{P}, \bar{V}) .

To prove soundness, we proceed by contradiction. Assume that pHTP is not sound, i.e., there is no negligible function δ such that the soundness condition in Definition 5.1 holds with respect to δ . We show that the DLP is easy under KEA3.

By the assumption that pHTP is not sound and a result of Bellare [2], there exists a probabilistic polynomial-time algorithm \hat{P} such that the function

$$\mathbf{Err}_{\hat{P}}(n) = \max\{ \mathbf{Acc}_{V'}^{\hat{P}, a}(x) : x \in \{0, 1\}^n \wedge x \notin L \wedge a \in \{0, 1\}^* \}^3$$

is not negligible. Hence there exists a probabilistic polynomial-time algorithm \hat{P} , a polynomial p , and an infinite set $S = \{ (x, a) : x \in \{0, 1\}^* \setminus L \wedge a \in \{0, 1\}^* \}$ such that for every $(x, a) \in S$

$$\mathbf{Acc}_{V'}^{\hat{P}, a}(x) > 1/p(|x|), \quad (4)$$

and $\{ x \in \{0, 1\}^* : \exists a \in \{0, 1\}^* \text{ such that } (x, a) \in S \}$ is infinite.

Since \hat{P} takes an auxiliary input a , we may assume, without loss of generality, that \hat{P} is deterministic. We also assume that, if (CMT, q', g', A') is \hat{P} 's commitment on input ε when the initial state is (x, a, ε) , for some $x, a \in \{0, 1\}^*$ with $|x| = n$, then $(q', g') \in GL_n$. (There exists a prover \hat{P}' for which $\mathbf{Acc}_{V'}^{\hat{P}', a}(x) = \mathbf{Acc}_{V'}^{\hat{P}, a}(x)$ for every $x, a \in \{0, 1\}^*$ and this assumption holds.) We will use \hat{P} to construct an adversary \mathbf{A} for the KEA3 problem. By assumption, there exists for it an extractor $\bar{\mathbf{A}}$ with negligible error bound. Using $\bar{\mathbf{A}}$ and \hat{P} , we then present a poly-size family of randomized circuits $\mathbf{J} = \{ \mathbf{J}_n \}_{n \in \mathbb{N}}$ and show that it does not have a negligible success bound. By Proposition 2.2, this shows that the DLP is not hard.

Let $K = \{ n \in \mathbb{N} : \exists (x, a) \in S \text{ such that } |x| = n \}$. We observe that K is an infinite set. For each $n \in K$, fix $(x, a) \in S$ such that $|x| = n$. The poly-size family of circuits $\mathbf{A} = \{ \mathbf{A}_n \}_{n \in \mathbb{N}}$ is presented in Figure 4. Now, under KEA3, there exists a poly-size family of circuits $\bar{\mathbf{A}} = \{ \bar{\mathbf{A}}_n \}_{n \in \mathbb{N}}$ and a negligible function ν such that $\bar{\mathbf{A}}$ is an extractor for \mathbf{A} with error bound ν . For each $n \in K$, let $a' = \text{DLog}_{q', g'}(A')$, where (CMT, q', g', A') is \hat{P} 's commitment on input ε when the initial state is (x, a, ε) . Using $\bar{\mathbf{A}}$, we define the poly-size family of circuits $\mathbf{J} = \{ \mathbf{J}_n \}_{n \in \mathbb{N}}$ shown in Figure 4.

Claim 5.4 For infinitely many $n \in \mathbb{N}$ there exists $(q, g) \in GL_n$ such that for every $A \in G_q$

$$\Pr \left[\bar{a} \stackrel{\$}{\leftarrow} \mathbf{J}_n(q, g, A) : g^{\bar{a}} = A \right] > \frac{1}{p(n)^2} - \frac{8}{2^n p(n)} - 2\nu(n).$$

Proof: We let $\Pr[\cdot]$ denote the probability in the experiment of executing $\mathbf{J}_n(q, g, A)$. We show that

³We note that this set is finite since \hat{P} is a polynomial-time algorithm and $\mathbf{Acc}_{V'}^{\hat{P}, a}(x)$ depends only on the first $t_{\hat{P}}(|x|)$ bits of a , where $t_{\hat{P}}(\cdot)$ is the running time of \hat{P} .

$\mathbf{A}_n(q, g, A, B, X) \quad // n \in K$
 $St \leftarrow (x, a, \varepsilon); ((\text{CMT}, q', g', A'), St) \leftarrow \widehat{P}(\varepsilon; St)$
 If $q' \neq q \vee g' \neq g \vee A' \neq A$ then return $(1, 1)$
 else $((\text{RSP}, C, \text{CH}), St) \leftarrow \widehat{P}((B, X); St);$ return (C, CH) EndIf

$\mathbf{A}_n(q, g, A, B, X) \quad // n \notin K$
 Return $(1, 1)$

$\mathbf{J}_n(q, g, A) \quad // n \in K$
 $St \leftarrow (x, a, \varepsilon); ((\text{CMT}, q', g', A'), St) \leftarrow \widehat{P}(\varepsilon; St)$
 If $q' \neq q \vee g' \neq g$ then return \perp EndIf
 $b \xleftarrow{\$} \mathbb{Z}_q; B \leftarrow A \cdot g^b; X \leftarrow B^{a'}$
 $((\text{RSP}, C, \text{CH}), St_1) \leftarrow \widehat{P}((B, X); St); (c_1, c_2) \leftarrow \bar{\mathbf{A}}_n(q, g, A', B, X)$
 If $\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 0 \vee \text{CH} \neq B^{c_1} X^{c_2}$ then return \perp EndIf
 $b' \xleftarrow{\$} \mathbb{Z}_q; B' \leftarrow g^{b'}; X' \leftarrow B'^{a'}$
 If $B = B'$ then $\bar{a} \leftarrow b' - b \pmod q;$ return \bar{a} EndIf
 $((\text{RSP}', C', \text{CH}'), St'_1) \leftarrow \widehat{P}((B', X'); St); (c'_1, c'_2) \leftarrow \bar{\mathbf{A}}_n(q, g, A', B', X')$
 If $\text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 0 \vee \text{CH}' \neq B'^{c'_1} X'^{c'_2}$ then return \perp EndIf
 If $c_1 + a'c_2 \not\equiv 0 \pmod q$ then
 $\bar{a} \leftarrow (b'c'_1 + b'a'c'_2 - bc_1 - ba'c_2) \cdot (c_1 + a'c_2)^{-1} \pmod q;$ return \bar{a}
 else return \perp EndIf

$\mathbf{J}_n(q, g, A) \quad // n \notin K$
 Return \perp

Figure 4: Adversary $\mathbf{A} = \{\mathbf{A}_n\}_{n \in \mathbb{N}}$ for the KEA3 problem and adversary $\mathbf{J} = \{\mathbf{J}_n\}_{n \in \mathbb{N}}$ for the DLP, for the proof of Theorem 5.3.

for every $n \in K$ such that $n \geq 4$, if (CMT, q, g, A') is \widehat{P} 's commitment on input ε when the initial state is (x, a, ε) , then for every $A \in G_q$

$$\Pr [g^{\bar{a}} = A] > \frac{1}{p(n)^2} - \frac{8}{2^n p(n)} - 2\nu(n).$$

Since K is infinite and, by our assumption about the output of \widehat{P} , q, g are such that $(q, g) \in GL_n$, this proves the claim.

Fix $n \in K$ such that $n \geq 4$. Let (CMT, q, g, A') be \widehat{P} 's commitment on input ε when the initial state is (x, a, ε) , and let $A \in G_q$. We first write some inequalities leading to the claim and then justify them:

$$\begin{aligned} & \Pr [g^{\bar{a}} = A] \\ & \geq \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1 \wedge \text{CH} = B^{c_1} X^{c_2} \wedge B \neq B' \wedge \right. \\ & \quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 1 \wedge \text{CH}' = B'^{c'_1} X'^{c'_2} \wedge c_1 + a'c_2 \not\equiv 0 \pmod q \right] \end{aligned} \quad (5)$$

$$\begin{aligned} & \geq \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1 \wedge \text{CH} = B^{c_1} X^{c_2} \wedge \text{CH} \neq 1 \wedge B \neq B' \wedge \right. \\ & \quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 1 \wedge \text{CH}' = B'^{c'_1} X'^{c'_2} \wedge \text{CH}' \neq 1 \right] \end{aligned} \quad (6)$$

$$\begin{aligned} &\geq \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1 \wedge \text{CH} = C^{\text{DLog}_{q,g}(B)} \wedge \text{CH} \neq 1 \wedge B \neq B' \wedge \right. \\ &\quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 1 \wedge \text{CH}' = C'^{\text{DLog}_{q,g}(B')} \wedge \text{CH}' \neq 1 \right] - \\ &\quad \left(\Pr \left[\text{CH} \neq B^{c_1} X^{c_2} \wedge \text{CH} = C^{\text{DLog}_{q,g}(B)} \right] + \Pr \left[\text{CH}' \neq B'^{c'_1} X'^{c'_2} \wedge \text{CH}' = C'^{\text{DLog}_{q,g}(B')} \right] \right) \end{aligned} \quad (7)$$

$$\geq \left(\text{Acc}_{V',a}^{\hat{P}}(x) \right)^2 - \frac{1}{q-1} \text{Acc}_{V',a}^{\hat{P}}(x) - 2 \text{Adv}_{\mathbf{A},\bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A') \quad (8)$$

$$> \frac{1}{p(n)^2} - \frac{1}{(q-1)p(n)} - 2\nu(n) \quad (9)$$

$$\geq \frac{1}{p(n)^2} - \frac{8}{2^n p(n)} - 2\nu(n). \quad (10)$$

We justify Equation (5) by showing that if $\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1$, $\text{CH} = B^{c_1} X^{c_2}$, $B \neq B'$, $\text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 1$, $\text{CH}' = B'^{c'_1} X'^{c'_2}$ and $c_1 + a'c_2 \not\equiv 0 \pmod{q}$ then $g^{\bar{a}} = A$. Assume that the former statement holds. By the strong soundness property of protocol (\bar{P}, \bar{V}) , $\text{CH} = \text{CH}'$, whence $B^{c_1} X^{c_2} = B'^{c'_1} X'^{c'_2}$. Thus we have

$$\begin{aligned} g^{\bar{a}} &= g^{(b'c'_1 + b'a'c'_2 - bc_1 - ba'c_2) \cdot (c_1 + a'c_2)^{-1} \pmod{q}} = \left(g^{b'c'_1 + b'a'c'_2} \right)^{(c_1 + a'c_2)^{-1}} g^{-b} \\ &= \left(B'^{c'_1} X'^{c'_2} \right)^{(c_1 + a'c_2)^{-1}} g^{-b} = \left(B^{c_1} X^{c_2} \right)^{(c_1 + a'c_2)^{-1}} g^{-b} \\ &= \left(B^{c_1} B^{a'c_2} \right)^{(c_1 + a'c_2)^{-1}} g^{-b} = \left(B^{c_1 + a'c_2} \right)^{(c_1 + a'c_2)^{-1}} g^{-b} \\ &= B g^{-b} = A, \end{aligned}$$

as desired.

To justify Equation (6) we observe that if $\text{CH} = B^{c_1} X^{c_2}$ and $\text{CH} \neq 1$ then $c_1 + a'c_2 \not\equiv 0 \pmod{q}$, and that adding the condition $\text{CH}' \neq 1$ can only decrease the probability further.

Equation (7) is justified as follows.

$$\begin{aligned} &\Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 0 \vee \text{CH} \neq B^{c_1} X^{c_2} \vee \text{CH} = 1 \vee B = B' \vee \right. \\ &\quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 0 \vee \text{CH}' \neq B'^{c'_1} X'^{c'_2} \vee \text{CH}' = 1 \right] \\ &\leq \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 0 \vee \text{CH} \neq C^{\text{DLog}_{q,g}(B)} \vee \text{CH} = 1 \vee B = B' \vee \right. \\ &\quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 0 \vee \text{CH}' \neq C'^{\text{DLog}_{q,g}(B')} \vee \text{CH}' = 1 \vee \right. \\ &\quad \left. \left(\text{CH} \neq B^{c_1} X^{c_2} \wedge \text{CH} = C^{\text{DLog}_{q,g}(B)} \right) \vee \left(\text{CH}' \neq B'^{c'_1} X'^{c'_2} \wedge \text{CH}' = C'^{\text{DLog}_{q,g}(B')} \right) \right] \\ &\leq \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 0 \vee \text{CH} \neq C^{\text{DLog}_{q,g}(B)} \vee \text{CH} = 1 \vee B = B' \vee \right. \\ &\quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 0 \vee \text{CH}' \neq C'^{\text{DLog}_{q,g}(B')} \vee \text{CH}' = 1 \right] + \\ &\quad \Pr \left[\text{CH} \neq B^{c_1} X^{c_2} \wedge \text{CH} = C^{\text{DLog}_{q,g}(B)} \right] + \Pr \left[\text{CH}' \neq B'^{c'_1} X'^{c'_2} \wedge \text{CH}' = C'^{\text{DLog}_{q,g}(B')} \right]. \end{aligned}$$

$\text{Exp}_{\mathbf{A},\bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A')$ returns 1 exactly when $Y = C^{\text{DLog}_{q,g}(B)}$ and $g^{c_1} A'^{c_2} \neq C$. By construction of \mathbf{A} , we have $Y = \text{CH}$, and thus $\text{CH} = C^{\text{DLog}_{q,g}(B)} \wedge \text{CH} \neq B^{c_1} X^{c_2}$ implies that $\text{Exp}_{\mathbf{A},\bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A')$ returns 1. Similarly, $\text{CH}' = C'^{\text{DLog}_{q,g}(B')} \wedge \text{CH}' \neq B'^{c'_1} X'^{c'_2}$ implies that $\text{Exp}_{\mathbf{A},\bar{\mathbf{A}}}^{\text{kea3}}(n, q, g, A')$ returns 1.

To justify Equation (8) it remains to show that

$$\begin{aligned}
& \Pr \left[\text{DEC}_x((\text{CMT}, q, g), \text{CH}, \text{RSP}) = 1 \wedge \text{CH} = C^{\text{DLog}_{q,g}(B)} \wedge \text{CH} \neq 1 \wedge B \neq B' \wedge \right. \\
& \quad \left. \text{DEC}_x((\text{CMT}, q, g), \text{CH}', \text{RSP}') = 1 \wedge \text{CH}' = C^{\text{DLog}_{q,g}(B')} \wedge \text{CH}' \neq 1 \right] \\
& \geq \left(\mathbf{Acc}_{V'}^{\hat{P},a}(x) \right)^2 - \frac{1}{q-1} \mathbf{Acc}_{V'}^{\hat{P},a}(x). \tag{11}
\end{aligned}$$

Let RES denote the event in the experiment of executing $\mathbf{J}_n(q, g, A)$ whose probability is bounded from below in Equation (11). Note that the corresponding sample space is $\mathbb{Z}_q^* \times \mathbb{Z}_q^*$. Let ACC denote the event that in an interaction between \hat{P} (with initial state (x, a, ε)) and V' (with input x), the latter accepts (i.e., $\Pr[\text{ACC}] = \mathbf{Acc}_{V'}^{\hat{P},a}(x)$). The sample space of the corresponding experiment is \mathbb{Z}_q^* . We observe that if $b \in \text{ACC}$, $b' \in \text{ACC}$ and $b \neq b'$ then $(b, b') \in \text{RES}$. Therefore,

$$\begin{aligned}
|\text{RES}| & \geq |\text{ACC}|(|\text{ACC}| - 1) \quad \text{and} \\
\Pr[\text{RES}] & = \frac{|\text{RES}|}{|\mathbb{Z}_q^* \times \mathbb{Z}_q^*|} \geq \frac{|\text{ACC}|}{|\mathbb{Z}_q^*|} \left(\frac{|\text{ACC}|}{|\mathbb{Z}_q^*|} - \frac{1}{|\mathbb{Z}_q^*|} \right) = \left(\mathbf{Acc}_{V'}^{\hat{P},a}(x) \right)^2 - \frac{1}{q-1} \mathbf{Acc}_{V'}^{\hat{P},a}(x).
\end{aligned}$$

Equation (9) is justified by Equation (4) and the assumption that $\bar{\mathbf{A}}$ is an extractor for \mathbf{A} with error bound ν .

The assumption that $(q, g) \in \text{GL}_n$ implies that $|2q + 1| = n$, i.e., $2^{n-1} \leq 2q + 1 < 2^n$, and hence $q - 1 \geq 2^{n-3}$ (recall that $n \geq 4$). This justifies Equation (10). ■

Claim 5.4 implies that \mathbf{J} does not have a negligible success bound, which, by Proposition 2.2, shows that the DLP is not hard, contradicting the assumption made in this Theorem.

ZERO KNOWLEDGE OF PHTP. Having modified HTP, we need to revisit the zero-knowledge. Hada and Tanaka proved that if the canonical argument is HVZK (property (3) above) then HTP is zero-knowledge under KEA1. However, we observe that pHTP modifies only the verifier, not the prover. Furthermore, only the decision predicate of the verifier is modified, not the messages it sends. This means that the view (i.e., the internal coin tosses and the sequence of messages received during an interaction with a prover P) of verifier V' of pHTP is identical to that of verifier V of HTP. Thus, zero-knowledge of pHTP follows from zero-knowledge of HTP, and in particular is true under the same assumptions, namely KEA1.

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A Proof of Proposition 2.2

Let $K = \{n \in \mathbb{N} : GL_n \neq \emptyset\}$. For each $n \in K$ we let $(q_n, g_n) \in GL_n$ be such that

$$\forall (q, g) \in GL_n : \mathbf{Adv}_{\mathbf{J}}^{\text{dl}}(n, q, g) \leq \mathbf{Adv}_{\mathbf{J}}^{\text{dl}}(n, q_n, g_n). \quad (12)$$

For $n \in K$, let $R(n)$ denote the set from which \mathbf{J}_n draws its coins on inputs n, q_n, g_n . We say that $r \in R(n)$ is *n-good* if

$$\Pr \left[A \stackrel{\$}{\leftarrow} G_{q_n} ; \bar{a} \leftarrow \mathbf{J}_n(q_n, g_n, A; r) : g^{\bar{a}} = A \right] \geq \mathbf{Adv}_{\mathbf{J}}^{\text{dl}}(n, q_n, g_n).$$

Claim A.1 For each $n \in K$ there exists a $r \in R(n)$ such that r is *n-good*.

Proof: Define $X: G_{q_n} \times \mathbb{Z}_{q_n} \rightarrow \{0, 1\}$ as follows:

$$\begin{aligned} X(A, r) \\ \bar{a} \leftarrow \mathbf{J}_n(q_n, g_n, A; r) \\ \text{If } g^{\bar{a}} = A \text{ then return 1 else return 0} \end{aligned}$$

Then we have:

$$\sum_{r \in R(n)} \frac{1}{|R(n)|} \cdot \Pr \left[A \stackrel{\$}{\leftarrow} G_{q_n} ; \bar{a} \leftarrow \mathbf{J}_n(q_n, g_n, A; r) : g^{\bar{a}} = A \right]$$

$$\begin{aligned}
&= \sum_{r \in R(n)} \frac{1}{|R(n)|} \sum_{A \in G_{q_n}} \frac{1}{q_n} \cdot X(A, r) \\
&= \sum_{A \in G_{q_n}} \frac{1}{q_n} \sum_{r \in R(n)} \frac{1}{|R(n)|} \cdot X(A, r) \\
&= \mathbf{Adv}_{\mathbf{J}_n}^{\text{dl}}(n, q_n, g_n) .
\end{aligned}$$

This means that there must exist a $r \in R(n)$ such that

$$\Pr \left[A \stackrel{s}{\leftarrow} G_{q_n} ; \bar{a} \leftarrow \mathbf{J}_n(q_n, g_n, A; r) : g^{\bar{a}} = A \right] \geq \mathbf{Adv}_{\mathbf{J}_n}^{\text{dl}}(n, q_n, g_n) ,$$

which proves the claim. \blacksquare

We now define a poly-size family $\mathbf{I} = \{\mathbf{I}_n\}_{n \in \mathbb{N}}$ of (deterministic) circuits, as follows. Let $n \in \mathbb{N}$. If $n \notin K$ then we define \mathbf{I}_n arbitrarily. If $n \in K$ then Claim A.1 tells us that there exists a string, which we denote by r_n , that is n -good. We then define \mathbf{I}_n as follows:

```

In(q, g, A)
  If q ≠ qn or g ≠ gn then abort
   $\bar{a} \leftarrow \mathbf{J}_n(q_n, g_n, A; r_n)$ 
  Return  $\bar{a}$ 

```

Since \mathbf{I} is a poly-size family of deterministic circuits, the assumption that the DLP is hard says that there is a negligible function ν such that \mathbf{I} has success bound ν . Now putting this together with Equation (12) and Claim A.1 we have

$$\forall n \in K \ \forall (q, g) \in GL_n : \mathbf{Adv}_{\mathbf{J}}^{\text{dl}}(n, q, g) \leq \mathbf{Adv}_{\mathbf{J}}^{\text{dl}}(n, q_n, g_n) \leq \mathbf{Adv}_{\mathbf{I}}^{\text{dl}}(n, q_n, g_n) \leq \nu(n) .$$

This means that \mathbf{J} also has success bound ν , which proves the Proposition.