

# Badger - A Fast and Provably Secure MAC

Martin Boesgaard, Thomas Christensen, and Erik Zenner

CRYPTICO A/S  
Fruebjergvej 3  
2100 Copenhagen  
Denmark  
info@cryptico.com

**Abstract.** We present Badger, a new fast and provably secure MAC based on universal hashing. In the construction, a modified tree hash that is more efficient than standard tree hash is used and its security is being proven. Furthermore, in order to derive the core hash function of the tree, we use a novel technique for reducing  $\Delta$ -universal function families to universal families. The resulting MAC is very efficient on standard platforms both for short and long messages. As an example, for a 64-bit tag, it achieves performances up to 2.2 and 1.2 clock cycles per byte on a Pentium III and Pentium 4 processor, respectively. The forgery probability is at most  $2^{-52.2}$ .

*Keywords:* MAC, universal hash, tree, pseudo-random generator

## 1 Introduction

A Message Authentication Code (MAC) provides a way to detect whether a message has been tampered with during transmission. The usual model for authentication includes three participants: a transmitter, a receiver and an opponent. The transmitter sends a message over an insecure channel, where the opponent can introduce new messages as well as alter existing ones. Insertion of a new message by the opponent is called impersonation, and modification of an existing message by the opponent is called substitution. In both cases the opponent's goal is to deceive the receiver into believing that the new message is authentic.

In many applications, it is of significant importance that the receiver can verify the integrity of a message. In some cases this is even more important than encryption [10]. Often encryption and authentication are both required. With the emergence of fast software-based encryption algorithms like Rijndael [6], SNOW [8], Rabbit [4] etc., the need for fast software-based message authentication codes is increasing. Some attempts have been made to construct integrated MAC and encryption algorithms, e.g. Helix [11]. However, such approaches make it hard to prove the security of the MAC part. In contrast, MACs that can be proven secure with respect to an underlying cryptographic primitive exist. Prominent examples are HMAC [14] and the universal hashing approach [5].

The construction presented here is based on the universal hashing paradigm introduced by Carter and Wegman [5, 21]. They proposed to hash a given message with a randomly chosen function from a strongly universal family of hash functions, whereafter the output is encrypted with a one-time-pad (OTP) in order to obtain the MAC tag. Since universal hash functions are only required to fulfill, in a cryptographical sense, a rather simple combinatorial property, they can usually be constructed to be very fast. Recent research has been successful in achieving high speed for long messages. Notable examples can be found in [17, 2, 9, 15, 12]. However, for short messages, these algorithms lose some of their efficiency due to initialization and finalization overhead.

It is the aim of this paper to construct a Wegman-Carter based MAC which is fast on both short and long messages. The performance on short messages is important, as e.g. the MAC function used in IPsec operates on 43-1500 bytes (see chapter 3 of [15]) and the MAC function used in TLS operates on 0-17 kilobytes. In addition, the setup procedure must be simple and fast, as the number of messages and amount of data processed per setup is small in many applications, e.g. TLS. Finally, the MAC should provide verifiable-selectable assurance<sup>1</sup>.

In order to achieve high performance we introduce new families of universal hash functions especially well suited for tree-like hashing. These are obtained using a novel technique for reducing  $\Delta$ -universal hash families to universal hash families. This results in significant performance gains for small compressions. Furthermore, we develop an effective tree-like hashing procedure which basically consists of combining a tree hash with a linear hash. The construction is provably secure (relative to a cryptographic primitive) with simple proofs.

*Organization:* The paper is organized as follows. In section 2 we present the definitions of the different classes of universal hash families, we review composition theorems and sketch our construction. In section 3 we introduce a simple method to reduce delta-universal hash families to universal hash families. A modification of the standard tree hashing scheme is presented in section 4. Section 5 discusses how to build a strongly universal hash family from this scheme. Section 6 contains the specification of Badger, and performance results are presented in section 7. We conclude in section 8.

## 2 Universal Hashing and Message Authentication

In 1981, Wegman and Carter [21] showed that randomly chosen elements from a strongly universal hash function family can be used to compress a given message and encrypt the output using a OTP<sup>2</sup>. We describe briefly in the following why this is possible, and how it will be used in our design.

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<sup>1</sup> For a more detailed description of verifiable-selectable assurance, see [15]. In short, this means that the receiver can choose to verify to lower assurance levels than for the full tag in order to increase performance.

<sup>2</sup> Of course, a cryptographic pseudo-random generator (PRG) can also be used to generate a pseudo-random pad, but then the security depends on the security of the PRG.

*Universal hash function families:* The following definitions of universal hash function families are well-known from the literature.

**Definition 1.** [5, 18] An  $\epsilon$ -almost universal ( $\epsilon$ -AU) family  $H$  of hash functions maps from a set  $A$  to a set  $B$ , such that for any distinct elements  $a, a' \in A$ :

$$\Pr_{h \in H} [h(a) = h(a')] \leq \epsilon \quad (1)$$

$H$  is universal ( $U$ ) if  $\epsilon = 1/|B|$ .

**Definition 2.** [13, 20] Let  $(B, +)$  be an Abelian group. A family  $H$  of hash functions that maps from a set  $A$  to the set  $B$  is said to be  $\epsilon$ -almost  $\Delta$ -universal ( $\epsilon$ -A $\Delta$ U) w.r.t.  $(B, +)$ , if for any distinct elements  $a, a' \in A$  and for all  $\delta \in B$ :

$$\Pr_{h \in H} [h(a) - h(a') = \delta] \leq \epsilon \quad (2)$$

$H$  is  $\Delta$ -universal ( $\Delta$ U) if  $\epsilon = 1/|B|$ .

**Definition 3.** [21, 18] An  $\epsilon$ -almost strongly-universal ( $\epsilon$ -ASU) family  $H$  of hash functions maps from a set  $A$  to a set  $B$ , such that for any distinct elements  $a, a' \in A$  and all  $b, b' \in B$ :

$$\Pr_{h \in H} [h(a) = b] = 1/|B| \quad \text{and} \quad (3)$$

$$\Pr_{h \in H} [h(a) = b, h(a') = b'] \leq \epsilon/|B| \quad (4)$$

$H$  is strongly universal ( $SU$ ) if  $\epsilon = 1/|B|$ .

*The Wegman-Carter MAC:* From the definitions it follows that strongly universal hashing can be used for message authentication. If we denote the probability for an impersonation attack to succeed by  $P_i$  and the probability for a substitution attack to succeed by  $P_s$ , we have the following theorem:

**Theorem 1.** [21, 19, 16] There exists an  $\epsilon$ -ASU family of hash functions from  $A$  to  $B$  if and only if there exists an authentication code with  $|A|$  messages,  $|B|$  authenticators and  $k = |H|$  keys, such that  $P_i = 1/|B|$  and  $P_s \leq \epsilon$ .

The particular Wegman-Carter MAC can be defined as follows:

**Definition 4.** Given an  $\epsilon$ -ASU family  $\mathcal{H}$  of hash functions mapping from a set  $A$  to a set  $B$ , a nonce  $n$ , and an OTP  $r(n)$ , then the Wegman-Carter MAC is

$$\text{MAC}_{\text{WC}}(M; h, r(n)) = h(M) \oplus r(n), \quad (5)$$

where  $h$  is a random hash function from  $\mathcal{H}$  and  $M$  is the message.

A new nonce must be used for each application of the MAC to ensure the unconditional security of the construction.

*Composition rules:* Hash families can be combined in order to obtain new hash families. The below composition rules (see [19]) describe what happens to the resulting  $\epsilon$ , domains, and ranges.

**Composition 1** *If there exists an  $\epsilon_1$ -AU family  $H_1$  of hash functions from  $A$  to  $B$  and an  $\epsilon_2$ -AU family  $H_2$  of hash functions from  $B$  to  $C$ , then there exists an  $\epsilon$ -AU family  $H$  of hash functions from  $A$  to  $C$ , where  $H = H_1 \times H_2$ ,  $|H| = |H_1| \cdot |H_2|$ , and  $\epsilon = \epsilon_1 + \epsilon_2 - \epsilon_1\epsilon_2$ .*

**Composition 2** *If there exists an  $\epsilon_1$ -AU family  $H_1$  of hash functions from  $A$  to  $B$  and an  $\epsilon_2$ -ASU family  $H_2$  of hash functions from  $B$  to  $C$ , then there exists an  $\epsilon$ -ASU family  $H$  of hash functions from  $A$  to  $C$ , where  $H = H_1 \times H_2$ ,  $|H| = |H_1| \cdot |H_2|$ , and  $\epsilon = \epsilon_1 + \epsilon_2 - \epsilon_1\epsilon_2$ .*

*Our construction:* In the following, we will use composition rule 2 to construct a Wegmann-Carter MAC. First, we will use an  $\epsilon_{H^*}$ -AU universal function family  $H^*$  to hash messages of all sizes onto a fixed size. Subsequently, we will use an  $\epsilon_F$ -ASU function family  $F$  to guarantee for the strong universality of the overall construction. Thus, the strongly universal hash family used for our MAC can be described as  $\mathcal{H} = H^* \times F$ . Note that the following theorem follows immediately from composition rule 2:

**Theorem 2.** *The hash function family  $\mathcal{H} = H^* \times F$  is  $\epsilon_F + (1 - \epsilon_F)\epsilon_{H^*}$ -ASU.*

We proceed by describing  $H^*$  in sections 3 and 4 and  $F$  in section 5.

### 3 Reducing $A\Delta U$ Families to AU Families

*Reducing function families:* Note that for the classes of hash function families defined in definitions 1-3, the latter are contained in the former, i.e. an  $A\Delta U$  family is also an AU family a.s.o. On the other hand, a stronger family can be reduced to a weaker one. This is, of course, only relevant when a performance gain can be achieved. In the following, we will describe a method to reduce delta-universal hash functions to universal hash functions. It turns out that these new universal hash families are particularly well-suited for tree structures.

**Theorem 3.** *Let  $H^\Delta$  be an  $\epsilon$ - $A\Delta U$  hash family from a set  $A$  to a set  $B$ . Consider a message  $(m, m_b) \in A \times B$ . Then the family  $H$  consisting of the functions  $h(m, m_b) = h^\Delta(m) + m_b$  is  $\epsilon$ -AU for equal length messages.*

*Proof.* From the definitions above we have

$$\begin{aligned} \Pr_{h \in H} [h(m, m_b) - h(m', m'_b) = 0] &= \Pr_{h^\Delta \in H^\Delta} [h^\Delta(m) + m_b - h^\Delta(m') - m'_b = 0] \\ &= \Pr_{h^\Delta \in H^\Delta} [h^\Delta(m) - h^\Delta(m') = m'_b - m_b]. \end{aligned}$$

If  $m \neq m'$ , then this probability is at most  $\epsilon$ , since  $H^\Delta$  is an  $\epsilon$ - $A\Delta U$  family. If  $m = m'$  but  $m_b \neq m'_b$ , then the probability is trivially 0.  $\square$

Note that this construction is most useful when the domain  $A$  is not much larger than the range,  $B$ , but useless when  $|A| \gg |B|$ . Thus, if relatively short messages are hashed for each key, the extra block results in a significant performance gain.

*Constructing the ENH family:* A very fast universal hash family is the NH family used in UMAC [15]:

$$\text{NH}_K(M) = \sum_{i=1}^{l/2} (k_{2i-1} +_w m_{2i-1}) \cdot (k_{2i} +_w m_{2i}) \bmod 2^{2w}, \quad (6)$$

where  $'+_w'$  means 'addition modulo  $2^w$ ', and  $m_i, k_i \in \{0, \dots, 2^w - 1\}$ . It is a  $2^{-w}$ -A $\Delta$ U hash family. In [15], the A $\Delta$ U property is mentioned, but only the AU property is explicitly proven.

**Lemma 1.** *The following version of NH:*

$$\text{NH}_K(M) = (k_1 +_w m_1) \cdot (k_2 +_w m_2) \bmod 2^{2w}, \quad (7)$$

is  $2^{-w}$ -A $\Delta$ U for equal length messages.

*Proof.* This proof is just a slight modification of the one presented in [15]. We must show that

$$\Pr_{k_1, k_2} [(k_1 +_w m_1)(k_2 +_w m_2) - (k_1 +_w m'_1)(k_2 +_w m'_2) = \delta] \leq 2^{-w}.$$

where all arithmetic is carried out modulo  $2^{2w}$ . Assume that  $m_2 \neq m'_2$ . Define  $c = k_2 + m_2$  and  $c' = k_2 + m'_2$ . By assumption it follows that  $c \neq c'$ . So we have

$$\Pr_{k_1, k_2} [(k_1 +_w m_1)c - (k_1 +_w m'_1)c' - \delta = 0] \leq 2^{-w}.$$

since from lemma 2.4.3 in [15], the equality will only be satisfied by one  $k_1$ .  $\square$

Choosing  $w = 32$  and applying theorem 3, we obtain the  $2^{-32}$ -AU function family ENH, which will be the basic building block of our MAC:

$$\begin{aligned} \text{ENH}_{k_1, k_2}(m_1, m_2, m_3, m_4) \\ = (m_1 +_{32} k_1)(m_2 +_{32} k_2) +_{64} m_3 +_{64} 2^{32} m_4, \end{aligned} \quad (8)$$

where all arguments are 32-bit and the output is 64-bit.

## 4 The Modified Tree Construction

*The standard tree construction:* The ENH function family maps 128-bit inputs to 64-bit outputs. An immediate use of such a function is in a tree-like structure that allows hashing of messages of arbitrary length. More generally, assume a block length  $b$ , a universal hash family  $H$  that maps from  $bc$  to  $b$  bits, and a message of length  $|M| = b \cdot c^n$ , for some suitable value  $n$ . Then a hash tree can be defined by a succession of  $n$  parallel hashes, as follows [5, 1]:

**Definition 5.** Let  $H$  be a universal hash family, taking  $bc$  bits to  $b$  bits. Given a message  $M = m_1 || \dots || m_{c^n}$  with length  $|M| = bc^n$ , we hash  $c$  blocks at a time with a function  $h \in H$  and concatenate the results. The result is a string of length  $bc^{n-1}$ . We denote the hash family by  $H^{\text{par}}$  and a member by  $h^{\text{par}}$ .

$$h^{\text{par}}(M) = h(m_1, \dots, m_c) || \dots || h(m_{c^{n-c+1}}, \dots, m_{c^n}) \quad (9)$$

It is easy to see that if  $H$  has a collision bound of  $\epsilon$  then so does the parallel hash,  $H^{\text{par}}$ . We define the standard tree construction as follows:

**Definition 6.** Let  $M$  and  $H$  be as in definition 5. We define a new hash family by applying  $h_i^{\text{par}}$   $n$  times, each time with a new random  $h_i \in H$ . We denote the resulting function family by  $H_n^{\text{tree}}$  and a member by  $h_n^{\text{tree}}$ :

$$h_n^{\text{tree}}(M) = h_n^{\text{par}} \circ h_{n-1}^{\text{par}} \circ \dots \circ h_1^{\text{par}}(M).$$

**Theorem 4.** The function family  $H_n^{\text{tree}}$  is a  $1 - (1 - \epsilon)^n$ -universal family of hash functions for equal length messages.

*Proof.* Let us define  $\epsilon_i$  as the collision bound for  $H_i^{\text{tree}}$ , then we have for  $H_{i+1}^{\text{tree}}$ :

$$\Pr[h_{i+1}^{\text{par}}(h_i^{\text{tree}}(m)) - h_{i+1}^{\text{par}}(h_i^{\text{tree}}(m')) = 0] \leq \epsilon_i(1 - \epsilon) + \epsilon.$$

Solving the recurrence we get:

$$\begin{aligned} \Pr[h_n^{\text{par}}(h_{n-1}^{\text{tree}}(m)) - h_n^{\text{par}}(h_{n-1}^{\text{tree}}(m')) = 0] \\ \leq (1 - \epsilon)^{n-1} \epsilon + \epsilon \sum_{i=1}^{n-2} (1 - \epsilon)^i + \epsilon \\ = 1 - (1 - \epsilon)^n \end{aligned}$$

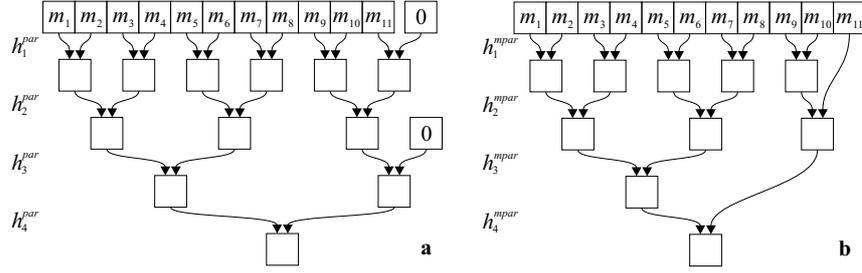
□

*The modified tree construction:* Consider, as an example, the case  $c = 2$ , yielding a binary tree. Then the message length must be  $b \cdot 2^n$ , for some suitable  $n$ . If that is not the case, Wegman and Carter propose [21] to break the message into substrings of length  $2b$  and if necessary pad the last substring with zeroes. The resulting string is hashed with the parallel hash. If necessary, the resulting string is again padded with zeroes. This is repeated until the resulting string has length  $b$ . This procedure is illustrated in fig. 1a.

Note that this algorithm is not always optimal, because for message lengths not equal to a power of two, extra applications of the universal hash function are required. Of course, this is only significant for short messages. We start constructing a modified tree hash by defining a modified parallel hash, as follows:

**Definition 7.** Given a universal hash family,  $H$ , whose members  $h$  take  $bc$  bits to  $b$  bits, consider the message  $M = m_1 || \dots || m_q$  where  $|M| = bq$ . Let  $r = q \bmod c$ , then the modified parallel hash can be defined as:

$$h^{\text{mpar}}(M) = \begin{cases} h(m_1, \dots, m_c) || \dots || h(m_{q-c+1}, \dots, m_q) & \text{if } r = 0 \\ h(m_1, \dots, m_c) || \dots || h(m_{q-c-r+1}, \dots, m_{q-r}) || m_{q-r+1} || \dots || m_q & \text{if } r \neq 0 \end{cases} \quad (10)$$



**Fig. 1.** Figure (a) illustrates the standard tree construction using the parallel hash and figure (b) illustrates the modified tree construction using the modified parallel hash.

**Lemma 2.** *The modified parallel hash is  $\epsilon$ -AU on equal length messages.*

*Proof.* In the first case, where  $q$  is a multiple of  $c$  we simply have a parallel hash and the bound on the collision probability is  $\epsilon$ . In the case where  $q$  is not a multiple of  $c$ , there are two possible situations. Either the difference in the messages  $M$  and  $M'$  is in the part, which is processed by  $h$ , or in the part which is not processed but simply concatenated to the result. In the first situation the bound on the collision probability is  $\epsilon$ . In the second situation the collision probability is trivially zero. Thus, the collision probability is at most  $\epsilon$ .  $\square$

It is straightforward to define a modified tree hash, i.e. define it as in Definition 6 but use the modified parallel hash instead of the usual parallel hash.

**Corollary 1.** *Given a message with length  $|M| = bq$ , where  $c^{n-1} < q \leq c^n$ , the modified tree hash defines a  $1 - (1 - \epsilon)^n$ -AU family of hash functions on equal length messages.*

*Proof.* This follows from theorem 4, when the usual parallel hash is replaced by the modified parallel hash, since both are  $\epsilon$ -AU, and the number of levels is the same in both cases<sup>3</sup>.  $\square$

*The binary case:* Again, consider  $c = 2$ . The message is divided into blocks of size  $b$ . If the message length is not a multiple of  $b$ , zeros are appended to the message such that the length becomes a multiple of  $b$ . If the length hereafter is a multiple of  $2b$ , the hash function is applied to each block and the results are concatenated. If the length is an odd multiple of  $b$ , the hash function is applied to each block except the last block. The results and the last block are concatenated. The procedure is repeated until the size of the result is  $b$ . The construction is illustrated in Fig. 1b.

<sup>3</sup> In a Wegman-Carter binary tree hash, a message consisting of an odd number of blocks is padded such that the number of blocks is even. This is done after each application of the parallel hash. The number of levels is equal to the number of levels for a message whose length is the nearest larger power of two. Now it is easy to convince oneself that the number of levels of the modified tree hash is exactly the same.

*A different view:* Note that the construction can be defined in an alternative way. Considering again the case  $c = 2$ , the message length can be described by  $|M| \equiv b \sum_{i=0}^n a_i 2^i$  with  $a_i \in \{0, 1\}$ . To each term  $a_i = 1$  in the sum there corresponds a tree with  $i$  levels. We order these trees according to size with the largest tree first. More precisely, we use the tree hash for each group of data corresponding to a term in the sum, concatenate the result, and linearly hash it backwards, i.e. take the  $b$ -bit block as output from the last tree and hash it with the result of the second to last tree and so on, until only one  $b$ -bit string is left. In other words, the construction consists of a series of concatenated tree hashes followed by a linear hash [1]. For the example in Fig. 1b, the message length can be written as:  $|M| = b(2^3 + 2^1 + 2^0)$ . There is one tree with 3 levels, one with 1 level and one with 0 levels. The hash results if the outputs of those trees are linearly hashed starting with the result from the smallest tree.

*The function family  $H^*$ :* The above construction is only AU for equal length messages. To ensure universality for different length messages, we simply concatenate the length of the given message in a fixed  $z$ -bit format [15, 1]:

**Definition 8.** Fix  $z > 0$  and let the message  $M$  (before padding) have any length less than  $2^z$ . Define  $L_z = |M|$  to be the  $z$ -bit representation of the length and define the family  $H^*$  by its members  $h^*$ , as follows:

$$h^*(M) = L_z || h(M). \quad (11)$$

We then have the following property:

**Lemma 3.** The hash function family  $H^*$  is  $1 - (1 - \epsilon)^n$ -AU.

*Proof.* In the case  $|M| \neq |M'|$ , the collision probability is trivially zero. In the case  $|M| = |M'|$ , the collision probability is defined according to corollary 1 by the number of levels necessary to compress the message.  $\square$

Note that for Badger, we will use  $H = \text{ENH}$ . This immediately yields a binary tree with a block size of 64 bit. The input size of the function family  $H^*$  is defined to be between 0 and  $2^{64} - 1$  bit. Consequently, the output size is 128 bit, 64 bit each for the hash and for the message length. Also note that the tree will contain between 1 and 58 levels, yielding a collision probability between  $\epsilon_{H^*} \leq 2^{-32}$  for small and  $\epsilon_{H^*} = 1 - (1 - 2^{-32})^{58} \leq 2^{-26.14}$  for large trees.

## 5 The function family $F$

*Strengthening a function family:* What is left now according to theorem 1 is to construct a suitable SU family  $F$ , such that the overall function family  $\mathcal{H} = H^* \times F$  is both efficient and secure. Note that without considering the details of  $H^*$ , the input size of  $F$  is  $b + z$  bit, and its output size should be equivalent to the security of the overall scheme.

In section 3, we reduced a strong class of universal hash functions to a weaker one. In order to construct the strongly universal function family  $F$ , we will do the opposite: In accordance with lemma 1 from [9], we will transform the  $\Delta$ -universal hash family,  $\text{MMH}^*$ , proposed by Halevi and Krawczyk [12], into a strongly universal hash family. This is accomplished by adding an additional key,  $k_{l+1}$ , in the following way:

$$\text{MMH}_K^{\text{su}}(M) = \sum_{i=1}^l m_i k_i + k_{l+1} \bmod p, \quad (12)$$

where  $p$  is a prime number,  $M = m_1 || \dots || m_n$ , and  $m_i, k_i \in \{0, \dots, p-1\}$ .

*Key material:* It is easily seen that for a given message  $M$ , the amount of key material,  $N_{\mathcal{H}}(M)$ , needed to choose a function from the family  $\mathcal{H}$  is defined by

$$N_{\mathcal{H}}(M) = N_H \lceil \log_c(|M|/b) \rceil + N_F, \quad (13)$$

where  $N_H$  is the amount of key material needed for the  $H$ -function in the tree and  $N_F$  is the amount of key material needed for the  $F$ -function. Note that the amount of key material required is the same as for the usual tree MAC.

*The choice for Badger:* Remember from section 4 that  $H^*$  produces a 128-bit output, which is also the minimum input size for  $F$ . Also remember that the collision probability for the  $H^*$ -function ranges from  $2^{-32}$  to  $2^{-26.14}$ , depending on the size of the tree. Since the overall security can not get better than that (according to theorem 2), an output size of 32 bit for the  $F$ -function is sufficient, since additional bits do not improve the security.

Consequently, we use a 32-bit version of the  $\text{MMH}^{\text{su}}$ -construction. We take  $p$  to be the largest 32-bit prime number, which is  $p = 2^{32} - 5$ . In order to process a 128-bit input, we have to choose  $n = 5$  and obtain:

$$F_K(M) = \sum_{i=1}^5 q_i k_i + k_6 \bmod (2^{32} - 5). \quad (14)$$

Note that the 128-bit output of  $H^*$  has to be divided into five input blocks  $q_i$  in some way. For Badger, it is padded with 7 leading zeroes and then split into 27-bit blocks<sup>4</sup>. The rationale for this design and an efficient way to implement it is given in section A in the appendix. In section B of the appendix, a mathematical simplification of the  $F$ -function is discussed, along with an explanation why it is not used for Badger.

<sup>4</sup> Note that the security claims for the SU function family also hold if not all messages from  $\{0, \dots, 2^{32}-6\}$  are actually used as inputs, as long as all keys from  $\{0, \dots, 2^{32}-6\}$  occur with equal probability.

## 6 The Badger Specification

*Key generation:* To generate the key material for the  $H^*$ - and  $F$ -functions, any secure PRG can be used, as long as the key length is at least 128 bit. Since the height of the tree is 58 for maximum size messages, we require 58 64-bit keys for the  $H^*$ -function. Furthermore, 6 keys from the interval  $\{0, \dots, 2^{32} - 6\}$  have to be generated for the  $F$ -function. Note that (as opposed to the key material for the pseudo-random pad), this key material can be computed once and then be re-used for the computation of all future MACs.

*Message processing:* Pseudo-code for Badger is presented in figure 2. To process the message, it is first divided into 64-bit blocks and padded with zeroes if necessary. The resulting bit string is then compressed into one 64-bit block, using the  $H^*$ -function. The length of the message in bit (before padding) is represented as a 64-bit number and concatenated to the 64-bit result. The resulting 128-bit block is prefixed with 7 zeroes, divided into five 27-bit blocks and run through the  $F$ -function.

The final tag is generated by xor-ing the output of the hash function with a pseudo-random pad, according to Definition 4. Note that no output of the PRG must ever be re-used; this can be achieved by running the PRG without resetting, or by using a new nonce for every new message.

*Forgery probability:* The forgery probability is  $\epsilon \leq \epsilon_F + (1 - \epsilon_F)\epsilon_{H^*}$ , according to theorem 2. Remember that depending on the message length, the upper bound on  $\epsilon_{H^*}$  can range from  $2^{-32}$  to  $2^{-26.14}$ . Also note that  $\epsilon_F = 1/(2^{32} - 5) \approx 2^{-32}$ . Using these values, it can be seen that the overall forgery probability has an upper bound ranging from  $2^{-31}$  for extremely short to  $2^{-26.12}$  for extremely long messages.

Forgery probabilities of up to  $2^{-26.12}$  are insufficient for most applications. However, a simple method to reduce the forgery probability is to hash the message  $u$  times with independent keys and concatenate the results. This results in a forgery probability of  $\epsilon^u$ . To obtain 128-bit security, we need to hash the message 5 times, yielding bounds on the forgery probability of between  $2^{-155}$  and  $2^{-130.6}$  and a tag size of 160 bits. In particular, this leads to the verifiable-selective assurance as each 32-bit tag can be verified independently.<sup>5</sup>

## 7 Performance

Performance of the Badger algorithm was measured on a 1.0 GHz Pentium III processor. The speed-optimized version was programmed in assembly language

<sup>5</sup> At first glance, it seems that the Toeplitz construction (proposed by Krawczyk in [13]) is applicable here, i.e. that the  $u$  parallel MACs are calculated using  $(k_1, \dots, k_{58}), (k_2, \dots, k_{59})$  etc. However, this only makes sense if the resulting forgery probability is at most  $\epsilon^u$ , and experiments with smaller versions of the  $H^*$ -function indicate that this is not the case here. Thus, the Toeplitz construction is not used with Badger.

```

Function  $h(k, m_1, m_2)$ 
1. return  $(m_1 +_{32} k) \cdot ((m_1 \gg 32) +_{32} (k \gg 32)) +_{64} m_2$ 

Function Keygen( $k$ )
1. generate  $k_1^F, \dots, k_6^F \in \{0, \dots, 2^{32} - 6\}$  from PRG( $k$ )
2. generate  $k_1, \dots, k_{58} \in \{0, \dots, 2^{32} - 1\}$  from PRG( $k$ )
3. return  $k_1^F, \dots, k_6^F, k_1, \dots, k_{58}$ 

Function Badger( $k, M, N$ )
1.  $L = |M|$ 
2. while  $|M| \bmod 64 \neq 0$  do:  $M = M||0$ 
3. for  $i = 1$  to  $i = \lceil \log_2(L/64) \rceil$  do:
4.   divide  $M$  into 64-bit blocks,  $M = m_1 || \dots || m_t$ 
5.   if  $t$  is even:
        $M = h(k_i, m_1, m_2) || \dots || h(k_i, m_{t-1}, m_t)$ 
     else:
        $M = h(k_i, m_1, m_2) || \dots || h(k_i, m_{t-2}, m_{t-1}) || m_t$ 
6. set  $Q = 0^7 || L || M$ 
7. divide  $Q$  into 27-bit blocks,  $Q = q_5 || \dots || q_1$ 
8.  $S = \sum_{i=1}^5 q_i k_i^F + k_6^F \bmod (2^{32} - 5)$ 
9. return  $S \oplus \text{PRG}(k, N)$ 

```

**Fig. 2.** Pseudo-code of the Badger algorithm

inlined in C and compiled using the Intel C++ 7.0 compiler. All performance results in this section are based on generating a 2·32 bit tag. The pseudo-random key material required for the algorithm was generated using the stream cipher Rabbit [4, 3], which is very fast in software.

Note that the pseudo-random pad can be generated either with or without an explicit IV-setup. If an explicit IV is used, the stream cipher has to be re-initialized for each message. Without an explicit IV, the key material for successive messages is produced by continuous extraction of bytes from the stream cipher, yielding a performance advantage. This corresponds to interpreting the message number as the IV. However, this technique is only applicable if messages are guaranteed to be received in the same order as generated, which is often not the case (e.g. in IPsec communication). Table 1 gives performance numbers both with and without explicit IV-setup.

Since the amount of key material required for Badger depends on the length of the message, optimized versions can be used in applications where the message length is upper bounded. For example, in typical IPsec applications, the message length cannot exceed 1500 bytes and when authenticating TLS protected data, each message cannot exceed 17 kilobytes [7]. Furthermore, the evaluation of the  $F$ -function is simplified since part of the input is zero, see eq. (12). The properties of Badger when the message length is limited are shown in Table 2.

**Table 1.** Performance results with and without IV-setup. “Key setup” generates all keys for the  $\epsilon$ -AU and SU hash functions, “Universal hash” processes the tree, and “Finalization” includes the  $F$ -function and generates the pseudo-random pad.

Function	IV-setup	Without IV-setup
Key setup	4093 cycles	4093 cycles
Universal hash	2.2 cycles/byte	2.2 cycles/byte
Finalization	433 cycles	175 cycles

**Table 2.** Badger properties for various restricted message lengths. “Memory req.” denotes the amount of memory required to store the internal state including key material, temporary results and a key instance for the Rabbit stream cipher. “Fin. no IV” denotes finalization without IV-setup and “Fin. IV” denotes finalization with IV-setup.

Max message size	Forgery pr.	Memory req.	Setup	Fin. no IV	Fin. IV
$2^{11}$ bytes (e.g. IPsec)	$2^{-57.7}$	420 bytes	1133 cycles	151 cycles	409 cycles
$2^{15}$ bytes (e.g. TLS)	$2^{-56.6}$	548 bytes	1370 cycles	163 cycles	421 cycles
$2^{32}$ bytes	$2^{-54.2}$	1092 bytes	2376 cycles	163 cycles	421 cycles
$2^{61} - 1$ bytes	$2^{-52.2}$	2020 bytes	4093 cycles	175 cycles	433 cycles

Note that the performance numbers for the key setup and finalization (which are dependent on the PRG in use) are partially based on estimates. The numbers for the universal hash function, however, are independent of the PRG and are fully based on measurements. Also note that Badger performs significantly better on Pentium 4 processors. Preliminary measurements indicate that the universal hash function requires only 1.2 clock cycles per byte, compared to 2.2 cycles for the Pentium III. Work on a full set of measurements is currently in progress.

## 8 Conclusion

We presented a new fast and provably secure MAC called Badger, based on universal hashing. In the construction, a modified tree hash was introduced that basically combines a tree hash with a linear hash. The modified tree hash is more efficient than the standard tree hash, and its security has been proven. Furthermore, in order to derive the core hash function of the tree, we introduced a novel technique for reducing  $\Delta$ -universal function families to universal families. The resulting MAC is very efficient on standard processors both for short and long messages. As an example, for a 64-bit tag, it achieves performances of up to 2.2 and 1.2 clock cycles per byte on a Pentium III and Pentium 4 processor, respectively. The key material necessary for the hash functions is only 976 bytes, and the forgery probability is at most  $2^{-52.2}$ .

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## A Efficient computation of the $F$ -function

*Computation of the modulus operation:* Remember from section 5 that the  $F$ -function is of the form

$$F_K(M) = \underbrace{\sum_{i=1}^5 q_i k_i + k_6}_{t} \bmod (2^{32} - 5),$$

where a 128-bit input has to be divided into five variables  $q_1, \dots, q_5$ . Assuming that additions of 64-bit variables and multiplications of 32-bit values can be implemented efficiently on standard processors, the major problem is the calculation of the modulus.

Also remember that the 128-bit output of  $H^*$  is padded with 7 leading zeroes and divided into 5 blocks  $q_i$  of length 27 bit. This has the effect of guaranteeing that  $t < (2^{64} - 1)/5$ . In particular, the result of the full addition can be calculated as a 64-bit value without any need to handle of carry bits. All that remains is to calculate  $t \bmod (2^{32} - 5)$ .

Here, we use the well-known fact that

$$(a \cdot 2^n + b) \bmod (2^n - k) = (a \cdot k + b) \bmod (2^n - k).$$

In the concrete case, if we write  $t = (a||b)$ , with  $a$  being the upper and  $b$  being the lower word, then the output can be computed as  $5a + b \bmod (2^{32} - 5)$ . Note that since  $t < (2^{64} - 1)/5$ , we have  $a < (2^{32} - 1)/5$  and thus  $5 \cdot a < 2^{32} - 1$ . Again, no carry occurs, this time for a 32-bit addition. In fact, the only carry that can occur is when adding the 32-bit words  $5a$  and  $b$ . If this happens, we subtract  $2^{32} - 5$  from the result, as described in figure 3.

Finally, we have to check whether the result is  $\geq 2^{32} - 5$ . If this is the case, again, we have to subtract  $2^{32} - 5$ . Close examination reveals that if an addition overflow occurs, then the value of  $r$  is in the range  $\{0, \dots, 0xC7FFFFE0\}$ , which means that the second condition can not be true. Thus, only one of the conditions can hold.

*Saving additions for short messages:* For Badger, the input was padded to 135 bits by prefixing zeroes. Thus, the total input to the  $F$ -function is of the form  $0^7||L||M$ , where  $L$  is the 64-bit representation of the message length, and  $M$  is the output of the  $H^*$ -function. Note that if the first 20 bits of  $L$  are zero (i.e.,

```

r = 5 * a;
r += b;

if (r < b)
    r -= 0xFFFFFFFFB;           // if overflow has occurred
else if (r >= 0xFFFFFFFFB)
    r -= 0xFFFFFFFFB;           // if result ≥ 232 - 5

```

**Fig. 3.** Computing modulus  $2^{32} - 5$  in the  $F$ -function

$L < 2^{44}$ ), then the term  $q_5k_5$  is zero and can be left out to increase performance. Likewise, if the first 47 bits of  $L$  are zero (i.e.,  $L < 2^{17}$  bit), then the term  $q_4k_4$  can be left out, too. This possible saving in computing time (both in key generation and in finalizing) is the reason for choosing 27-bit blocks  $q_i$  instead of 26-bit blocks, as would have been possible.

## B A mathematical simplification of the $F$ -function

*MAC from  $\Delta$ -universal families:* An obvious generalization of the Wegmann-Carter MAC is as follows:

**Definition 9.** Let  $(B, \boxplus)$  be an Abelian group, and  $\mathcal{H}$  be an  $\epsilon$ - $A\Delta U$  function family mapping from a set  $A$  to the set  $B$ . Using a nonce  $n$  and a random pad  $r(n)$ , the  $\Delta$ -MAC is defined as

$$MAC_{\Delta}(M; h, r(n)) = h(M) \boxplus r(n),$$

where  $h$  is a random hash function from  $\mathcal{H}$  and  $M$  is the message.

Then in a fashion similar to the proof on  $\epsilon$ -AXU function families [13], the following can be shown:

**Theorem 5.** The probability of an impersonation attack to succeed against the  $\Delta$ -MAC is  $1/|B|$ , and the probability of a substitution attack to succeed is at most  $\epsilon$ .

Also note that in a fashion similar to [19], the following composition rule can be proven:

**Composition 3** If there exists an  $\epsilon_1$ -AU family  $H_1$  of hash functions from  $A$  to  $B$  and an  $\epsilon_2$ - $A\Delta U$  family  $H_2$  of hash functions from  $B$  to  $C$ , then there exists an  $\epsilon$ - $A\Delta U$  family  $H$  of hash functions from  $A$  to  $C$ , where  $H = H_1 \times H_2$ ,  $|H| = |H_1| \cdot |H_2|$ , and  $\epsilon = \epsilon_1 + \epsilon_2 - \epsilon_1\epsilon_2$ .

Thus, the construction presented in the paper could be simplified by using an  $F$ -function from an  $\epsilon$ - $A\Delta U$  family, as long as its output is combined with the pseudo-random pad by using the group operation  $\boxplus$ .

*Possible simplification 1:* Remember that the family  $\text{MMH}^*$  (without the final key addition) is  $\Delta\text{U}$ . Thus, we could also use the following  $F$ -function for our construction:

$$F_K^\Delta(M) = \sum_{i=1}^5 q_i k_i \bmod (2^{32} - 5),$$

i.e. we could leave out the final key addition altogether. In this case, we would have to implement the pseudo-random pad over the group  $(\{0, \dots, 2^{32} - 6\}, \boxplus)$ , where  $\boxplus$  denotes the addition modulo  $2^{32} - 5$ . This, however, means that for all  $k_i$  and all pseudo-random words from the PRG, it has to be checked whether they are  $< 2^{32} - 5$ . This introduces more computational overhead than generating and adding one additional key  $k_6$ ; thus, we chose the solution proposed in section 5.

*Possible simplification 2:* Another possible use for theorem 5 is to use a function  $F$  that is  $\Delta$ -universal with regards to the group  $(\{0, 1\}^n, \oplus)$ . A possible solution would be to use a version of  $\text{MMH}^*$  that operates over  $\text{GF}(2^{32})$ . In fact, it can be shown that this version of  $\text{MMH}^*$  is  $\Delta\text{U}$ , too. If  $\oplus$  and  $\odot$  denote the addition and multiplication in  $\text{GF}(2^{32})$ , respectively, then we have

$$F_K^\oplus(M) = (q_1 \odot k_1) \oplus \dots \oplus (q_4 \odot k_4).$$

However, this involves four polynomial multiplications, which are much less efficient on standard processors than additions over the integers. Thus, we discarded this solution, too.