An efficient way to access an array at a secret index

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Abstract

We propose cryptographic primitives for reading and assigning the (shared) secret found at a secret index in a vector of secrets. The problem can also be solved in constant round with existing general techniques based on arithmetic circuits and the "equality test" in [5]. However the proposed technique requires to exchange less bits. The proposed primitives require a number of rounds that is independent of the size N of the vector, and only depends (linearly) on the number t of computing servers. A previously known primitive for reading a vector at a secret index works only for 2-party computations. Our primitives work for any number of computing participants/servers.

The proposed techniques are secure against passive attackers, and zero knowledge proofs are provided to show that exactly one index of the array is read/written. The techniques work both with multiparty computations based on secret sharing and with multiparty computations based on threshold homomorphic encryption.

1 Introduction

In many general multi-party computation (MPC) frameworks, secrets s from a ring F are distributed among participants using sharing schemes. In a sharing scheme, each participant A_i gets a share denoted $[s]_i^F$, and at least t participants are required to reconstruct the secret from their shares. Arithmetic circuits can then be evaluated securely over these shares [3, 14, 8, 7]. The proposed primitives also work with MPC schemes where secrets are encrypted with a homomorphic public key cypher E allowing additions of plaintext by operations on ciphertexts [4], and whose secret key is distributed among t servers/participants.

Cryptographic Primitives on Shared Secrets Examples of known primitives working on secret shares in a number of rounds that is independent on the possible values of the secrets are:

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- bits(x). Transform the shared secret x (with ℓ bits) into a vector $[x]^B$ of ℓ shared secrets, $[x]^B = b_0, b_1, ..., b_\ell$, with possible values $\{0,1\}$ and representing the corresponding bits of x [5].
- $EXP(x, [y]^B)$. This primitive computes raises x at exponent y where y is shared on bits [6].
- +, -, *, =, ==, &&, ||, <. These constant round primitives are equivalent to the corresponding "C" operators but work on shared secrets [3, 5].
- unbounded fan-in multiplications (e.g., [2]).
- SHUFFLE(a). Applies a secret random permutation on vector a [12].
- UNSHUFFLE(b). Applies on vector b the inverse of the secret permutation applied on vector a [12].

We now introduce the following primitives:

• $y = a[\overline{x}]$. Reads in y the item at secret index x in the array a containing N shared secrets.

Note that it could be implemented with arithmetic circuits using N equality tests, $y = \sum_{i=0}^{N-1} ((x == i) * a[i]).$

[10, 11] gave an efficient version for 2-party computations. We propose next an algorithm for this operation in t rounds for computations with threshold t.

We propose next an algorithm with less bits exchange and t rounds for this operation.

• $a[\overline{x}] = y$. Writes y at index x in the array of N shared secrets a. Note that this primitive could also be implemented in constant number of rounds with arithmetic circuits using N equality tests, a[i] = (x ==i) * (y - a[i]) + a[i], i = [0..(N - 1)].

2 Accessing arrays at secret indices

As mentioned before, this can be done with other primitives in constant round, but requiring N executions of the equality test operator in [5], and which requires more bits exchanges than our methods.

2.1 Bit-based Access

A fast method we propose is based on bit decomposition and exponentiation with secret index [5, 6]. It works for accessing arrays with size $N < \ell$ where $\ell = \log_2(|F|)$. Given the shared secret index x for a vector of length N, first compute

$$d_0, d_1, ..., d_\ell = bits(EXP(2, [x]^B))$$

by first running the *bits* algorithm [5] followed by the secret exponentiation of [6], and followed again by the *bits* algorithm of [5].

Now one can do array read with

$$\sum_{i=0}^N (d_i \ast a[i])$$

One can write y the array a at index x with

$$a[i] = a[i] + (y - a[i]) * d_i, \forall i \in [0..N].$$

This version is much faster than the ones above since it needs only two expensive bits primitives instead on N of them.

2.2 Mixnet-based secret index access

We show how to achieve the result in t rounds, where t is the number of supposed trusted servers/participants. First, we assume that $x \in [0..(N-1)]$ and is shared among t participants using an additive sharing with shares either from the set of integers Z or from Z_N . Transformations from any sharing to a sharing of this form was described in [9, 4].

Read at secret index To perform the operation $y = a[\overline{x}]$ where a is an array of N shared secrets, one can use a mixnet related to the one we proposed in [12]. Each participant A_j encrypts his shares $[a_1]_j, ..., [a_N]_j$ of a using a homomorphic encryption scheme E_j for which it holds the secret key and which allows for applying addition in F on its plaintexts via some operation \oslash on ciphertexts [12]. All shares are then passed through a mixnet formed by the t participants holding shares of x. Each A_i generates a vector z of N random sharings of zero, and then for each input:

$$I_{j,i} = |E_j([a_1]_j), ..., E_j([a_N]_j)|$$

computes the output

$$O_{j,i} = |E_j([a_1]_j) \oslash E_j([z_1]_j), ..., E_j([a_N]_j) \oslash E_j([z_N]_j)| <<<[x]_i$$

where $[x]_i$ is A_i 's share of x and "<<< $[x]_i$ " denotes rotational shift with $[x]_i$ positions.

 A_i can prove that he shifted the arrays and did not simply replaced them with new arrays, by generating an interactive zero knowledge proof. The zero knowledge proof is based on generating a set of K additional claims, consisting of vectors obtained with different z and different shifts.

$$C_{j,k} = |E_j([a_1]_j) \oslash E_j([z_1^k]_j), \dots, E_j([a_N]_j) \oslash E_j([z_N^k]_j)| < < s^k, k = [1..K]$$

The verifiers specify a challenge bit c_k for each k. For bits $c_k = 0$, the prover reveals s^k and all shares of z^k , showing that the claims $C_{*,k}$ are a rotation of

the input. For bits $c_k = 1$, the prover reveals $s^k - [x]_i$ and all shares of $z - z^k$, showing that the claim is a rotation of the output.

At the end of the mix-net, the last agent in the chain broadcasts all encryptes shares in $O_{*,t}[1]$ and each participant decrypts its shares obtaining $a[\overline{x}]$.

In the case of MPCs based on homomorphic threshold encryption E, the mixnet is run on ciphertexts (operations remain the same but without involving shares):

$$O_i = |E(a_1) \oslash E_j(0), ..., E(a_N) \oslash E_j(0)| <<< [x]_i$$

and the claims in the ZK proof are:

$$C_k = |E([a_1]) \oslash E([z_1^k]), \dots, E([a_N]) \oslash E([z_N^k])| < << s^k, k = [1..K]$$

Write at a secret index To perform the operation $a[\overline{x}] = y$ where a is an array of N shared secrets, one can use a bidirectional mixnet related to the one proposed in [12]. In Phase 1, each participant A_j encrypts his shares $[a_1]_j, ..., [a_N]_j$ of a using a homomorphic encryption scheme E_j for which it holds the secret key and which allows for applying addition in F on its plaintext via some operation \oslash on ciphertexts [12]. All shares are then passed through a mixnet formed by the t participants holding shares of x. Each A_i generates a vector z of N random sharings of zero, and then for each input:

$$I_{i,j} = |E_j([a_1]_j), ..., E_j([a_N]_j)|$$

computes the output

$$O_{i,j} = |E_j([a_1]_j) \oslash E_j([z_1]_j), ..., E_j([a_N]_j) \oslash E_j([z_N]_j)| <<<[x]_i$$

where $[x]_i$ is A_i 's share of x and $<<<[x]_i$ denotes rotational shift with $[x]_i$ positions towards the left.

At the end of the mix-net, the t^{th} participant in the chain obtains as $O_{*,t}[1]$ the encrypted shares of $a[\overline{x}]$. Now each participant A_j sends to A_t its share of y encrypted with E_j , and A_t replaces $O_{j,t}[1]$ with $E_j([y]_j)$.

In Phase 2, the mix-net is now run in the reverse direction with $O_{*,t}$ as input. Each A_i generates a vector z' of N random sharings of zero, and then for each input:

$$I'_{j,i} = |E_j([a_1]_j), ..., E_j([a_N]_j)|$$

computes the output

$$O_{j,i}' = |E_j([a_1]_j) \oslash E_j([z_1']_j), ..., E_j([a_N]_j) \oslash E_j([z_N']_j)| >>> [x]_i$$

where $[x]_i$ is A_i 's share of x and $>>> [x]_i$ denotes rotational shift with $[x]_i$ positions towards the right. The result $O'_{*,i}$ is the result vector a.

 A_i can prove that he shifted the arrays and did not simply replaced them with new arrays, by generating an interactive zero knowledge proof. This proof also shows that the rotation is with the same number of positions and in the reverse direction as the first phase. The zero knowledge proof is based on generating, besides a set of K claims for Phase 1 as at the previous technique:

$$C_{j,k} = |E_j([a_1]_j) \oslash E_j([z_1^k]_j), \dots, E_j([a_N]_j) \oslash E_j([z_N^k]_j)| < < s^k, k = [1..K]$$

a set of K additional claims, consisting of vectors obtained with different z' and the shifts of the corresponding claims of the first phase.

$$C'_{j,k} = |E_j([a_1]_k) \oslash E_j([z_1'^k]_j), \dots, E_j([a_N]_j) \oslash E_j([z_N'^k]_j)| >>> s^k, k = [1..K]$$

The verifiers specify a challenge bit c_k for each k in [1..K]. For bits $c_k = 0$, the prover reveals s^k and all shares of z and z'^k , showing that at both phases the claims $C_{j,*}$ and $C'_{j,*}$ are a rotation of their inputs. For bits $c_k = 1$, the prover reveals $s^k - [x]_i$ and all shares of $z - z^k$ and $z' - z'^k$, showing that at both phases the claims $C_{j,*}$ and $C'_{j,*}$ are rotations of the output.

At the end of the mix-net, the last agent in the chain broadcasts all encrypted shares in $O'_{*,t}$ and each participant decrypts its shares obtaining the result vector a.

As for the read operation, in the case of MPCs based on homomorphic threshold encryption E, the mixnet is run on ciphertexts (operations remain the same but without involving shares). At Phase 1:

$$O_i = |E(a_1) \oslash E_j(0), ..., E(a_N) \oslash E_j(0)| <<<[x]_i$$

and the claims in the ZK proof are:

$$C_k = |E([a_1]) \oslash E([z_1^k]), \dots, E([a_N]) \oslash E([z_N^k])| < < s^k, k = [1..K]$$

at Phase 2:

$$O'_i = |E(a_1) \oslash E_j(0), ..., E(a_N) \oslash E_j(0)| >>> [x]_i$$

and the claims in the ZK proof are:

$$C'_{k} = |E([a_{1}]) \oslash E([z_{1}^{k}]), ..., E([a_{N}]) \oslash E([z_{N}^{k}])| >>> s^{k}, k = [1..K]$$

A similar ZK proof as above is performed simultaneously with the claims of both phases.

3 Analysis

The array read requires t+1 rounds in the passive attacker model (without the ZK proofs which add Kt messages). The number of exchanged bits is $(t+1) * t * \ell * N = O(t^2 \ell N)$ for secret sharing and $(t+1) * \ell * N = O(t\ell N)$ for MPC with homomorphic threshold encryption when $|F| = \ell$. The array write requires two times this number of operations.

requires two times this number of operations. With the $y = \sum_{i=0}^{N-1} ((x == i) * a[i])$ implementation of array read, the number of exchanged bits is $O(N * t^2 \ell \log(\ell))$ for secret sharing.

Generating Random Secret Numbers 4

The protocols described here were design to speed up the computations employed by the applications in [13, 1]. Another needed component of those applications consist in generating random numbers between 0 and M, for some maximum vale M. There exist well known techniques for generating random secret numbers:

- RANDOM(0, |p| 1): generates a radom number in Z_p , the field of the secret sharing [3].
- RANDOM(0,1): generates a sharing of a random secret uniformly distributed over $\{0,1\}$ [5].

The version we use is a simple modification of the protocol for generating a "Random Solve Instance" for conversion to bits in [5], where instead of checking that the result is smaller than p, we check that the result is smaller than M.

We therefore employ the bitwise Less-Than protocol, $[a \stackrel{?}{<} b]_p$ in [5]. Let ℓ be the number of bits needed to represent M, i.e., $2^{\ell-1} \leq M < 2^{\ell}$. The protocol is given in Algorithm 1.

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algorithm RANDOM(0, M, \ell)
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 $\begin{aligned} \forall k \in [0, \ell - 1] : [b_k]_p &= RANDOM(0, 1); \\ c &= [b \stackrel{?}{<} M]_p, \text{ where } b \text{ is the number represented on bits by} \\ [b_0]_p, \dots, [b_{\ell-1}]_p; \\ \text{reconstruct } c; \\ \textbf{while } (c=0); \\ \textbf{return } \sum_{i=0}^{\ell-1} 2^i [b_i]_p; \end{aligned}$ do

Algorithm 1: Algorithm for generating a random number (with ℓ bits) between [0, M]

The algorithm consists of creating an ℓ bit random number, and the testing if it is smaller than M. The expected number of trials is less than 2, and the trials can be done in parallel.

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