# Overlap-free Karatsuba-Ofman Polynomial Multiplication Algorithm 

Haining Fan, Jiaguang Sun, Ming Gu and Kwok-Yan Lam


#### Abstract

We describe how a new way to split input operands allows for fast VLSI implementations of the polynomial Karatsuba-Ofman algorithm. Compared to previous Karatsuba-Ofman multipliers, the XOR gate delay of the proposed $G F(2)[x]$ multiplier is reduced by about $33 \%$ for $n=2^{i}(i>1)$.


## Index Terms

Karatsuba algorithm, Karatsuba-Ofman algorithm, polynomial multiplication, subquadratic space complexity multiplier, finite field.

## I. Introduction

Published in 1962 [1], Karatsuba-Ofman algorithm (KOA) was the first integer multiplication method broke the quadratic complexity barrier in positional number systems. Due to its simplicity, its polynomial version is widely adopted to design VLSI $G F\left(2^{n}\right)$ parallel multipliers in $G F\left(2^{n}\right)$ based cryptosystems [7]-[25]. Two parameters are often used to measure the performance of a $G F\left(2^{n}\right)$ parallel multiplier, namely, the space and time complexities. The space complexity is often represented in terms of the total number of 2-input XOR and AND gates used. The corresponding time complexity is given in terms of the maximum delay faced by a signal due to these XOR and AND gates. Symbols " $T_{A}$ " and " $T_{X}$ " are often used to represent the delay of one 2-input AND and XOR gates, respectively.

The existing bit parallel $G F\left(2^{n}\right)$ multipliers may be simply classified into the following three categories according to the asymptotic space complexity of the multiplication algorithm : subquadratic, quadratic and hybrid multipliers. KOA has been used in many subquadratic and hybrid multipliers. These multipliers first perform a KOA-based multiplication of two input binary polynomials $A=\sum_{i=0}^{n-1} a_{i} x^{i}$ and $B=\sum_{i=0}^{n-1} b_{i} x^{i}$, and then a modulo reduction operation using the field generating irreducible polynomial. To explain the general idea of KOA easily, we assume that $n=2 m=2^{i}(i>1)$ in the following.

First, previous KOA implementations split polynomials $A$ and $B$ into the "most significant half" and the "least significant half" as follows:

$$
\begin{aligned}
A & =\sum_{i=0}^{n-1} a_{i} x^{i}=x^{m} \sum_{i=0}^{m-1} a_{m+i} x^{i}+\sum_{i=0}^{m-1} a_{i} x^{i}=x^{m} A_{H}+A_{L}, \\
B & =\sum_{i=0}^{n-1} b_{i} x^{i}=x^{m} \sum_{i=0}^{m-1} b_{m+i} x^{i}+\sum_{i=0}^{m-1} b_{i} x^{i}=x^{m} B_{H}+B_{L},
\end{aligned}
$$

where $A_{H}=\sum_{i=0}^{m-1} a_{m+i} x^{i}, A_{L}=\sum_{i=0}^{m-1} a_{i} x^{i}, B_{H}$ and $B_{L}$ are defined similarly.
Then the product $A B$ is computed recursively using

$$
\begin{equation*}
A B=A_{H} B_{H} x^{2 m}+\left\{\left[\left(A_{H}+A_{L}\right)\left(B_{H}+B_{L}\right)\right]-\left[A_{H} B_{H}+A_{L} B_{L}\right]\right\} x^{m}+A_{L} B_{L} \tag{1}
\end{equation*}
$$

Please note that "-" is the same as " + " in $G F(2)[x]$. For the VLSI implementation of (1), terms in square brackets are calculated concurrently, and therefore two XOR gate delays, i.e., $2 T_{X}$, are required to compute the expression in curly brackets besides the cost to compute the half sized product $A_{L} B_{L}$ and $A_{H} B_{H}$.

Finally, the three polynomials $A_{H} B_{H} x^{2 m},\left[\left(A_{H}+A_{L}\right)\left(B_{H}+B_{L}\right)-A_{H} B_{H}-A_{L} B_{L}\right] x^{m}$ and $A_{L} B_{L}$ in (1) are XORed in an overlap module by adding coefficients of common exponents of $x$ together. In order to explain overlaps of common exponents of $x$ clearly, we present the following table which shows ranges of $x$ 's exponents in these three polynomials.

Please note that overlaps occur only when $n \geq 4$ (or $m \geq 2$ ), and there is no overlap when $n=2($ or $m=1)$. Because of these overlaps, one XOR gate delay $T_{X}$ is required in the overlap

TABLE I
RANGES OF $x$ 'S EXPONENTS IN THREE POLYNOMIALS OF (1)

module. Therefore, a total of 3 XOR gate delays, i.e., $3 T_{X}$, are required in (1) besides the cost of the recursive computation of three half sized products.

Let $n=k d$, previous generalizations of KOA split the two input operands into $k$ successive block each with $d$ coefficients. Since the product of two degree- $(d-1)$ polynomials is a polynomial of degree-( $2 d-2$ ), overlaps always exist if $d>1$.

In order to obtain exact complexities of the above binary polynomial KOA (1), we introduce some symbols of [3]. Let $\mathcal{S}$ and $\mathcal{D}$ stand for "Space" and "Delay", respectively. We use $\mathcal{S}^{\otimes}(n)$, $\mathcal{S}^{\oplus}(n), \mathcal{D}^{\otimes}(n)$ and $\mathcal{D}^{\oplus}(n)$ to denote the number of multiplication (AND) and addition (XOR) operations, the time delays introduced by multiplication and addition operations, respectively. Then the following recurrence relations, which describe the complexities of KOA, can be established.

$$
\begin{aligned}
& \left\{\begin{array}{l}
\mathcal{S}^{\otimes}(2)=3, \\
\mathcal{S}^{\otimes}(n)=3 \mathcal{S}^{\otimes}(n / 2) ;
\end{array}\right. \\
& \left\{\begin{array} { l } 
{ \mathcal { S } ^ { \oplus } ( 2 ) = 4 , } \\
{ \mathcal { S } ^ { \oplus } ( n ) = 3 \mathcal { S } ^ { \oplus } ( n / 2 ) + 4 n - 4 ; }
\end{array} \quad \text { and } \quad \left\{\begin{array}{l}
\mathcal{D}^{\otimes}(2)=1, \\
\mathcal{D}^{\otimes}(n)=\mathcal{D}^{\otimes}(n / 2) ;
\end{array}\right.\right. \\
& \mathcal{D}^{\oplus}(2)=2, \\
& \mathcal{D}^{\oplus}(n)=\mathcal{D}^{\oplus}(n / 2)+3 .
\end{aligned}
$$

After solving the above recurrence relations using formulae derived in [3], we have the
following complexity results for the binary polynomial KOA [7].

$$
\left\{\begin{array}{l}
\mathcal{S}^{\otimes}(n)=n^{\log _{2} 3} \\
\mathcal{S}^{\oplus}(n)=6 n^{\log _{2} 3}-8 n+2 \\
\mathcal{D}^{\otimes}(n)=1 \\
\mathcal{D}^{\oplus}(n)=3 \log _{2} n-1
\end{array}\right.
$$

Besides KOA, a Toeplitz matrix-vector product approach was presented recently to construct subquadratic $G F\left(2^{n}\right)$ multipliers [3]. It takes advantage of a shifted polynomial basis [4] and applies the coordinate transformation technique of [5] and [6]. Both the space and time complexities of the resulting multiplier are better than those of the best KOA-based subquadratic multipliers. For example, with $n=2^{i}(i>0)$, the space complexity is about $8 \%$ better, while the time complexity is about $33 \%$ better, respectively.

Since these Toeplitz matrix-vector product formulae are obtained by transposing [2, Th6, p.17] corresponding polynomial KOA-like formulae, the following question arises naturally: is it possible to reduce the time or space complexity of KOA further? We answer this question positively in the next section. We will propose a new implementation of the polynomial KOA. When it is used recursively to design binary polynomial multipliers, the asymptotic time complexity is also about $33 \%$ better than the previous KOA for $n=2^{i}(i>0)$. The key point of the proposed implementation is a new method to split input operands. The splitting method is simple and straightforward, but it does not seem to have been reported in the open literature.

## II. New Polynomial KOA Implementation

Instead of splitting input operands into the "most significant half" and the "least significant half', we split them according to the parity of $x$ 's exponent. That is to say, we may rewrite $A$ and $B$ as follows

$$
\begin{aligned}
& A=\sum_{i=0}^{n-1} a_{i} x^{i}=\sum_{i=0}^{m-1} a_{2 i} x^{2 i}+\sum_{i=0}^{m-1} a_{2 i+1} x^{2 i+1}=\sum_{i=0}^{m-1} a_{2 i} x^{2 i}+x \sum_{i=0}^{m-1} a_{2 i+1} x^{2 i} \\
& B=\sum_{i=0}^{n-1} b_{i} x^{i}=\sum_{i=0}^{m-1} b_{2 i} x^{2 i}+\sum_{i=0}^{m-1} b_{2 i+1} x^{2 i+1}=\sum_{i=0}^{m-1} b_{2 i} x^{2 i}+x \sum_{i=0}^{m-1} b_{2 i+1} x^{2 i}
\end{aligned}
$$

Now let $y=x^{2}, A_{e}(y)=\sum_{i=0}^{m-1} a_{2 i} y^{i}$ and $A_{o}(y)=\sum_{i=0}^{m-1} a_{2 i+1} y^{i} . B_{e}(y)$ and $B_{o}(y)$ are defined similarly. Operands $A$ and $B$ can be rewritten as $A=A_{e}(y)+x A_{o}(y)$ and $B=B_{e}(y)+x B_{o}(y)$. Please note that terms $A_{e}(y), A_{o}(y), B_{e}(y)$ and $B_{o}(y)$ are polynomials in $y$ of degrees less than $m$. Therefore multiplication operations between them may also be computed recursively. Using the above splitting method of $A$ and $B$, we have the following KOA-like formula

$$
\begin{align*}
A B= & \left(A_{e}(y)+x A_{o}(y)\right)\left(B_{e}(y)+x B_{o}(y)\right) \\
= & \left.\left\{A_{e}(y) B_{e}(y)+x^{2} A_{o}(y) B_{o}(y)\right\}+x\left\{A_{e}(y) B_{o}(y)+A_{o}(y) B_{e}(y)\right)\right\} \\
= & \left\{A_{e}(y) B_{e}(y)+y A_{o}(y) B_{o}(y)\right\}+ \\
& x\left\{\left[\left(A_{e}(y)+A_{o}(y)\right)\left(B_{e}(y)+B_{o}(y)\right)\right]+\left[A_{e}(y) B_{e}(y)+A_{o}(y) B_{o}(y)\right]\right\} . \tag{2}
\end{align*}
$$

For the VLSI implementation of (2), multiplying a polynomial by $x$ or $y=x^{2}$ is equivalent to shifting its coefficients, and no gate is required. It is easy to see that the expansion of $\left\{A_{e}(y) B_{e}(y)+y A_{o}(y) B_{o}(y)\right\}$ in (2) contains only terms with even exponents of $x$ since $y=x^{2}$, and the expansion of $x\left\{\left[\left(A_{e}(y)+A_{o}(y)\right)\left(B_{e}(y)+B_{o}(y)\right)\right]+\left[A_{e}(y) B_{e}(y)+A_{o}(y) B_{o}(y)\right]\right\}$ contains only terms with odd exponents of $x$. Thus, there is no overlap exists when computing their summation, and no gate is required either. Moreover, terms in square brackets can be computed concurrently, and the addition operation requires 1 XOR gate delay $T_{X}$. Therefore, we know that computing $A B$ via (2) needs only a total of $2 T_{X}$ besides the cost of the recursive computation of three half sized products. Please recall that the corresponding XOR gate delay is $3 T_{X}$ in (1). Consequently, the following recurrence relations, which describe the algorithm complexities, can be established.

$$
\begin{aligned}
& \left\{\begin{array}{l}
\mathcal{S}^{\otimes}(2)=3, \\
\mathcal{S}^{\otimes}(n)=3 \mathcal{S}^{\otimes}(n / 2) ;
\end{array}\right. \\
& \left\{\begin{array} { l } 
{ \mathcal { S } ^ { \oplus } ( 2 ) = 4 , } \\
{ \mathcal { S } ^ { \oplus } ( n ) = 3 \mathcal { S } ^ { \oplus } ( n / 2 ) + 4 n - 4 ; }
\end{array} \quad \text { and } \quad \left\{\begin{array}{l}
\mathcal{D}^{\otimes}(2)=1, \\
\mathcal{D}^{\otimes}(n)=\mathcal{D}^{\otimes}(n / 2) ;
\end{array}\right.\right. \\
& \mathcal{D}^{\oplus}(2)=2, \\
& \mathcal{D}^{\oplus}(n)=\mathcal{D}^{\oplus}(n / 2)+2 .
\end{aligned}
$$

Their solutions are as follows:

$$
\left\{\begin{array}{l}
\mathcal{S}^{\otimes}(n)=n^{\log _{2} 3} \\
\mathcal{S}^{\oplus}(n)=6 n^{\log _{2} 3}-8 n+2 \\
\mathcal{D}^{\otimes}(n)=1 \\
\mathcal{D}^{\oplus}(n)=2 \log _{2} n
\end{array}\right.
$$

Compared to previous implementations of polynomial KOA , the XOR gate delay of (2), i.e., $\mathcal{D}^{\oplus}(n)$, is about $33 \%$ better for $n=2^{i}(i>0)$.

Similar to the generalizations of KOA, we may also derive some new KOA-like formulae for polynomials of higher degrees. As an example, we show the formula for $n=3 k=3^{i}(i>1)$. Let $y=x^{3}$ and split $A$ as follows

$$
\begin{aligned}
A & =\sum_{i=0}^{n-1} a_{i} x^{i}=\sum_{i=0}^{k-1} a_{3 i} x^{3 i}+x \sum_{i=0}^{k-1} a_{3 i+1} x^{3 i}+x^{2} \sum_{i=0}^{k-1} a_{3 i+2} x^{3 i} \\
& =A_{0}(y)+x A_{1}(y)+x^{2} A_{2}(y)
\end{aligned}
$$

where $A_{0}(y)=\sum_{i=0}^{k-1} a_{3 i} y^{i}, A_{1}(y)=\sum_{i=0}^{k-1} a_{3 i+1} y^{i}$ and $A_{2}(y)=\sum_{i=0}^{k-1} a_{3 i+2} y^{i}$.
Then we have

$$
\begin{aligned}
A B= & \left\{A_{0} B_{0}+y\left[\left(A_{1}+A_{2}\right)\left(B_{1}+B_{2}\right)+A_{1} B_{1}+A_{2} B_{2}\right]\right\}+ \\
& x\left\{\left[\left(A_{0}+A_{1}\right)\left(B_{0}+B_{1}\right)+A_{0} B_{0}+A_{1} B_{1}\right]+y A_{2} B_{2}\right\}+ \\
& x^{2}\left\{\left(A_{0}+A_{2}\right)\left(B_{0}+B_{2}\right)+A_{0} B_{0}+A_{2} B_{2}+A_{1} B_{1}\right\},
\end{aligned}
$$

where " $(y)$ "s in expressions $A_{i}(y)$ and $B_{i}(y)$ are omitted.
The following recurrence relations describe the complexities of this formula.

$$
\begin{aligned}
& \left\{\begin{array}{l}
\mathcal{S}^{\otimes}(3)=6, \\
\mathcal{S}^{\otimes}(n)=6 \mathcal{S}^{\otimes}(n / 3) ;
\end{array}\right. \\
& \left\{\begin{array} { l } 
{ \mathcal { S } ^ { \oplus } ( 3 ) = 1 2 , } \\
{ \mathcal { S } ^ { \oplus } ( n ) = 6 \mathcal { S } ^ { \oplus } ( n / 3 ) + \frac { 2 2 } { 3 } n - 1 0 ; }
\end{array} \quad \text { and } \quad \left\{\begin{array}{l}
\mathcal{D}^{\otimes}(3)=1, \\
\mathcal{D}^{\otimes}(n)=\mathcal{D}^{\otimes}(n / 3) ;
\end{array}\right.\right. \\
& \mathcal{D}^{\oplus}(3)=3, \\
& \mathcal{D}^{\oplus}(n)=\mathcal{D}^{\oplus}(n / 3)+3 .
\end{aligned}
$$

Table II compares asymptotic complexities of proposed formulae with the previous KOA and Toeplitz matrix-vector product (TMVP) formulae over the ground field $G F(2)$. The size of operands is assumed to be $n=2^{t}$ or $3^{t}(t>0)$.

TABLE II
COMPARISONS OF ASYMPTOTIC COMPLEXITIES FOR $n=b^{t}$

| $b$ | Algorithm | \#AND | \#XOR | Gate delay |
| :--- | :--- | :---: | :---: | :---: |
| 2 | KOA [7] | Proposed | $n^{\log _{2} 3}$ | $6 n^{\log _{2} 3}-8 n+2$ |
|  | $n^{\log _{2} 3}$ | $6 n^{\log _{2} 3}-8 n+2$ | $\left(2 \log _{2} n-1\right) T_{X}+T_{A}$ |  |
|  | TMVP | $\left.n^{\log _{2} 3} n\right) T_{X}+T_{A}$ |  |  |
|  | KOA [13] [17] | $n^{\log _{3} 6}$ | $\frac{16}{3} n^{\log _{3} 6}-\frac{22}{3} n+2$ | $\left(4 \log _{3} n-1\right) T_{X}+T_{A}$ |
|  | Proposed | $n^{\log _{3} 6}$ | $\frac{16}{3} n^{\log _{3} 6}-\frac{22}{3} n+2$ | $\left(3 \log _{3} n\right) T_{X}+T_{A}$ |
|  | TMVP | $n^{\log _{3} 6}$ | $\frac{24}{5} n^{\log _{3} 6}-5 n+\frac{1}{5}$ | $\left(3 \log _{3} n\right) T_{X}+T_{A}$ |

## III. Conclusions

We have proposed a new implementation of the polynomial KOA. It is based on a different splitting method of input operands. In this paper, the polynomial ring $G F(2)[x]$ has been used as the background to demonstrate the new implementation. It is clear that the method may also be generalized to other polynomial rings that the original polynomial KOA can be applied.

Although both KOA and TMVP can be used to design $G F\left(2^{n}\right)$ parallel multipliers, we must emphasize that these two algorithms are distinct. One obvious difference is the size of their inputs and outputs. Take the multiplication operation of two degree- $(n-1)$ binary polynomials $A$ and $B$ for example, KOA computes their $(2 n-1)$-bit product while the TMVP multiplier of [3] calculates the residue of $A B$ modulo a degree- $n$ binary polynomial $f(x)$. Or, more precisely, KOA computes the product of two elements in the ring $G F(2)[x]$ while the TMVP multiplier of [3] performs the multiplication operation in the quotient ring $G F(2)[x] /(f(x))$, which becomes the finite field $G F\left(2^{n}\right)$ if $f(x)$ is irreducible over $G F(2)$.

## REFERENCES

[1] A. Karatsuba and Y. Ofman, "Multiplication of Multidigit Numbers on Automata," Soviet Physics-Doklady (English translation), vol. 7, no. 7, pp. 595-596, 1963.
[2] S. Winograd, Arithmetic Complexity of Computations, SIAM, 1980.
[3] H. Fan and M. A. Hasan, "A New Approach to Subquadratic Space Complexity Parallel Multipliers for Extended Binary Fields," IEEE Transactions on Computers, vol. 56, no. 2, pp. 224-233, Feb. 2007.
[4] H. Fan and Y. Dai, " Fast bit parallel $G F\left(2^{n}\right)$ Multiplier for All Trinomials," IEEE Transactions on Computers, vol. 54, no. 4, pp. 485-490, Apr. 2005.
[5] M. A. Hasan and V. K. Bhargava, "Division and Bit-serial Multiplication over $G F\left(q^{m}\right)$," IEE Proceedings-E, vol. 139, no. 3, pp. 230-236, May 1992.
[6] M. A. Hasan and V. K. Bhargava, "Architecture for Low Complexity Rate-Adaptive Reed-Solomon Encoder," IEEE Transactions on Computers, vol. 44, no. 7, pp. 938-942, July 1995.
[7] C. Paar, " A New Architecture for a Parallel Finite Field Multiplier with Low Complexity Based on Composite Fields," IEEE Transactions on Computers, vol. 45, no. 7, pp. 856-861, July 1996.
[8] C. Paar, P. Fleischmann, and P. Roelse, " Efficient Multiplier schemes for Galois Fields $G F\left(2^{4 n}\right)$, " IEEE Transactions on Computers, vol. 47, no. 2, pp. 162-170, Feb. 1998.
[9] M. Elia, M. Leone, and C. Visentin, "Low complexity bit-parallel multipliers for $G F\left(2^{m}\right)$ with generator polynomial $x^{m}+x^{k}+1$, , IEE Electronics Letters, vol. 35, no.7, pp.551-552, 1999.
[10] M. Jung, F. Madlener, M. Ernst, and S. Huss, "A Reconfigurable Coprocessor for Finite Field Multiplication in $G F\left(2^{n}\right)$," Proc. IEEE Workshop Heterogeneous reconfigurable Systems on Chip, 2002.
[11] M. Ernst, M. Jung, F. Madlener, S. Huss, and R. Blumel, "A Reconfigurable System on Chip Implementation for Elliptic Curve Cryptography over GF(2n)," Proc. Cryptographic Hardware and Embedded Systems (CHES 2002), LNCS 2523, pp. 381-399, 2003.
[12] C. Grabbe, M. Bednara, J. Shokrollahi, J. Teich and J. von zur Gathen, " FPGA Designs of parallel hign performance GF ( $2^{233}$ ) Multipliers," Proc. Int'l Symposium on Circuits and Systems (ISCAS 2003), vol. II, pp. 268-271, 2003.
[13] A. Weimerskirch and C. Paar, "Generalizations of the Karatsuba Algorithm for Efficient Implementations," 2003, http : //www.crypto.ruhr - uni - bochum.de/imperia/md/content/texte/kaweb.pdf.
[14] F. Rodríguez-Henríquez and Ç. K. Koç, "On fully parallel Karatsuba multipliers for $G F\left(2^{m}\right)$," Proc. Int'l Conf. Computer Science and Technology (CST 2003), pp. 405-410, 2003.
[15] S. S. Erdem and Ç. K. Koç, "A Less Recursive Variant of Karatsuba-Ofman Algorithm for Multiplying Operands of Size a Power of Two," Proc. 16th IEEE Symposium on Computer Arithmetic (Arith-16 2003), pp. 28-35, 2003.
[16] A. E. Cohen and K. K. Parhi, "Implementation of scalable elliptic curve cryptosystem crypto-accelerators for $G F\left(2^{m}\right)$," Proc. 13th Asilomar Conf. on Signals, Systems and Computers, vol. 1, pp. 471-477, Nov. 2004.
[17] B. Sunar, " A Generalized Method for Constructing Subquadratic Complexity GF ( $2^{k}$ ) Multipliers," IEEE Transactions on Computers, vol. 53, no. 9, pp. 1097-1105, Sept. 2004.
[18] P. L. Montgomery, "Five, Six, and Seven-Term Karatsuba-Like Formulae," IEEE Transactions on Computers, vol. 54, no. 3, pp. 362-369, Mar. 2005.
[19] H. Fan and M. A. Hasan, "Comments on "five, Six, and Seven-Term Karatsuba-Like Formulae"," IEEE Transactions on Computers, vol. 56, no. 5, pp. 716-717, May 2007.
[20] K. Y. Chang, D. Hong, and H. S. Cho, "Low Complexity Bit-Parallel Multiplier for $G F\left(2^{m}\right)$ Defined by All-One Polynomials Using Redundant Representation" IEEE Transactions on Computers, vol. 54, no. 12, pp. 1628-1630, Dec. 2005.
[21] N. S. Chang, C. H. Kim, Y. H. Park, and J. Lim, "A Non-Redundant and Efficient Architecture for Karatsuba-Ofman Algorithm," Proc. 8th International Conf. on Information Security (ISC 2005), LNCS 3650, pp. 288-299, 2005.
[22] Z. Dyka and P. Langendoerfer, "Area efficient hardware implementation of elliptic curve cryptography by iteratively applying karatsuba's method," Proc. Conf. on Design, Automation and Test in Europe 2005, pp. 70-75, 2005.
[23] J. von zur Gathen and J. Shokrollahi, " Efficient FPGA-based Karatsuba Multipliers for Polynomials over $F_{2}$," Proc. 12th Workshop on Selected Areas in Cryptography (SAC 2005), LNCS 3897 pp.359-369, 2006.
[24] L. S. Cheng, A. Miri and T. H. Yeap, "Improved FPGA Implementations of Parallel Karatsuba Multiplication over $G F\left(2^{n}\right)$," Proc. 23rd Biennial Symposium on Communications, 2006.
[25] S. Peter and P. Langendorfer, "An Efficient Polynomial Multiplier in $G F\left(2^{m}\right)$ and its Application to ECC Designs," Proc. Conf. on Design, Automation and Test in Europe 2007, pp. 1253-1258, 2007.

