

Multicore Implementation of the Tate Pairing over Supersingular Elliptic Curves

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Abstract. This paper describes the design of a fast multicore library for the cryptographic Tate pairing over supersingular elliptic curves. We report a calculation time of just 3.15 ms and 1.87 ms on the Intel Core2 and Intel Core i7 architectures, respectively, for the computation of the reduced modified Tate pairing over $\mathbb{F}_{3^{509}}$. We try also to answer one important design question that surges: how many cores should be utilized for a given application?

Keywords: Tate pairing, supersingular curve, finite field arithmetic, multiprocessor cores.

1 Introduction

During the early years of this century it was generally assumed that computing cryptographic bilinear pairings was a computationally expensive task. Taking as a starting point the breakthrough introduced by Miller [23, 24], who proposed the first iterative approach to compute a cryptographic pairing, several authors focused their efforts on finding algorithmic improvements and shortcuts to further reduce the complexity of the so-called *Miller's Algorithm* [3, 4, 9, 18, 19, 30]. Those theoretical findings were experimentally validated by different means. At first, it was thought that the rich parallelization potential shown by hardware platforms could be exploited in order to produce faster and more compact pairing implementations. Through the years, this assumption has been confirmed in many research works (see for instance [8, 20, 29] for a comprehensive bibliography). Nevertheless, with the only exception of the ASIP in [20], all hardware accelerators reported in the open literature until today, have targeted low and medium security levels.

On the other hand, in the last few years a second wave of authors have investigated the challenges associated to the efficient implementation of pairings

in software platforms [12, 16, 17, 21]. From the results reported by those research works, it appears that software pairing libraries can compete and even outperform their hardware accelerator counterparts. Furthermore, yet another way to exploit parallelism can be instrumented when the multiprocessor core architectures introduced just recently by Intel are targeted. Multicore architectures can be seen as a massive way to obtain parallelism via the concurrent usage of powerful individual processors that are tightly interconnected.

To our knowledge, the only bilinear pairing library targeting a multicore architecture was reported in [12]. However the results presented in [12] were not too optimistic. After essaying several scenarios, the authors concluded that on a Core2 64-bit platform, the best option to parallelize the computation of multiple pairings in their library was to perform one pairing on each core. They state that, “*if the requirement is for two pairing evaluations, the slightly moronic conclusion is that one can perform one pairing on each core [...], doubling the performance versus two sequential invocations of any other method that does not already use multi-core parallelism internally*” [12].

This paper is devoted to the design of a software library for the reduced modified Tate pairing on supersingular elliptic curves defined over characteristics three and two. After a careful selection of the field arithmetic (Section 2) and pairing algorithms (Section 3) we show that the usage of multicore architectures can be effectively used to provide significant computational speedups. A sequential version of our software¹ computes the reduced modified Tate pairing in 7.59 and 11.19 ms for the extension fields $\mathbb{F}_{3^{509}}$ and $\mathbb{F}_{3^{1223}}$, respectively on an Intel Quad Core running at 2.4 GHz. Speedups of approximately $2.6\times$ are obtained when using the four cores available in the target architecture (Section 4).

2 Finite Field Arithmetic Using SSE

2.1 Characteristic Two Fields \mathbb{F}_{2^m}

Frobenius and Frobenius Inverse Operators Let $f(x)$ be an irreducible polynomial of degree m over \mathbb{F}_2 . Then, the binary extension field \mathbb{F}_{2^m} is defined as, $\mathbb{F}_{2^m} \cong \mathbb{F}_2[x]/(f(x))$. Let a be an arbitrary element in \mathbb{F}_{2^m} , which in canonical basis can be written as, $a(x) = \sum_0^{m-1} a_i x^i$, with $a_i \in \mathbb{F}_2$ for $i = 0, 1, \dots, m-1$. Let us also assume that the extension degree m can be expressed as, $m = 2u + 1$, with $u \geq 1$. Then, the Frobenius operator applied to a consists of computing $c = a^2 \bmod f(x)$, which can be obtained as,

$$c = \sum_{i=0}^u a_i x^{2i} + x^m \sum_{i=1}^u a_{u+i} x^{2i-1} \bmod f(x) = a^L + a^H x^m \bmod f(x).$$

The field element c can be efficiently calculated in software by extracting the two half-length vectors a^L and a^H along with the computation of an $\frac{m}{2}$ -bit multiplication by the per-field constant x^m . The subsequent reduction process modulo $f(x)$ is typically implemented in software by using XOR and shift operations.

¹ A full-open source code for benchmarking the library will be made available.

The Frobenius inverse operator of a is computed by determining the unique field element $b \in \mathbb{F}_{2^m}$ such that $b^2 = a$ holds. The element b can be computed in terms of the square root of the field constant x as,

$$b = \sum_{i=0}^{\lfloor \frac{m-1}{2} \rfloor} a_{2i} x^i + x^{\frac{1}{2}} \sum_{i=0}^{\lfloor \frac{m-3}{2} \rfloor} a_{2i+1} x^i \pmod{f(x)} = a_{\text{even}} + x^{\frac{1}{2}} a_{\text{odd}} \pmod{f(x)}.$$

The efficient computation in software of b defined as above is performed by extracting the even and odd bits of a into the half length vectors a_{even} and a_{odd} , respectively. This should be followed by multiplying the half length vector a_{odd} times the pre-computed constant $x^{\frac{1}{2}}$.

In the case that the irreducible polynomial happens to be a trinomial of the form $f(x) = x^m + x^n + 1$ with m, n odd numbers, we have that, $x^{\frac{1}{2}} = x^{\frac{m+1}{2}} + x^{\frac{n+1}{2}}$. Since $x^{\frac{1}{2}} \cdot a_{\text{odd}}$ has degree $m - 1$, it follows that we do not need to perform a polynomial modular reduction and hence the inverse Frobenius operator of an arbitrary element $a \in \mathbb{F}_{2^m}$ can be obtained by computing [10],

$$b = \sum_{i=0}^{\lfloor \frac{m-1}{2} \rfloor} a_{2i} x^i + \left(x^{\frac{m+1}{2}} + x^{\frac{n+1}{2}} \right) \cdot \sum_{i=0}^{\lfloor \frac{m-3}{2} \rfloor} a_{2i+1} x^i.$$

Multiplier We implemented this arithmetic block by using a variation of the left-to-right *comb* multiplication scheme presented in [22], one of the fastest multiplier schemes for binary fields \mathbb{F}_{2^m} reported in the open literature.

Let w be the processor word size in bits. Then, the number of processor words required for storing an arbitrary element in the field \mathbb{F}_{2^m} is $s = \lceil \frac{m}{w} \rceil$. From these definitions, authors in [22] found that the computational complexity of their algorithm (excluding the one associated to the reduction process), was of $s(\frac{m}{4})$ w -bit XOR operations and a total of $(w/4 - 1)$ 4-bit left shift operations of a $2s$ -word vector. Additionally, their method makes use of a look-up table containing sixteen s -word entries, which is queried a total of $s(\frac{m}{4})$ times. The look-up table is pre-computed at a cost of three 1-bit left shift operations of an s -word vector and eleven w -bit XOR operations.

In the case of the SSE instruction set, we have $w = 128$. Hence, we can invoke specialized instructions to perform any logic or arithmetic operation over a bank of 128-bit SSE register operators. It is also possible to manipulate the contents of the SSE registers by applying left/right shift/rotate operators over them. Those shift and rotate operations are executed with very high efficiency if the operand is shifted or rotated by a constant value multiple of 8 bits. This feature motivated us to propose a right-to-left *comb* multiplication scheme that trades all but one of the 4-bit left shift operations required by the multiplier in [22], with 8-bit right shift operations. In the rest of this subsection, we describe our formulation.

Let us define $n = 32 \cdot s$. It results convenient to group the bit representation of a field element $a \in \mathbb{F}_{2^m}$ into 4-bit digits as follows,

$$a = (a_{m-1} \dots a_1 a_0) \Leftrightarrow a = (A_{n-1} \dots A_1 A_0),$$

where A_i , for $i = 0, 1, \dots, n-1$, is defined as, $A_i = \sum_{j=0}^3 a_{4i+j} x^{4i+j}$. It is noticed that each 128-bit SSE register can store thirty-two such digits².

As it will be discussed below, it results convenient to rearrange the n digits of the field element a into a $2 \times 16s$ matrix Idx as,

$$\text{Idx}[2][16s] = \begin{pmatrix} A_{n-1} & \dots & A_{33} & A_{31} & \dots & A_3 & A_1 \\ A_{n-2} & \dots & A_{32} & A_{30} & \dots & A_2 & A_0 \end{pmatrix} \quad (1)$$

In order to calculate the product $c = a \cdot b$ we prepare first a 16-entry look-up table by pre-computing,

$$\text{TblMul}[i] \leftarrow (i_3 x^3 + i_2 x^2 + i_1 x + i_0) \cdot b,$$

for $i = 0, 1, \dots, 15$ and where $i = (i_3 i_2 i_1 i_0)_2$ is the binary expansion of i . Then, the polynomial product $c = ab$ can be computed as follows,

$$\begin{aligned} ab &= \sum_{i=0}^{n-1} A_i x^{4i} b = \sum_{i=0}^{16s-1} x^{8i} (A_{2i} + x^4 A_{2i+1}) b \\ &= \sum_{i=0}^{15} \sum_{j=0}^{s-1} x^{8i} (A_{32j+2i} + x^4 A_{32j+2i+1}) x^{128j} b \\ &= \sum_{i=0}^{15} \sum_{j=0}^{s-1} x^{8i} (\text{TblMul}[A_{32j+2i}] + x^4 \text{TblMul}[A_{32j+2i+1}]) x^{128j} \quad (2) \\ &= \left(\sum_{i=0}^{15} \sum_{j=0}^{s-1} x^{8(i-16)} \text{TblMul}[\text{Idx}[1][16j+i]] x^{128j} \right) x^{128} + \\ &\quad \left(\sum_{i=0}^{15} \sum_{j=0}^{s-1} x^{8(i-16)} x^4 \text{TblMul}[\text{Idx}[0][16j+i]] x^{128j} \right) x^{128}. \end{aligned}$$

Note that in the last equality of Eq. (2) we used the matrix Idx as defined in Eq. (1), which allows us to recover the digits $A_{2(16j+i)}$ and $A_{2(16j+i)+1}$ as,

$$\text{Idx}[1][16j+i] = A_{2(16j+i)} \quad \text{and} \quad \text{Idx}[0][16j+i] = A_{2(16j+i)+1}.$$

One can compute Eq. (2) as shown in Algorithm 1. It is worth to stress that,

- In step 5, we extract the bits of a in such a way that its digits A_i for $i = 0, \dots, n-1$, are rearranged into a two dimensional array $\text{Idx}[2][16s]$ as described in Eq. (1).
- The shift operations of step 10, namely, $x^{128}, x^{256} \dots x^{1152}$, can be performed at no cost because they are at the beginning of each one of the 128-bit SSE registers.

² Note that the $v = 128 - (m \bmod 128)$ most significant bits of the last SSE register should be filled with zeroes.

Algorithm 1 SSE Implementation of a Right-to-Left Comb Multiplier over \mathbb{F}_{2^m} .**Input:** $a, b \in \mathbb{F}_{2^m}$.**Output:** $c = a \cdot b \bmod f(x) \in \mathbb{F}_{2^m}$.

```

1. for  $i \leftarrow 0$  to 15 do
2.   Compute the binary expansion of  $i = (i_3 i_2 i_1 i_0)_2$ ;
3.    $\text{TblMul}[i] \leftarrow (i_3 x^3 + i_2 x^2 + i_1 x + i_0) \cdot b(x)$ ;
4. end for
5.  $\text{Idx}[2][16s] \leftarrow \text{extractIdx}(a)$ ;
6.  $R \leftarrow 0$ ;
7. for  $k \leftarrow 0$  to 1 do
8.   for  $i \leftarrow 0$  to 15 do
9.     for  $j \leftarrow 0$  to  $s - 1$  do
10.       $R \leftarrow R + \text{TblMul}[\text{Idx}[k][i + 16 \cdot j]]x^{128 \cdot j}$ ;
11.    end for
12.     $R \leftarrow \text{rotRight\_Byte}(R, 1)$ 
13.  end for
14.  if  $i = 0$  then
15.     $R \leftarrow \text{rotLeft\_bit}(R, 4)$ ;
16.  end if
17.   $R \leftarrow \text{rotLeft\_Byte}(R, 16)$ ;
18. end for
19.  $c \leftarrow R \bmod f(x)$ ;
20. return  $C$ ;

```

- In step 12, a 1-byte right rotation is applied over the contents of the $2s$ -word accumulator R . This rotation operation is invoked thirty-two times.
- In step 15, a 4-bit left rotation over the $2s$ -word accumulator R is performed. This is the only left rotation by four bits included in the algorithm.
- In step 17, a left rotation by 16 bytes must be executed. This rotation is almost at free cost as it only implies the reassignment of the SSE registers.
- Finally in Step 19, a modular reduction with the polynomial $f(x)$ must be performed.

It is easy to verify that the computational cost of Algorithm 1 is of $(32 + 11)s$ XOR operations, $32s$ queries to the look-up table TblMul , thirty-two 1-byte right rotations of a $2s$ -word vector, one 4-bit left rotation of a $2s$ -word vector, plus the computational cost of the reduction operation of Step 19.

As a final remark we state that it is straightforward to generalize the multiplier of Algorithm 1 so that it can compute field multiplications over finite fields with characteristic $p > 2$.

Multiplicative Inverse We compute the multiplicative inverse of an arbitrary field element $a \in \mathbb{F}_{2^m}$ by implementing the Almost Inverse Algorithm [15, 27], which is a variant of the binary extended Euclid algorithm.

2.2 Characteristic Three Fields \mathbb{F}_{3^m}

Addition and Subtraction In 2002, Galbraith *et al.* [11] showed how to compute additions of two elements $a, b \in \mathbb{F}_3$ using 12 AND, OR, XOR and NOT Boolean functions. That same year, Harrison *et al.* [17] noted that this operation could be computed using only 7 OR and XOR logical instructions. This was considered the minimal number of logical operations for this arithmetic operation until Kawahara *et al.* [21] presented in 2008 an expression that only requires 6 logical instructions. However, our experiments, performed on a multicore processor environment, showed that the expression in [17] consistently yields a shorter computation time than the one associated to the expression in [21]. We believe that this may be due to the fact that the formulation in [21], includes a ANDN instruction³ that in our platform is implemented less efficiently than the logical set of functions proposed by Harrison *et al.* which, as it was mentioned above, only requires OR and XOR logic operations. Hence, we decided to adopt the expression reported in [17], which is briefly describe next. Each coefficient (trit) $a \in \mathbb{F}_3$ can be encoded as $a = 2a_h + a_l$. The addition of two elements $a, b \in \mathbb{F}_3$ can then be computed as [17],

$$t = (a_l|b_h) \oplus (a_h|b_l), \quad c_l = t \oplus (a_h|b_h), \quad c_h = t \oplus (a_l|b_l).$$

As mentioned by the authors of [17], the order in which the above expression is evaluated has a major impact in the performance of its implementation in software. In fact, we use the following equivalent expression for computing $c = a + b$,

$$t = (a_l|a_h) \& (b_l|b_h), \quad c_l = t \oplus (a_l|b_l), \quad c_h = t \oplus (a_h|b_h).$$

Similarly, subtraction in \mathbb{F}_3 can be computed using only 7 instructions as,

$$t = (a_l|a_h) \& (b_l|b_h), \quad c_l = t \oplus (a_l|b_h), \quad c_h = t \oplus (a_h|b_l).$$

Frobenius and Frobenius Inverse Operators Let $f(x)$ be an irreducible polynomial of degree m over \mathbb{F}_3 . Then, the ternary extension field \mathbb{F}_{3^m} can be defined as, $\mathbb{F}_{3^m} \cong \mathbb{F}_3[x]/(f(x))$. Let a be an arbitrary element of that field, which can be written in canonical basis as $a = \sum_{i=0}^{m-1} a_i x^i$, $a_i \in \mathbb{F}_3$. Assume that the extension degree m is an integer of the form $m = 3u + r$, with $u \geq 1$ and $r \in \{0, 1, 2\}$. Then, the Frobenius operator applied to a consists of computing $c = a^3$, which can be obtained as [1],

$$c = a^3 \bmod f(x) = C_0 + x^m C_1 + x^{2m} C_2 \bmod P(x), \quad (3)$$

where, $C_0 = \sum_{i=0}^u a_i x^{3i}$, $C_1 = \sum_{i=1}^{u+r-1} a_{i+u} x^{3i-r}$, and $C_2 = \sum_{i=r}^{u+r-1} a_{i+2u} x^{3i-2r}$.

One can evaluate Eq. (3) by determining the constants x^m and x^{2m} , which are per-field constants. The Frobenius inverse operator of a is computed by

³ The logical function ANDN is defined in MMX and SSE implementations as $x \text{ ANDN } y = x \& \bar{y}$.

determining the unique field element $b \in \mathbb{F}_{3^m}$ such that $b^3 = a$ holds. The element b can be computed as [2],

$$b = \sum_{i=0}^{u-s} a_{3i} x^i + x^{1/3} \cdot \sum_{i=0}^{u+r-2} a_{3i+1} x^i + x^{2/3} \cdot \sum_{i=0}^{u-1} a_{3i+2} x^i \pmod{f(x)}. \quad (4)$$

where $s = 1$ if $r = 0$, and $s = 0$, otherwise. Eq. (4) allows us to compute the Frobenius inverse operator by performing two third-length polynomial multiplications with the per-field constants $x^{1/3}$ and $x^{2/3}$.

Table 1. Pre-computation look-up table when using the comb method for $a \in \mathbb{F}_{3^m}$, $w = 2$.

Entry	Value	Entry	Value
[00]	0	[12]	[10] + [02]
[01]	a	[20]	\sim [10]
[02]	\sim [01]	[21]	\sim [12]
[10]	[01] \ll 1	[22]	\sim [11]
[11]	[10] + [01]		

Multiplier We use here the same comb method discussed previously. As we did in characteristic two, we selected a window size $w = 4$, which in characteristic three means that we have to pre-compute a look-up table containing $3^4 = 81$ entries. Due to the fact that almost half of the entries can be obtained by performing one single logical NOT, pre-computing such look-up table requires a moderate computational effort. As an example, consider the case where we want to build a look-up table for a given element $a \in \mathbb{F}_{3^m}$, with $w = 2$. Then, we have to pre-compute $3^2 = 9$ entries. Table 1 shows how to obtain those 9 elements, where \sim and \ll stand for the logical negation and left shift operations, respectively. As it can be seen in Table 1, 4 out of 9 entries can be computed using logical negation only. Besides these operations, we need to compute two \mathbb{F}_{3^m} additions, one initialization to zero and one assignation of the element a . In the case of $w = 4$, generating the 81-entry look up table requires a computational effort of 40, 36 and 3 logical negations, field additions and left shift operations, respectively.

Multiplicative Inverse In order to compute the multiplicative inverse d of a field element $b \in \mathbb{F}_{3^m}$, namely, $d = b^{-1} \pmod{P}$, we used the ternary variant of the binary extension Euclid algorithm reported in [17].

2.3 Field Arithmetic Implementation Timings

We present in Table 2 a timing performance comparison of our field arithmetic library against the timings reported by Hankerson *et al.* in [16].⁴ In both works, the libraries were executed on a Intel Core2 processor running at 2.4 GHz. It is noticed that our multipliers in characteristic two and three are faster than their counterparts in [16]. However, the field multiplier for 256-bit prime fields reported in [16], easily outperforms all the other four multipliers listed in Table 2.

Table 2. A comparison of field arithmetic software implementations on an Intel Core2 processor (clock frequency: 2.4 GHz). All timings are reported in μs .

	Field	Prime/polynomial	x^p	$\sqrt[x]{x}$	Mult
Hankerson <i>et al.</i> [16]	$\mathbb{F}_{p^{256}}$	256-bit prime Hamming weight 87	–	–	0.129
	$\mathbb{F}_{3^{509}}$	$x^{509} - x^{477} + x^{445} + x^{32} - 1$	0.375	0.500	3.208
	$\mathbb{F}_{2^{1223}}$	$x^{1223} + x^{255} + 1$	0.250	0.208	3.417
This work	$\mathbb{F}_{3^{509}}$	$x^{509} - x^{318} - x^{191} + x^{127} + 1$	0.375	0.406	1.811
	$\mathbb{F}_{2^{1223}}$	$x^{1223} + x^{255} + 1$	0.200	0.312	2.266

3 Pairing Computation on Supersingular Curves in Characteristics Two and Three

In the following, we consider a supersingular elliptic curve E/\mathbb{F}_{p^m} (where $p = 2$ or $p = 3$) with a distortion map ψ . The point at infinity is denoted by \mathcal{O} . Let ℓ be a large prime factor of $N = \#E(\mathbb{F}_{p^m})$, and suppose that the embedding degree of the curve k is larger than 1 and that there are no points of order ℓ^2 in $E(\mathbb{F}_{p^{km}})$. Let $f_{n,P}$ be a family of normalized $\mathbb{F}_{p^{km}}$ -rational function with divisor $(f_{n,P}) = n(P) - ([n]P) - (n-1)(\mathcal{O})$. The modified Tate pairing of order ℓ is a non-degenerate and bilinear pairing given by the map

$$\begin{aligned} \hat{e} : E(\mathbb{F}_{p^m})[\ell] \times E(\mathbb{F}_{p^m})[\ell] &\longrightarrow \mathbb{F}_{p^{km}}^* / (\mathbb{F}_{p^{km}}^*)^\ell \\ (P, Q) &\longmapsto f_{\ell,P}(\psi(Q)). \end{aligned}$$

Note that $\hat{e}(P, Q)$ is defined up to a coset of $(\mathbb{F}_{p^{km}}^*)^\ell$. However, $\mathbb{F}_{p^{km}}^* / (\mathbb{F}_{p^{km}}^*)^\ell$ is cyclic of order ℓ and isomorphic to the group of ℓ -th roots of unity $\mu_\ell = \{u \in \overline{\mathbb{F}}_{p^m}^* : u^\ell = 1\} \subseteq \mathbb{F}_{p^{km}}^*$. Hence, in order to obtain a unique representative, which is desirable for pairing-based protocols, it suffices to raise $f_{\ell,P}(\psi(Q))$ to the $(p^{km} - 1)/\ell$ -th power. This operation is often referred to as *final exponentiation*. We define the reduced modified Tate pairing as $\hat{e}_r(P, Q) = \hat{e}(P, Q)^{(p^{km} - 1)/\ell}$.

⁴ Our library was compiled using the MS Visual Studio 2008SP1 in 64-bit mode, and it was executed on the Windows XP 64 bit SP2 environment.

3.1 Miller's Algorithm

Miller [23,24] proposed the first iterative approach to compute the function $f_{\ell,P}$. By proving the equality of the divisors, he showed that:

$$f_{a+b,P} = f_{a,P} \cdot f_{b,P} \cdot \frac{l_{[a]P,[b]P}}{v_{[a+b]P}},$$

where $l_{[a]P,[b]P}$ is the equation of the line through $[a]P$ and $[b]P$ (or the tangent line if $[a]P = [b]P$), $v_{[a+b]P}$ is the equation of the vertical line through $[a+b]P$, and $f_{1,P}$ is a constant function (usually, $f_{1,P} = 1$). We obtain a double-and-add algorithm for computing the rational function $f_{n,P}$ in $\lfloor \log_2 n \rfloor$ iterations. A nice property of supersingular elliptic curves is that multiplication by p is a relatively easy operation: it involves only a few Frobenius maps and additions over \mathbb{F}_{p^m} (see for instance [3] for details) and a p -ary expansion of ℓ seems perfectly suited to the computation of $f_{\ell,P}(\psi(Q))$.

Several researchers focused on shortening the loop of Miller's algorithm (see for instance [18,19,30] for a comprehensive bibliography). Barreto *et al.* [3] introduced the η_T pairing as "an alternative means of computing the Tate pairing on certain supersingular curves" [26, page 108]. They suggest to compute $\hat{e}_r(P, Q)$ using an order $T \in \mathbb{Z}$ that is smaller than ℓ . Their main result is a lemma giving a method to select T such that $\eta_T(P, Q)$ is a non-degenerate bilinear pairing [3]. In the case of characteristics two and three, they show that one can and hence half the number of basic Miller's iterations by choosing $T = p^m - N$:

$$\eta_T(P, Q) = \begin{cases} f_{T,P}(\psi(Q)) & \text{if } T > 0, \text{ or} \\ f_{-T,-P}(\psi(Q)) & \text{if } T < 0. \end{cases}$$

It is worth noticing that T has a low Hamming weight and the computation of $f_{T,P}$ (or $f_{-T,-P}$) requires $(m+1)/2$ multiplications by p and an addition. It is therefore possible to pre-compute multiples of P by means of Frobenius maps and to parallelize Miller's algorithm on several cores. Multiplications over $\mathbb{F}_{p^{km}}$ are of course necessary to obtain $\hat{e}(P, Q)$ from the partial results computed on each core.

3.2 Reduced Modified Tate Pairing in Characteristic Three

We consider a supersingular curve E/\mathbb{F}_{3^m} with embedding degree $k = 6$ defined by $E : y^2 = x^3 - x + b$, where m is coprime to 6 and $b \in \{-1, 1\}$. According to [3,6], we have:

$$\hat{e}_r(P, Q) = \eta_T \left(\left[-\mu b 3^{\frac{3m-1}{2}} \right] P, Q \right)^{\frac{3^{6m}-1}{N}},$$

where $\mu = 1$ when $m \equiv 1, 11 \pmod{12}$; in all other cases, $\mu = -1$. There are several ways to compute the η_T pairing (see for instance [3,7,8]) and the choice of an algorithm depends on the target architecture. Here, we decided to minimize

the number of arithmetic operations over \mathbb{F}_{3^m} and applied the well-known loop unrolling technique [13] to [7, Algorithm 3] (technical details are provided in Appendix A). This approach allows us to save several multiplications over \mathbb{F}_{3^m} compared to the original algorithm. Final exponentiation is carried out according to [7, 8].

3.3 Reduced Modified Tate Pairing in Characteristic Two

We follow [6, Algorithm 1] to compute $\hat{e}(P, Q)$ on a supersingular curve E/\mathbb{F}_{2^m} with embedding degree $k = 4$ given by $E : y^2 + y = x^3 + x + b$, where $b \in \{0, 1\}$. The reduced modified Tate pairing is defined by [3, 6]:

$$\hat{e}_r(P, Q) = \eta_T([2^m]P, Q)^{\frac{2^{4m}-1}{N}}.$$

Loop unrolling does not allow one to reduce the number of multiplications over \mathbb{F}_{2^m} and Miller's algorithm requires $(m-1)/2$ iterations which can be parallelized on several cores⁵. Final exponentiation consists of raising $\hat{e}(P, Q)$ to the exponent $M = \frac{2^{4m}-1}{N} = (2^{2m}-1) \cdot (2^m+1-\nu 2^{(m+1)/2})$ [3], where $\nu = (-1)^b$ when $m \equiv 1, 7 \pmod{8}$ and $\nu = (-1)^{1-b}$ in all other cases. We perform this operation according to a slightly optimized version of [6, Algorithm 3] (see Appendices B.1 and B.2 for technical details):

- **Raising to the $(2^m + 1)$ -th power.** Raising the outcome of Miller's algorithm to the $(2^{2m} - 1)$ -th power produces an element $U \in \mathbb{F}_{2^{4m}}$ of order $2^{2m} + 1$. This property allows one to save a multiplication over $\mathbb{F}_{2^{4m}}$ when raising U to the $(2^m + 1)$ -th power compared to [6, page 304].
- **Raising to the $2^{\frac{m+1}{2}}$ -th power.** Beuchat *et al.* [8] exploited the linearity of the Frobenius map in order to reduce the cost of successive cubings over $\mathbb{F}_{3^{6m}}$. The same approach can be straightforwardly transposed to characteristic two: raising an element of $\mathbb{F}_{2^{4m}}$ to the 2^i -th power involves $4i$ squarings and at most four additions over \mathbb{F}_{2^m} .

4 Results and Comparisons

We list in Table 3 the timings achieved by our library for low, medium and high security levels (66, 89, and 128 bits, respectively), including the performance obtained when using one, two, and four Intel Core2 processors. Our library was compiled using the MS Visual Studio 2008SP1 in 64-bit mode, and it was executed on the Windows XP 64 bit SP2 environment.

⁵ Shirase *et al.* propose a loop unrolling technique in reference [28] and claim that they reduce the computation time by 14.3%. However, they assume that additions and multiplications by small constants are almost free, and inverse Frobenius maps over \mathbb{F}_{2^m} are m times more expensive than Frobenius maps. Such estimates do not hold in our context (see for instance Table 2) and we did not investigate further the approach by Shirase *et al.*

For comparison purposes, we also include in Table 3, the performance reported by Hankerson *et al.* [16], which is the fastest pairing library that we know of. The work by Grabher *et al.* [12] is also of interest as it is the only pairing library preceding this work that reports a multi-core platform implementation. All the pairing libraries included in Table 3 were implemented on an Intel Core2 processor. Table 4 shows the timings achieved by our library when implemented on an Intel core i7 multi-processor platform running at 2.9 GHz. Finally, in Table 5 we list some of the fastest hardware accelerators for the Tate pairing reported at low, medium and high security levels.

Table 3. Performance comparison of software implementations for pairings on an Intel Core2 processor.

	Curve	Security [bits]	# of cores	Freq. [GHz]	Calc. time [ms]
This work	$E(\mathbb{F}_{3^{97}})$	66	1	2.6	0.15
	$E(\mathbb{F}_{3^{97}})$	66	2	2.6	0.09
	$E(\mathbb{F}_{3^{193}})$	89	1	2.6	0.98
	$E(\mathbb{F}_{3^{193}})$	89	2	2.6	0.55
Hankerson <i>et al.</i> [16]	$E(\mathbb{F}_{p^{256}})$	128	1	2.4	6.25
	$E(\mathbb{F}_{3^{509}})$	128	1	2.4	13.75
	$E(\mathbb{F}_{2^{1223}})$	128	1	2.4	16.25
Grabher <i>et al.</i> [12]	$E(\mathbb{F}_{p^{256}})$	128	1	2.4	9.71
	$E(\mathbb{F}_{p^{256}})$	128	2	2.4	6.01
This work	$E(\mathbb{F}_{3^{509}})$	128	1	2.4	7.59
	$E(\mathbb{F}_{3^{509}})$	128	2	2.4	4.31
	$E(\mathbb{F}_{3^{509}})$	128	4	2.4	3.15
	$E(\mathbb{F}_{2^{1223}})$	128	1	2.4	11.19
	$E(\mathbb{F}_{2^{1223}})$	128	2	2.4	6.72
	$E(\mathbb{F}_{2^{1223}})$	128	4	2.4	4.22

Grabher *et al.* reported in [12] a multicore implementation of the Ate pairing defined over a Barreto-Naehrig (BN) curve [5], when using a 256-bit prime. Since the BN curves have a $k = 12$ embedding degree, this implies a 128-bit security level. As shown in Table 3, our pairing implementation in characteristic three is faster than the prime field pairing library reported in [12].

In Table 4 we report a calculation time for the reduced modified Tate pairing of just 1.87 ms and 3.08 ms for characteristics three and two, respectively. This performance, that was obtained on a two Intel quad-core i7 multi-processor platform, appears to be the fastest pairing timings yet reported.

As shown in Table 3, our pairing library achieved on an Intel Core2 Duo processor at 2.66 GHz, a computation time of 92 μ s and 553 μ s, respectively [25]. Although our software implementation outperforms several hardware architectures previously reported for low levels of security, when we compare our results against the ones in [8, 29], we see that there still exists a large gap between software and hardware pairing implementations for moderate security levels.

The computation of the reduced modified Tate pairing over $\mathbb{F}_{3^{193}}$ on a Virtex-4 LX FPGA reported in [8] with a medium speed grade, is for instance roughly fifty times faster than our software timings. Depending on the application, this speedup may justify the usage of large FPGAs which are now available in servers and supercomputers such as the SGI Altix 4700 platform.

Table 4. Implementations timings for the reduced modified Tate pairing at the 128-bit security level on an Intel core i7 processor (clock frequency: 2.9 GHz).

Curve	# of cores	Calc. time [ms]	Curve	# of cores	Calc. time [ms]
$E(\mathbb{F}_{3^{509}})$	1	5.22	$E(\mathbb{F}_{2^{1223}})$	1	7.94
$E(\mathbb{F}_{3^{509}})$	2	3.16	$E(\mathbb{F}_{2^{1223}})$	2	4.53
$E(\mathbb{F}_{3^{509}})$	4	2.31	$E(\mathbb{F}_{2^{1233}})$	4	3.13
$E(\mathbb{F}_{3^{509}})$	8	1.87	$E(\mathbb{F}_{2^{1223}})$	8	3.08

Table 5. Some hardware accelerators for the Tate pairing.

	Curve	Security [bits]	Platform	Area	Freq. [MHz]	Calc. time [ms]
Shu <i>et al.</i> [29]	$E(\mathbb{F}_{2^{239}})$	66	xc4vlx200	29920 slices	100	0.0365
Beuchat <i>et al.</i> [8]	$E(\mathbb{F}_{3^{97}})$	66	xc4vlx60-11	18683 slices	179	0.0048
Shu <i>et al.</i> [29]	$E(\mathbb{F}_{2^{457}})$	88	xc4vlx200	58956 slices	100	0.1
Beuchat <i>et al.</i> [8]	$E(\mathbb{F}_{3^{193}})$	89	xc4vlx100-11	47433 slices	167	0.01
Shu <i>et al.</i> [29]	$E(\mathbb{F}_{2^{557}})$	96	xc4vlx200	37931 slices	66	0.6758
Kammler <i>et al.</i> [20]	$E(\mathbb{F}_{p^{256}})$	128	130 nm CMOS	97 kGates	338	15.8

5 Conclusion

In this work we presented the multi-core implementation of a software library that is able to compute the reduced modified Tate pairing on supersingular elliptic curves at a high speed.

The sequential timings reported in this work are significantly faster than the ones achieved in [16] for pairings computed over characteristics two and three fields. We would like to interpret these results as a reduction of the gap between prime fields and binary/ternary fields.

In the light of the results obtained here, one important design question that surges is: how many cores should be utilized by a given application? As discussed in the Appendixes, our pairing library successfully parallelize the computation of Miller's algorithm. However, if we use n cores for the implementation of the

Miller's algorithm, in order to combine the n partial products generated by the n parallel sub-loops executed in each core, we are forced to add $n - 1$ extra field multiplications over $\mathbb{F}_{p^{km}}$. Furthermore, due to the dependencies among the different operations involved in the final exponentiation step, this portion of the pairing has to be computed sequentially. As shown in Table 4, these two factors cause the acceleration achieved by an n -core implementation to be always less than the ideal $n \times$ speedup factor. From Table 4, we can see for instance that the acceleration provided by the eight-core implementation is modest compared with the timings achieved by the four-core one. On the other hand, when comparing the timings of the one-core and two-cores implementations, the acceleration factor is roughly $1.70 \times$ for both, characteristics two and three.

Our future work includes the implementation of pairings for large characteristics on ordinary curves and the SSE Implementation of our pairing library using the built-in carry-less 64-bit Multiplier recently announced by Intel [14].

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A Reduced Modified Tate Pairing in Characteristic Three

In the following, we consider the computation of the reduced modified Tate pairing in characteristic three on several cores. Table 6 summarizes the parameters of the supersingular curve. Noting $T' = -\mu bT$ and $P' = (x_{P'}, y_{P'}) = \left[3^{\frac{3m-1}{2}}\right] P$, we have to compute:

$$\begin{aligned} \hat{e}_r(P, Q) &= f_{T', P'}(\psi(Q))^M \\ &= \left(l_{P'}(\psi(Q)) \cdot \left(\prod_{j=0}^{\frac{m-1}{2}} g_{\left[3^{\frac{m-1}{2}-j}\right]_{P'}}(\psi(Q))^{3^j} \right) \right)^M, \end{aligned} \quad (5)$$

where, for all $V \in E(\mathbb{F}_{3^m})[\ell]$, l_V is the equation of the line corresponding to the addition of $[\mu b]V$ with $\left[3^{\frac{m+1}{2}}\right] V$, and g_V is the rational function introduced by Duursma and Lee [9] and having divisor $(g_V) = 3(V) + ([-3]V) - 4(\mathcal{O})$. Expanding everything, we obtain the following expressions:

$$\begin{aligned} l_{P'}(\psi(Q)) &= y_Q \sigma + \lambda y_{P'}(x_{P'} + x_Q - \nu b - \rho), \text{ and} \\ g_{\left[3^{\frac{m-1}{2}-j}\right]_{P'}}(\psi(Q))^{3^j} &= -\lambda y_{P'}^{3^{-j}} y_Q^{3^j} \sigma - \left(x_{P'}^{3^{-j}} + x_Q^{3^j} - \nu b - \rho \right)^2. \end{aligned}$$

It is worth noticing that the Duursma-Lee functions can be pre-computed by building a table of all cube roots of x_P and y_P as well as all cubes of x_Q and y_Q .

Beuchat *et al.* described an algorithm to compute $\hat{e}_r(P, Q)$ according to Eq. (5) (see [7, Algorithm 3]). They took advantage of the sparsity of $l_{P'}$ and g_V to reduce the cost of the first multiplication over \mathbb{F}_{3^m} and needed therefore $\frac{m-1}{2}$ iterations of Miller's algorithm to accumulate the remaining products. We propose to optimize further the algorithm by computing two iterations at a time and obtain Algorithm 2 for the case where $\frac{m-1}{2}$ is even. When $\frac{m-1}{2}$ is odd, one has to restrict the loop on j from 1 to $\frac{m-3}{4}$, and perform the last product by means of an iteration of the original loop. Thanks to the sparsity of g_V , the cost of a double iteration is of 25 multiplications over \mathbb{F}_{3^m} , whereas two iterations of the original loop involve 28 multiplications [7]. The key observation is that the sparse multiplication over $\mathbb{F}_{3^{6m}}$ on line 15 requires only 8 multiplications over \mathbb{F}_{3^m} . Keeping in mind that our SSE implementation of multiplication over \mathbb{F}_{3^m} involves a precomputation step depending on the second operand, we designed Algorithm 3 where several multiplications over \mathbb{F}_{3^m} share a common operand (lines 5 and 6).

Table 6. Supersingular curves over \mathbb{F}_{3^m} (reprinted from [8]).

Underlying field	\mathbb{F}_{3^m} , where m is coprime to 6
Curve	$E : y^2 = x^3 - x + b$, with $b \in \{-1, 1\}$
Number of rational points	$N = \#E(\mathbb{F}_{3^m}) = 3^m + 1 + \mu b 3^{(m+1)/2}$, with $\mu = \begin{cases} +1 & \text{if } m \equiv 1, 11 \pmod{12}, \text{ or} \\ -1 & \text{if } m \equiv 5, 7 \pmod{12} \end{cases}$
Embedding degree	$k = 6$
Distortion map	$\psi : E(\mathbb{F}_{3^m})[\ell] \rightarrow E(\mathbb{F}_{3^{6m}})[\ell] \setminus E(\mathbb{F}_{3^m})[\ell]$ $(x, y) \mapsto (\rho - x, y\sigma)$ with $\rho \in \mathbb{F}_{3^{3m}}$ and $\sigma \in \mathbb{F}_{3^{2m}}$ satisfying $\rho^3 = \rho + b$ and $\sigma^2 = -1$
Tower field	$\mathbb{F}_{3^{6m}} = \mathbb{F}_{3^m}[\rho, \sigma] \cong \mathbb{F}_{3^m}[X, Y]/(X^3 - X - b, Y^2 + 1)$
Final exponentiation	$M = (3^{3m} - 1) \cdot (3^m + 1) \cdot (3^m + 1 - \mu b 3^{(m+1)/2})$
Parameters of Algorithm 2	$\lambda = \begin{cases} +1 & \text{if } m \equiv 7, 11 \pmod{12}, \text{ or} \\ -1 & \text{if } m \equiv 1, 5 \pmod{12}, \text{ and} \end{cases}$ $\nu = \begin{cases} +1 & \text{if } m \equiv 5, 11 \pmod{12}, \text{ or} \\ -1 & \text{if } m \equiv 1, 7 \pmod{12} \end{cases}$

Since lines 9 and 10 of Algorithm 2 do not present dependencies, the pre-computation of the Duursma-Lee functions can be performed in parallel on two cores. Then, one can split the execution of Miller's algorithm (lines 13 to 16) into several parts that are run concurrently.

B Reduced Modified Tate Pairing in Characteristic Two

Table 7 summarizes the parameters of the supersingular curve considered to compute $\hat{e}_r(P, Q)$ in characteristic two. Noting $T' = -\nu T$ and $P' = [-\nu 2^m]P$, we have:

$$\begin{aligned} \hat{e}_r(P, Q) &= f_{T', P'}(\psi(Q))^M \\ &= \left(l_{P'}(\psi(Q)) \cdot \left(\prod_{j=0}^{\frac{m-1}{2}} g_{\left[2^{\frac{m-1}{2}-j}\right]_{P'}}(\psi(Q)^{2^j}) \right) \right)^M, \end{aligned} \quad (6)$$

where, for all $V \in E(\mathbb{F}_{2^m})[\ell]$, l_V is the equation of the line corresponding to the addition of $[\nu]V$ with $\left[2^{\frac{m+1}{2}}\right]V$ and g_V is the rational function defined over $E(\mathbb{F}_{2^{4m}})[\ell]$ corresponding to the straight line in doubling V . More precisely, we

Algorithm 2 Unrolled loop for computing the reduced modified Tate pairing in characteristic three when $\frac{m-1}{2}$ is even.

Input: $P, Q \in E(\mathbb{F}_{3^m})[\ell]$.
Output: $\hat{e}_r(P, Q) \in \mathbb{F}_{3^{6m}}^*$.

1. $x_P \leftarrow \sqrt[3]{x_P} - (\nu + 1)b;$ (1 R, 1 A when $m \equiv 5, 11 \pmod{12}$)
2. $y_P \leftarrow \lambda \sqrt[3]{y_P};$ (1 R)
3. $y_Q \leftarrow -\lambda y_Q;$
4. $t \leftarrow x_P + x_Q;$ (1 A)
5. $R \leftarrow \lambda(y_P t - y_Q \sigma - y_P \rho) \cdot (-t^2 + y_P y_Q \sigma - t \rho - \rho^2);$ (6 M, 1 C, 6 A)
6. $x_P[0] \leftarrow x_P; y_P[0] \leftarrow y_P;$
7. $x_Q[0] \leftarrow x_Q; y_Q[0] \leftarrow y_Q;$
8. **for** $j = 1$ to $\frac{m-1}{2}$ **do**
9. $x_P[j] \leftarrow \sqrt[3]{x_P[j-1]}; x_Q[j] \leftarrow x_Q[j-1]^3;$ (1 R, 1 C)
10. $y_P[j] \leftarrow \sqrt[3]{y_P[j-1]}; y_Q[j] \leftarrow y_Q[j-1]^3;$ (1 R, 1 C)
11. **end for**
12. **for** $j \leftarrow 1$ to $\frac{m-1}{4}$ **do**
13. $t \leftarrow x_P[2j-1] + x_Q[2j-1]; u \leftarrow y_P[2j-1]y_Q[2j-1];$ (1 M, 1 A)
14. $t' \leftarrow x_P[2j] + x_Q[2j]; u' \leftarrow y_P[2j]y_Q[2j];$ (1 M, 1 A)
15. $S \leftarrow (-t^2 + u\sigma - t\rho - \rho^2) \cdot (-t'^2 + u'\sigma - t'\rho - \rho^2);$ (8 M, 13 A)
16. $R \leftarrow R \cdot S;$ (15 M, 67 A)
17. **end for**
18. **return** $R^M;$

have:

$$\begin{aligned} \left(g_{\left[2^{\frac{m-1}{2}-j}\right]_{P'}}(\psi(Q)) \right)^{2^j} &= (x_{P'}^{2^{-j}} + \alpha) \cdot (x_Q^{2^j} + \alpha) + y_{P'}^{2^{-j}} + y_Q^{2^j} + \beta + \\ &\quad (x_{P'}^{2^{-j}} + x_Q^{2^j} + \alpha)s + t, \text{ and} \\ l_{P'}(\psi(Q)) &= g_{\left[2^{\frac{m-1}{2}}\right]_{P'}}(\psi(Q)) + x_{P'}^2 + x_Q + \alpha + s. \end{aligned}$$

Algorithm 4 describes the computation of $\hat{e}_r(P, Q)$ according to Eq. (6) (this algorithm is based on [6, Algorithm 1]). The equations of all straight lines can be pre-computed by storing all square roots of x_P and y_P , as well as all squares of x_Q and y_Q (lines 12 and 13 that can be computed in parallel on two cores). Then, one can split the execution of Miller's algorithm (lines 16 to 20) into several parts that are run concurrently. We perform the final exponentiation according to an improved version of [6, Algorithm 3] detailed in the following.

B.1 Raising an Element of Order $2^{2m} + 1$ to the $(2^m + 1)$ -th Power over $\mathbb{F}_{2^{4m}}$

Let $F, U \in \mathbb{F}_{2^{4m}}$ and assume that $U = F^{2^{2m}-1}$. According to Fermat's little theorem, the result of raising to the $(2^{2m} - 1)$ -th power produces an element of

Algorithm 3 Computation of $(-t^2 + u\sigma - t\rho - \rho^2) \cdot (-t'^2 + u'\sigma - t'\rho - \rho^2)$.

Input: $t, u, t',$ and $u' \in \mathbb{F}_{3^m}$.**Output:** $W = (-t^2 + u\sigma - t\rho - \rho^2) \cdot (-t'^2 + u'\sigma - t'\rho - \rho^2)$.

1. $a_1 \leftarrow t + u; a_2 \leftarrow t' + u';$ (2 A)
 2. $a_3 \leftarrow t + t'; a_4 \leftarrow u + u';$ (2 A)
 3. $m_1 \leftarrow t \cdot t'; m_2 \leftarrow u \cdot u'; m_3 \leftarrow a_1 \cdot a_2;$ (3 M)
 4. $w_3 \leftarrow m_1 + m_2 - m_3;$ (2 A)
 5. $m_4 \leftarrow m_1 \cdot m_1; m_5 \leftarrow m_1 \cdot a_3; m_6 \leftarrow m_1 \cdot a_4;$ (3 M)
 6. $m_7 \leftarrow a_3 \cdot a_3; m_8 \leftarrow a_3 \cdot w_3;$ (2 M)
 7. $w_0 \leftarrow m_4 - m_2 + m_3;$ (2 A)
 8. $w_1 \leftarrow m_6 + m_8;$ (1 A)
 9. $w_2 \leftarrow m_5 + a_3 + b;$ (2 A)
 10. $w_4 \leftarrow m_7 - m_1 + 1;$ (2 A)
 11. $w_5 \leftarrow -a_4;$
 12. **return** $w_0 + w_1\sigma + w_2\rho + w_3\sigma\rho + w_4\rho^2 + w_5\sigma\rho^2;$
-

order $2^{2m} + 1$, i.e. $U^{2^{2m}+1} = 1$. Let us write

$$U = \underbrace{u_0 + u_1 s}_{U_0} + \underbrace{(u_2 + u_3 s)t}_{U_1},$$

where $u_0, u_1, u_2, u_3 \in \mathbb{F}_{2^m}$ and $U_0, U_1 \in \mathbb{F}_{2^{2m}}$. Since $t^{2^{2m}} = 1 + t$, we have:

$$U^{2^{2m}+1} = (U_0 + U_1 t)(U_0 + U_1 t)^{2^{2m}} = U_0^2 + U_0 U_1 + U_1^2 s = 1.$$

Therefore,

$$\begin{aligned} u_0 u_2 + u_1 u_3 &= u_0^2 + u_1^2 + u_3^2 + 1, \text{ and} \\ u_0 u_3 + u_1 u_2 + u_1 u_3 &= u_1^2 + u_2^2. \end{aligned}$$

Let $\alpha = 0$ when $m \equiv 3 \pmod{4}$ and $\alpha = 1$ when $m \equiv 1 \pmod{4}$. Seeing that $s^{2^m} = s + 1$ and $t^{2^m} = t + s + \alpha + 1$, we obtain:

$$U^{2^m} = \begin{cases} (u_0 + u_1 + u_3) + (u_1 + u_2)s + (u_2 + u_3)t + u_3 st & \text{if } \alpha = 1, \\ (u_0 + u_1 + u_2) + (u_1 + u_2 + u_3)s + (u_2 + u_3)t + u_3 st & \text{if } \alpha = 0. \end{cases}$$

A first solution to compute U^{2^m+1} would be to multiply U^{2^m} by U . There is however a faster way to raise U to the power of $2^m + 1$. Defining $m_0 = u_0 u_1$, $m_1 = u_0 u_3$, $m_2 = u_1 u_2$, and $m_3 = u_2 u_3$, we have:

$$\begin{aligned} U^{2^m+1} &= (u_0 u_1 + u_0 u_3 + u_1 u_2 + u_0^2 + u_1^2) + \\ &\quad \underbrace{(u_0 u_2 + u_1 u_3)}_{=u_0^2+u_1^2+u_3^2+1} + u_1 u_2 + u_2 u_3 + u_2^2 + u_3^2) s + \\ &\quad (u_0 u_3 + u_1 u_2 + u_2 u_3 + u_2^2 + u_3^2)t + (u_2 u_3 + u_2^2 + u_3^2)st \\ &= (m_0 + m_1 + m_2 + (u_0 + u_1)^2) + (m_2 + m_3 + (u_0 + u_1 + u_2)^2 + 1)s + \\ &\quad (m_1 + m_2 + m_3 + (u_2 + u_3)^2)t + (m_3 + (u_2 + u_3)^2)st, \end{aligned}$$

Table 7. Supersingular curves over \mathbb{F}_{2^m} .

Underlying field	\mathbb{F}_{2^m} , where m is an odd integer
Curve	$E : y^2 + y = x^3 + x + b$, with $b \in \{0, 1\}$
Number of rational points	$N = 2^m + 1 + \nu 2^{(m+1)/2}$, with $\nu = (-1)^\delta$ and $\delta = \begin{cases} b & \text{if } m \equiv 1, 7 \pmod{8}, \\ 1 - b & \text{if } m \equiv 3, 5 \pmod{8}, \end{cases}$
Embedding degree	$k = 4$
Distortion map	$\psi : E(\mathbb{F}_{2^m})[\ell] \rightarrow E(\mathbb{F}_{2^{4m}})[\ell] \setminus E(\mathbb{F}_{2^m})[\ell]$ $(x, y) \mapsto (x + s^2, y + sx + t)$ with s and $t \in \mathbb{F}_{2^{4m}}$ satisfying $s^2 = s + 1$ and $t^2 = t + s$
Tower field	$\mathbb{F}_{2^{4m}} = \mathbb{F}_{2^m}[s, t] \cong \mathbb{F}_{2^m}[X, Y]/(X^2 + X + 1, Y^2 + Y + X)$
Final exponentiation	$M = (2^{2m} - 1) \cdot (2^m + 1 - \nu 2^{(m+1)/2})$
Parameters of Algorithm 4	$\alpha = \begin{cases} 0 & \text{if } m \equiv 3 \pmod{4}, \text{ or} \\ 1 & \text{if } m \equiv 1 \pmod{4}, \text{ and} \end{cases}$ $\beta = \begin{cases} b & \text{if } m \equiv 1, 3 \pmod{8}, \text{ or} \\ 1 - b & \text{if } m \equiv 5, 7 \pmod{8} \end{cases}$

when $\alpha = 1$, and

$$\begin{aligned}
 U^{2^m+1} &= (\underbrace{u_0u_2 + u_1u_3}_{=u_0^2+u_1^2+u_3^2+1} + u_0u_1 + u_1u_2 + u_0^2 + u_1^2) + \\
 &\quad (\underbrace{u_0u_2 + u_1u_3}_{=u_0^2+u_1^2+u_3^2+1} + u_0u_3 + u_2u_3 + u_2^2 + u_3^2)s + \\
 &\quad (u_0u_3 + u_1u_2)t + (u_2u_3 + u_2^2 + u_3^2)st \\
 &= (m_0 + m_2 + u_3^2 + 1) + (m_1 + m_3 + (u_0 + u_1 + u_2)^2 + 1)s + \\
 &\quad (m_1 + m_2)t + (m_3 + u_2^2 + u_3^2)st,
 \end{aligned}$$

when $\alpha = 0$. Thus, computing U^{2^m+1} involves only four multiplications, three squarings, and eleven additions over \mathbb{F}_{2^m} (Algorithm 5). This approach allows us to save one multiplication over \mathbb{F}_{2^m} compared to [6].

B.2 Computing $U^{2^{\frac{m+1}{2}}}$ over $\mathbb{F}_{2^{4m}}$

Let $U = u_0 + u_1s + u_2t + u_3st \in \mathbb{F}_{2^{4m}}$. Noting that $s^{2^i} = s + i$ and $t^{2^i} = t + is + \lfloor \frac{i \bmod 4}{2} \rfloor$, we obtain the following formulae for U^{2^i} , depending on the

Algorithm 4 Computation of the reduced modified Tate pairing in characteristic two.

Input: $P, Q \in \mathbb{F}_{2^m}[\ell]$.

Output: $\hat{e}_r(P, Q) \in \mathbb{F}_{2^{4m}}^*$.

1. $x_P \leftarrow x_P + 1$; (1 XOR)
 2. $y_P \leftarrow x_P + y_P + \alpha + \bar{\delta}$; ($\alpha + \bar{\delta}$ XOR, 1 A)
 3. $u \leftarrow x_P + \alpha$; $v \leftarrow x_Q + \alpha$ (2α XOR)
 4. $g_0 \leftarrow u \cdot v + y_P + y_Q + \beta$; (1 M, 2 A, β XOR)
 5. $g_1 \leftarrow u + x_Q$; $g_2 \leftarrow v + x_P^2$; (1 S, 2 A)
 6. $G \leftarrow g_0 + g_1 s + t$;
 7. $L \leftarrow (g_0 + g_2) + (g_1 + 1)s + t$; (1 A, 1 XOR)
 8. $F \leftarrow L \cdot G$; (2 M, 1 S, 5 A, 2 XOR)
 9. $x_P[0] \leftarrow x_P$; $y_P[0] \leftarrow y_P$;
 10. $x_Q[0] \leftarrow x_Q$; $y_Q[0] \leftarrow y_Q$;
 11. **for** $j = 1$ to $\frac{m-1}{2}$ **do**
 12. $x_P[j] \leftarrow \sqrt{x_P[j-1]}$; $x_Q[i] \leftarrow x_Q[i-1]^2$; (1 R, 1 S)
 13. $y_P[j] \leftarrow \sqrt{y_P[j-1]}$; $y_Q[i] \leftarrow y_Q[i-1]^2$; (1 R, 1 S)
 14. **end for**
 15. **for** $j = 1$ to $\frac{m-1}{2}$ **do**
 16. $u \leftarrow x_P[j] + \alpha$; $v \leftarrow x_Q[j] + \alpha$ (2α XOR)
 17. $g_0 \leftarrow u \cdot v + y_P[j] + y_Q[j] + \beta$; (1 M, 2 A, β XOR)
 18. $g_1 \leftarrow u + x_Q[j]$; (1 A)
 19. $G \leftarrow g_0 + g_1 s + t$;
 20. $F \leftarrow F \cdot G$; (6 M, 14 A)
 21. **end for**
 22. **return** F^M ;
-

value of i modulo 4:

$$U^{2^i} = \begin{cases} u_0^{2^i} + u_1^{2^i} s + u_2^{2^i} t + u_3^{2^i} st & \text{when } i \equiv 0 \pmod{4}, \\ (u_0 + u_1 + u_3)^{2^i} \\ \quad + (u_1 + u_2)^{2^i} s + (u_2 + u_3)^{2^i} t + u_3^{2^i} st & \text{when } i \equiv 1 \pmod{4}, \\ (u_0 + u_2)^{2^i} + (u_1 + u_3)^{2^i} s + u_2^{2^i} t + u_3^{2^i} st & \text{when } i \equiv 2 \pmod{4}, \\ (u_0 + u_1 + u_2)^{2^i} + (u_1 + u_2 + u_3)^{2^i} s \\ \quad + (u_2 + u_3)^{2^i} t + u_3^{2^i} st & \text{when } i \equiv 3 \pmod{4}. \end{cases}$$

According to the value of $(m+1)/2 \pmod{4}$, the computation of $U^{2^{\frac{m+1}{2}}}$ requires $2m+2$ squarings and at most four additions over \mathbb{F}_{2^m} .

Algorithm 5 Computation of U^{2^m+1} over $\mathbb{F}_{2^{4m}}$, where U is an element of order $2^{2m} + 1$.

Input: $U = u_0 + u_1s + u_2t + u_3st \in \mathbb{F}_{2^{4m}}$ with $U^{2^{2m}+1} = 1$.

Output: $V = U^{2^m+1}$.

1. $m_0 \leftarrow u_0 \cdot u_1; m_1 \leftarrow u_0 \cdot u_3; m_2 \leftarrow u_1 \cdot u_2; m_3 \leftarrow u_2 \cdot u_3;$ (4 M)
 2. $a_0 \leftarrow u_0 + u_1; a_1 \leftarrow a_0 + u_2;$ (2 A)
 3. $s_1 \leftarrow a_1^2;$ (1 S)
 4. **if** $\alpha = 1$ **then**
 5. $a_2 \leftarrow u_2 + u_3; a_3 \leftarrow m_1 + m_2;$ (2 A)
 6. $s_0 \leftarrow a_0^2; s_2 \leftarrow a_2^2;$ (2 S)
 7. $v_3 \leftarrow m_3 + s_2;$ (1 A)
 8. $v_2 \leftarrow v_3 + a_3;$ (1 A)
 9. $v_1 \leftarrow m_2 + m_3 + s_1 + 1;$ (3 A)
 10. $v_0 \leftarrow m_0 + a_3 + s_0;$ (2 A)
 11. **else**
 12. $s_0 \leftarrow u_2^2; s_2 \leftarrow u_3^2;$ (2 S)
 13. $v_0 \leftarrow m_0 + m_2 + s_2 + 1;$ (3 A)
 14. $v_1 \leftarrow m_1 + m_3 + s_1 + 1;$ (3 A)
 15. $v_2 \leftarrow m_1 + m_2;$ (1 A)
 16. $v_3 \leftarrow m_3 + s_0 + s_2;$ (2 A)
 17. **end if**
 18. **Return** $v_0 + v_1s + v_2t + v_3st;$
-