

Proofs of Space: When Space is of the Essence

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Abstract. Proofs of computational effort were devised to control denial of service attacks. Dwork and Naor (CRYPTO '92), for example, proposed to use such proofs to discourage spam. The idea is to couple each email message with a proof of work that demonstrates the sender performed some computational task. A proof of work can be either CPU-bound or memory-bound. In a CPU-bound proof, the prover must compute a CPU-intensive function that is easy to check by the verifier. A memory-bound proof, instead, forces the prover to access the main memory several times, effectively replacing CPU cycles with memory accesses.

In this paper we put forward a new concept dubbed *proof of space*. To compute such a proof, the prover must use a specified amount of space, i.e., we are not interested in the number of accesses to the main memory (as in memory-bound proof of work) but rather on the amount of actual memory the prover must employ to compute the proof. We give a complete and detailed algorithmic description of our model. We develop a full theoretical analysis which uses combinatorial tools from Complexity Theory (such as pebbling games) which are essential in studying space lower bounds.

Keywords: Space Complexity, Proof of Work, Pebbling Game, Random Oracle Model.

1 Introduction

Space has a special meaning in Computer Science. It refers to the number of cells of the working tape used by a Turing Machine (TM). While a TM computes a function, it will make several steps (relevant to time complexity) and use a certain number of tape cells (relevant to space complexity).

In [15], Dwork and Naor proposed to employ proof of work (PoW) to discourage spam and, in general, to hinder denial of service attacks. Before any action (such as sending an email), the prover must perform some work and generate a proof of it that can be efficiently verified. Proofs of work are currently being used to implement a publicly verifiable ledger for Bitcoin, where transactions are registered and verified by a community of users to avoid the double-spending problem [28]. The work performed by the prover can be CPU-bound, in which the work represents the number of steps made by a TM, or memory-bound, in which the work represents the times a TM access the working tape. The motivation behind memory-bound PoW is that, while CPU speed may differ significantly among distinct platforms, memory latencies vary much less across machines and may prove to be more equitable and egalitarian. We stress that memory-bound function complexity measures the number of memory accesses and does not take into account the actual amount of memory employed. That is, a TM may read and mark a single cell several times to reach a certain complexity level but it will still end up using only one cell.

In this work we define the notion of *Proof of Space* (PoSpace). PoSpace forces the prover to use at least a specified amount of memory. This means, for instance, that a TM must now use a predetermined number of distinct tape cells to be able to respond to a challenge. We will show that our PoSpace construction is also a memory-bound PoW under the definition provided in [14, 16], while in general a PoW cannot be a PoSpace under our definition. The state of the art memory-bound PoW was described in [16] by Dwork, Goldberg, and Naor. Their scheme requires both the prover and the verifier to store a large table T but they devised ways to mitigate this problem via either hash trees or public-key signatures. We view PoSpace as a valid alternative to various flavors of PoWs. In PoSpace the spotlight is turned on the amount of space rather than on CPU cycles or memory accesses as in PoWs. In addition, PoSpace solves problems where PoW is not applicable. For instance, we believe PoSpace can be employed in forensic analysis or device attestation to confirm remotely that an embedded device has been successfully wiped. That is, a remote device could be instructed to respond to a wipe command with PoSpace as evidence that its functional memory is now overwritten.

Straw Man Solutions. Memory-bound functions were first introduced by Abadi et al. [1]. In the main construction of memory-bound PoW given in [14], both the prover and the verifier share a large random table T . The prover must compute a function by making several memory accesses to uniformly random positions in T . With proper parameters, the prover is forced to reserve an amount of memory which is proportional to S . In another construction, the authors of [14] show that the verifier does not have to store T . The idea is to sign all pairs $(i, T[i])$ and then challenge the prover on ℓ positions of T . The prover will return the ℓ values $T[i]$ along with an aggregate signature that can be checked by the verifier to ensure the prover is holding the table T .

We first remark that it is possible to harness recent advances in proof of storage schemes, such as PDP [4] and POR [32], to reduce the message complexity from $O(\ell)$ to essentially constant. This solution improves upon the one in [14] and, as long as the initialization phase is performed only once, would meet our *efficiency* requirements for PoSpace. However, proof of storage does not satisfy our definition of PoSpace since the running time of the verifier depends on the size of T . The only way to avoid linear dependency is to run a PDP-based scheme with spot checking [4], but then the prover either must use more space than required or won't access all the memory locations. Intuitively, the reason why a solution with proof of storage will not work rests upon the interpretation of what *proof of space* really means. Proof of storage applied to our context satisfies the notion that “*the prover can access space*”. PoSpace instead captures the stronger notion that “*the prover can handle space*”, i.e., the prover possesses, controls, and manipulates space directly. In particular, we distinguish between a prover that can only read memory and a prover that can read and write memory. This is important because, among other things, write operations cannot be parallelized within classical computer architectures. We will provide a formal definition later and make this intuition rigorous.

We also remark that the adversarial model considered in [1, 14] contemplates the existence of a small but fast cache memory that must be saturated to force the prover to dispense with the cache and use traditional RAM memory. Thus, the constructions in [1, 14, 16] do provide a form of proof of space where the space coincides with the cache memory. But, as for proof-of-storage schemes, these schemes satisfy the weaker notion of PoSpace where the prover can only read memory.

Other Related Work. Proof of work is also known as *cryptographic puzzle* in the computer security literature. Puzzles were devised to improve on the proposal by Back [6] and employed to thwart denial of service attacks. In particular, it is important to make them hard to precompute (see [24] and references therein). Waters et al. [34] suggest to outsource the creation of puzzles to an external secure entity. Abliz and Tznati [2] introduce the concept of network-bound puzzles where clients collect tokens from remote servers before querying the service provider. They argue that network latency provides a good solution to the resource disparity problem.

All solutions above deal with proof of effort and cannot be adapted to prove possession of space in the way it is meant and defined in this paper.

Litecoin (litecoin.org) is a variant of Bitcoin that employs *scrypt* [29] to certify a public ledger. *scrypt* is defined as a sequential memory-hard function and originally designed as a key derivation function, but it is used as a proof of effort in litecoin to hinder the use of specialized hardware. Technically, *scrypt* is not a memory-bound function as defined in [1] since no lower bound on the memory used by the function can be guaranteed. Thus, it is not even a PoSpace. We also note it requires both the prover and the verifier to dedicate a possibly large amount of memory, while ideally only the prover should reserve and use actual memory (as in our construction to be presented later).

Dziembowski et al. [20] have independently suggested a notion of proof of space. Their original construction generalizes the hash-based PoW of Cash [6] and does not employ the pebbling framework of [16] (cf. Appendix A of [17]). A major overhaul version of their paper later appeared on the IACR Crypto Eprint repository [17], along with ours [3]. Their new version does use pebbling and adopts techniques

similar to ours. More in details, their constructions corresponds to a weak **PoSpace** (**wPoSpace**) in our definition, they propose two different family of graphs: the first one is exactly the same of ours, the second one is a very elegant construction of graphs that allows more efficient protocol in term of “*space gap*”. The space gap for a proof of space is defined as the ratio between the actual space needed by an honest prover to carry on the protocol and the amount of space proved. A naive implementation for our protocol has space gap $O(\log S)$ while their second construction has space gap $O(1)$. It is possible to implement the prover algorithm in our construction in order to have space gap $O(1)$. However, in the context of **wPoSpace**, this implementation is more inefficient in term of time complexity respect to the construction given in [17]. We recall that PDP [4] and POR [32] can already be used to give **wPoSpace** with the same level of efficiency both for space gap and time complexity of [17], however the construction in [17] is more general since it doesn’t need any setup assumptions. We notice that in the context of **PoSpace**, our construction and (an adapted version of) the second construction in [17] have essentially the the same efficiency in term of both space gap and time complexity. In the end, both works are of interest in their own right.

General ideas behind our protocol. We cast **PoSpace** in the context of delegation of computation, where a *delegator* outsources to a *worker* the computation of a function on a certain input. Securely delegating computation is a very active area [11,12,21,23] thanks also to the popularity of cloud computing where weak devices use the cloud to compute heavy functions. In the case of **PoSpace**, a function f is first selected satisfying the property that there exists a space lower bound for any TM computing it. Then, the verifier chooses a random input x and delegates to the prover the computation of $f(x)$. Specifically, we will turn to the class of functions derived from the “*graph labeling problem*” already used in cryptography (see for example [16,18,19]). Important tools in delegation of computation are interactive proof (and argument) systems. The delegation problem, as described in [27], can be solved as follows: The worker computes $y := f(x)$ and proves using an interactive proof system that indeed $y = f(x)$. While interactive proofs with statistical soundness and non-trivial savings in verification time are unlikely to exist [8,9,22], Kilian showed [25] that proof systems for **NP** languages with only computational soundness (i.e., argument systems [7]) and *succinctness*³ do exist. The construction in [25] relies on Merkle trees and PCP proofs. However, in the context of **PoSpace**, the PCP machinery is an overkill. In fact, the prover does not have to prove the statement $f(x) = y$, but only that a space-consuming computation was carried out. Therefore, we replace the PCP verification with an ad-hoc scheme that results in a very efficient overall construction (while PCP-based constructions are notoriously impractical).

Our Contributions. We introduce a formal definition of Proof of Space (**PoSpace** Definition 2) capturing the intuitive idea of proving to be able to handle (read/write) at least a specified amount of space. We provide two **PoSpace** protocols in the ROM. In addition, we provide a weaker form of **PoSpace** (**wPoSpace**) and prove that it is indeed a separate notion. Most of previous work on proof of storage [4,5,32] and on

³ I.e., the total amount of communication and the verification time are both less than their respective values required to transmit and check the NP-witness.

memory-bound PoW, as defined in [1, 14, 16], can somehow be adapted to meet this weaker definition.

Structure of the paper. Section 2 contains some preliminary definitions. Section 3 contains the formal definition of PoSpace and provide two PoSpace protocols. Section 5 contains the definition of a *weak* variant of PoSpace that captures read-only provers and a separation result between PoSpace and this weaker variant.

Open Problems. It is not clear if in general PoSpace implies the standard definition of PoW in the sense of [15]. The major barrier in proving a positive result resides in showing Non-Amortizability. Roughly speaking, non-amortizable means that the “price” of computing the function for l different inputs is comparable to l times the “price” of computing the function once. In the context of PoWs the “price” is measured in terms of computation time. Technically, the naive idea is that a PoSpace prover for S space as a computation time proportional to S , thus we would like to reduce an adversary for l different protocol executions to an adversary for a single execution which spends less than S computational time. The point is that in order to carry on l executions the prover needs S space, because the space is reusable, and it is not sure that it has used $l \times S$ computational time, because it could be the case that something already computed for one instance can be used to compute the proof for another instance. On the other hand the second protocol we present can be adapted to merge the definition of PoW. The point is that the protocol uses the Random Oracle thus it is easy to show that two instance of the protocol are uncorrelated. In the authors’ opinion in order to show a negative result one has to come out with a PoSpace in the standard model which anyway seems very hard to achieve.

2 Notations and Preliminary Definitions

Graph Notation. Given a directed acyclic graph (DAG) G , the set of successors and predecessors of v in G are respectively $\Gamma^+(v)$ and $\Gamma^-(v)$. We will implicitly assume a topological ordering on its vertex set and a boolean encoding of the vertices respecting that ordering. If $\Gamma^+(v) = \emptyset$ then v is an *output-node* and $\mathbf{T}(G)$ is the set of all output-nodes of G . Analogously if $\Gamma^-(v) = \emptyset$ then v is an *input-node* and $\mathbf{S}(G)$ is the set of all input-nodes of G .

Sampling, Interactive Execution, and Space. Let \mathcal{A} be a probabilistic TM. We write $y \leftarrow \mathcal{A}(x)$ to denote y sampled from the output of \mathcal{A} on input x . Moreover, given $\sigma \in \{0, 1\}^*$, we write $y := \mathcal{A}(x; \sigma)$ to denote the output of \mathcal{A} on input x fixing the random coins to be σ . We write PPT for the class of probabilistic polynomial time algorithms. Let \mathcal{A}, \mathcal{B} be two probabilistic interactive TMs. An interactive joint execution between \mathcal{A}, \mathcal{B} on common input x is specified via the next message function notation: let $b_1 \leftarrow \mathcal{B}(x)$, $a_i \leftarrow \mathcal{A}(x, b_1, \dots, b_i)$, and $b_{i+1} \leftarrow \mathcal{B}(x, a_1, \dots, a_i)$, then $\langle \mathcal{A}, \mathcal{B} \rangle(x)$ denotes a joint execution of \mathcal{A} and \mathcal{B} on common input x . If the sequence of messages exchanged is $(b_1, a_1, \dots, b_k, a_k, b_{k+1})$ we say that k is the number of *rounds* of that joint execution and we denote with $\langle \mathcal{A}, \mathcal{B} \rangle(x) = b_{k+1}$ the last output of \mathcal{B} in that joint execution of \mathcal{A} and \mathcal{B} on common input x .

With $\text{Space}(\mathcal{A}, x)$, we denote the maximal amount of space used by the deterministic TM \mathcal{A} on input x without taking into account the length of the input and output. More formally, we say that $\mathcal{A}(x)$ has space s (or $\text{Space}(\mathcal{A}, x) = s$) if and only if at most s locations on \mathcal{A} 's work tapes (excluding the input and the output tapes) are ever written by \mathcal{A} 's head during its computation on x . In the case of probabilistic TM \mathcal{A} , the function $\text{Space}(\mathcal{A}, x)$ is a random variable that depends on \mathcal{A} 's randomness.

Similarly, given two interactive TMs \mathcal{A} and \mathcal{B} , we can define the space occupied by \mathcal{A} during a joint execution on common input x as follows. Let $(b_1, a_1, \dots, b_k, a_k, b_{k+1})$ the sequence of messages exchanged during $\langle \mathcal{A}, \mathcal{B} \rangle(x)$, then:

$$\text{Space}_{\mathcal{A}}(\langle \mathcal{A}, \mathcal{B} \rangle, x) := \max_{i=1, \dots, n} \{\text{Space}(\mathcal{A}, x, b_1, \dots, b_i)\}$$

As before, if \mathcal{A} and \mathcal{B} are probabilistic interactive TM then the function $\text{Space}_{\mathcal{A}}(\langle \mathcal{A}, \mathcal{B} \rangle, x)$ is a random variable that depends on the randomness of both \mathcal{A} and \mathcal{B} . For simplicity, when the inputs of \mathcal{A} (or $\langle \mathcal{A}, \mathcal{B} \rangle$) are clear from the context, we write $\text{Space}(\mathcal{A})$ (or $\text{Space}_{\mathcal{A}}(\langle \mathcal{A}, \mathcal{B} \rangle)$).

Merkle Trees. Merkle Trees (MT) are classical tools in cryptography. A MT enables a party to succinctly commit itself to a string $\mathbf{l} = (l_1, \dots, l_n)$ with $l_i \in \{0, 1\}^k$.

The term ‘‘succinctly’’ here means that the MT-commitment has size k which is independent respect to n the size of \mathbf{l} . In a later stage, when a party opens the MT-commitment, it is guaranteed that the ‘‘opening’’ can yield only the string \mathbf{l} committed before (the binding property). Moreover \mathbf{l} can be succinctly opened location-by-location: the party can open l_i for any i giving the certificate attesting the value of l_i . Here we use succinctly to mean that the ‘‘opening’’, i.e. the certificate, has size $\log n \times k$ (sub-linear in n) respect to the size of \mathbf{l} .

Abstractly, we define a Merkle Tree as a tuple of three algorithms (Gen_{CRH} , MT , Open) where the first algorithm is a key generation algorithms for a collision resistance hash (CRH) function. Suppose that Gen_{CRH} outputs a key s . Then MT takes as input s and a sequence of strings \mathbf{l} and outputs the *commitment* C for \mathbf{l} , i.e., $C = \text{MT}_s(\mathbf{l})$. Open , takes as input the key s , a sequence of strings \mathbf{l} , an index i (this is denoted as $\text{Open}_s(\mathbf{l}, i)$) and outputs the string l_i within the *opening for the i -th element in the sequence \mathbf{l}* .

Usually, the term ‘‘commitment’’ refers to a scheme that is both *hiding* (i.e., the receiver cannot infer any knowledge on \mathbf{l} from the commitment) and *binding*. The MT scheme that we use does not provide the *hiding* property but we still refer to it as a commitment.

A full description of the Merkle Tree is deferred to Appendix E.

Pebbling Games with wildcards. The following definition of *pebbling game with wildcards* is a modification of the standard definition of pebbling game that can be found, for instance, in [26]. Given a DAG $G = (V, E)$, we say that a sequence $\mathcal{P} = (P_0, \dots, P_T)$ of subsets of V is a *pebbling sequence on G with m wildcards* if and only if $P_0 = \emptyset$ and there exists a set $W \subseteq V$ of size m such that, for each $i \in \{1, \dots, T\}$, exactly one of the following holds:

- $P_i = P_{i-1} \cup \{v\}$ if $\Gamma^-(v) \subseteq P_{i-1} \cup W$ (*pebbling*) or

- $P_i \subseteq P_{i-1}$ (*unpebbling*).

If a set of vertexes Γ is such that $\Gamma \subseteq \bigcup_{i=0}^T P_i$, we say that \mathcal{P} *pebbles* Γ . If \mathcal{P} pebbles $\mathbf{T}(G)$ then we say that \mathcal{P} is a *pebbling game on G with m wildcards*. Moreover we say that the *pebbling time* of \mathcal{P} is T and the *pebbling space* of \mathcal{P} is $\max_i |P_i|$. Intuitively, a pebbled node is a node for which we have made some computations. Instead, W represents *complimentary* nodes, for which we have made no computations.

One of the main ingredients for the correctness of our constructions is the the Pebbling Theorem [26] that proves that stacks of *superconcentrators* graphs (Pip-penger [30]) have exponential pebbling space-time trade off.

Theorem 1 (Pebbling Theorem). *There exist a family of efficiently sampleable directed acyclic graphs $\{G_{N,k}\}_{k,N \in \mathbb{N}}$ with constant fan-in d , N input nodes, N output nodes and $kN \log N$ nodes in total, such that:*

1. *Any pebbling sequence that pebbles Δ output nodes has pebbling space at most S pebbles and m wildcards, where $|\Delta| \geq 4S + 2m + 1$, has pebbling time T such that*

$$T \geq |\Delta| \left(\frac{N - 2S - m}{2S + m + 1} \right)^k.$$

2. *There exists a pebbling sequence that pebbles the graph $G_{N,k}$ has pebbling space $N + 2$ and needs time $O(kN \log N)$.*
3. *For any node $v \in V(G_{N,k})$, the incoming nodes of v and the position of v in first lexicographic topologically order of $G_{N,k}$ are computable in $O(k \log N)$.*

More detail about the construction and the proof of this theorem are given in Appendix A.

Graph Labeling Problem with faults. We adopt the paradigm where the action of pebbling a node in a DAG G is made equivalent to the action of having calculated some labeling on it. This paradigm was introduced in [16] and also recently used in [18, 19]. We make use of a Random Oracle (RO) \mathcal{H} to build a labeling on G according to the pebbling rules.

Definition 1 (\mathcal{H} -labeling with faults). *Given a DAG G with a fixed ordering of the nodes and a random oracle function \mathcal{H} , we say that $\ell : V(G) \rightarrow \{0, 1\}^*$ is a (partial) \mathcal{H} -labeling of G with m faults if and only if there exists a set $M \subseteq V(G)$ of size m such that for each $v \in V(G) \setminus M$*

$$\ell(v) := \mathcal{H}(v \parallel \ell(v_1) \parallel \dots \parallel \ell(v_d)) \text{ where } \{v_1, \dots, v_d\} = \Gamma^-(v). \quad (1)$$

Given a label ℓ and a node v we say that ℓ well-label v if only if the equation (1) holds for ℓ and v .

Our framework generalize the paradigm of [16] by introducing the concept of “faults”. As it is shown in Section 3.1, dealing with “faults” is necessary because an adversary challenged on a labeling function could cheat by providing an inconsistent label on some nodes (which, indeed, are then referred to as “faults”).

The use of a random oracle \mathcal{H} provides two important benefits to our construction: First, given many instances $\mathcal{H}(x_1), \dots, \mathcal{H}(x_k)$, the compression rate of $\mathcal{H}(x)$ is still very poor; second, in order to \mathcal{H} -label the graph, any TM must follow a pebble strategy. In particular, to label a node v , a TM must necessarily calculate and store the label values of all the predecessors $\ell(v_1), \dots, \ell(v_d)$ of the node v . If the graph G needs at least S pebbles to be pebbled efficiently in a pebbling game, then a TM needs to store at least S labels (i.e., RO outputs) to compute an \mathcal{H} -labeling of G . This general strategy is proven sound in [16] and referred to as the *Labeling Lemma*. In our context, however, we provide m degrees of freedom and, given a partial \mathcal{H} -labeling ℓ of G with m faults and a \mathcal{H} -labeling ℓ' of G , it will likely be the case that $\ell(v) \neq \ell'(v)$ for each node v that is a descendant of a not well-labeled node. For this reason, we must state a more general version of the Labeling Lemma in [16] and its proof is provided in Appendix B.

Lemma 1 (Labeling Lemma). *Given a DAG G with degree d and a TM \mathcal{A} with advice h and access to a random oracle $\mathcal{H} : \{0, 1\}^* \rightarrow \{0, 1\}^k$ computing a \mathcal{H} -labeling ℓ with m faults of G . If h is independent of \mathcal{H} , with overwhelming probability, there exists a pebbling sequence $\mathcal{P} = (P_1, \dots, P_T)$ for the DAG G with m wildcards having pebbling space S such that:*

- $S \leq \frac{1}{k} \text{Space}(\mathcal{A}) + d$,
- $T \leq (d + 2)\sigma$, where σ is the number of queries of \mathcal{A} to \mathcal{H} .

In particular T is a lower bound for the execution time of \mathcal{A} .

3 Proof-of-Space Protocols

In this section we define the notion of **PoSpace**, then we provide two constructions that meet the definition. We later define a second notion of a *weak* form of **PoSpace** and show a separation result between the two notions. In our definition below, we allow the adversary to access extra information to model the case in which the adversary may outsource storage and computation to an external provider.

We model the write permission on the storage by providing a precomputation phase to the adversary. That is, the adversary can use as much space as needed to produce an hint. The hint, that may depends on the public parameters of **PoSpace**, can be read during the interactive phase (i.e., when the protocol is started). In contrast, **wPoSpace** does not provide the adversary with a precomputation phase.

Definition 2 (PoSpace). *Suppose we are given $\Sigma = (\text{Gen}, \text{P}, \text{V})$, where Gen is a PPT TM, P and V are interactive PPT TMs and $k \in \mathbb{N}$ is a security parameter. Suppose that the following points hold :*

- (Completeness)** *For all $\text{pk} \in \text{Gen}(1^k)$ and for all $S \in \mathbb{N}$, $S > k$ it holds $\langle \text{P}, \text{V} \rangle(\text{pk}, 1^S) = 1$, time complexity of P is $O(\text{poly}(k, S))$;*
- (Succinctness)** *For all $S \in \mathbb{N}$, $S > k$ the time and space complexity of V and the message complexity of $\langle \text{P}, \text{V} \rangle$, as functions of k and S , are $O(\text{poly}(k) \cdot \text{poly} \log S)$;*
- (Soundness)** *For any PPT adversary \mathcal{A} and for any PPT TM with advice \mathcal{A}' such that $\text{pk} \leftarrow \text{Gen}(1^k)$ and $h \leftarrow \mathcal{A}'(\text{pk}, 1^S)$, the following event:*

$$\text{Snd}_{\Sigma, S}^{\mathcal{A}, \mathcal{A}'}(k) := \langle \mathcal{A}(h), \text{V} \rangle(\text{pk}, 1^S) = 1 \wedge \text{Space}_{\mathcal{A}}(\langle \mathcal{A}(h), \text{V} \rangle, \text{pk}, 1^S) < S$$

has negligible probability (as a function of k) for all $S \in \mathbb{N}$, $S > k$.

Then, we say that $\Sigma = (\text{Gen}, \text{P}, \text{V})$ is a Proof of Space (PoSpace). To be concise, we could say informally that \mathcal{A} wins when the event $\exists S \in \mathbb{N} : \mathbf{Snd}_{\Sigma, S}^{\mathcal{A}, \mathcal{A}'}(k)$ occurs.

Notice that in the completeness part we set just a very mild upper bound on the space complexity of P . This is done on purpose to allow comparison among different PoSpace protocols. In particular a useful measure on a PoSpace protocol $(\text{Gen}, \text{P}, \text{V})$ is the following *space gap*, the ratio $\text{Space}(\text{P}(\text{pk}, 1^S))/S$.

Notice that \mathcal{A}' is a space-unbounded PPT TM that models the fact that there might be information that can be efficiently computed that the space-bounded adversary \mathcal{A} can exploit somehow to compromise PoSpace.

It is easy to see that the PoSpace definition implicitly provides *sequential composability*. In fact, the adversary \mathcal{A}' gives to \mathcal{A} a hint which is a function of the public key, therefore the adversary \mathcal{A}' can compute all the previous execution of protocol “in his head”. We give a 4-messages PoSpace protocol in the Random Oracle Model (ROM) without any computational assumption. Applying the Fiat-Shamir paradigm to the scheme, we obtain a 2-message non-interactive PoSpace.

3.1 A 4-message PoSpace protocol

The protocol $\Sigma_4 = (\text{Gen}, \text{V}, \text{P})$ is described in Figure 3.1 and it is a 4-message protocol.

The protocol follows in some way Kilian’s construction of argument systems [25]. For any string $\alpha \in \{0, 1\}^*$, let $\mathcal{H}_\alpha(\cdot)$ be defined as $\mathcal{H}(\alpha \parallel \cdot)$. The verifier chooses a random α and asks the prover to build a \mathcal{H}_α labeling of graph $G_{N,k}$, where N depends on S . The purpose of α is to “reset” the random oracle. That is, any previous informations about \mathcal{H} is now useless with overwhelming probability. The labeling is the witness that the prover has handled at least S memory cells. The prover then commits the labeling and sends the commitment to the verifier. At this point, the verifier asks to open several random locations in the commitment and then it checks locally the integrity of the labeling. For a commitment C and for any node that the verifier has challenged, the prover sends what we call a *C-proof* for the node (defined next).

Definition 3 (C-proof). *Given a DAG G , a commitment C , and a random oracle \mathcal{H} , we say that a string $\boldsymbol{\pi} = (\pi_0, \dots, \pi_d)$ is a C-proof for a vertex $v \in V(G)$ w.r.t. \mathcal{H} if only if given $\Gamma_G^-(v) = \{w_1, \dots, w_d\}$ the following points hold:*

1. π_0 is a C-opening for v , let x be the value π_0 is opening to;
2. for each $i = 1, \dots, d$, π_i is a C-opening for w_i , let x_i be the value π_i is opening to;
3. $x = \mathcal{H}(v \parallel x_1 \parallel \dots \parallel x_d)$.

We omit C , G , and \mathcal{H} , when they are clear from the context, saying that $\boldsymbol{\pi}$ is a proof.

In the definition of C-proof, the points 1 and 2 refer to the commitment C while point 3 ensures the integrity of the labeling. Note that the size of $\boldsymbol{\pi}$ is $O(kd \log N)$.

We remark that when TM \mathcal{B} takes as input a RO \mathcal{H} , it is intended that \mathcal{B} has oracle access to \mathcal{H} and the length of the input of \mathcal{B} does not take into account the (exponentially long) length of \mathcal{H} .

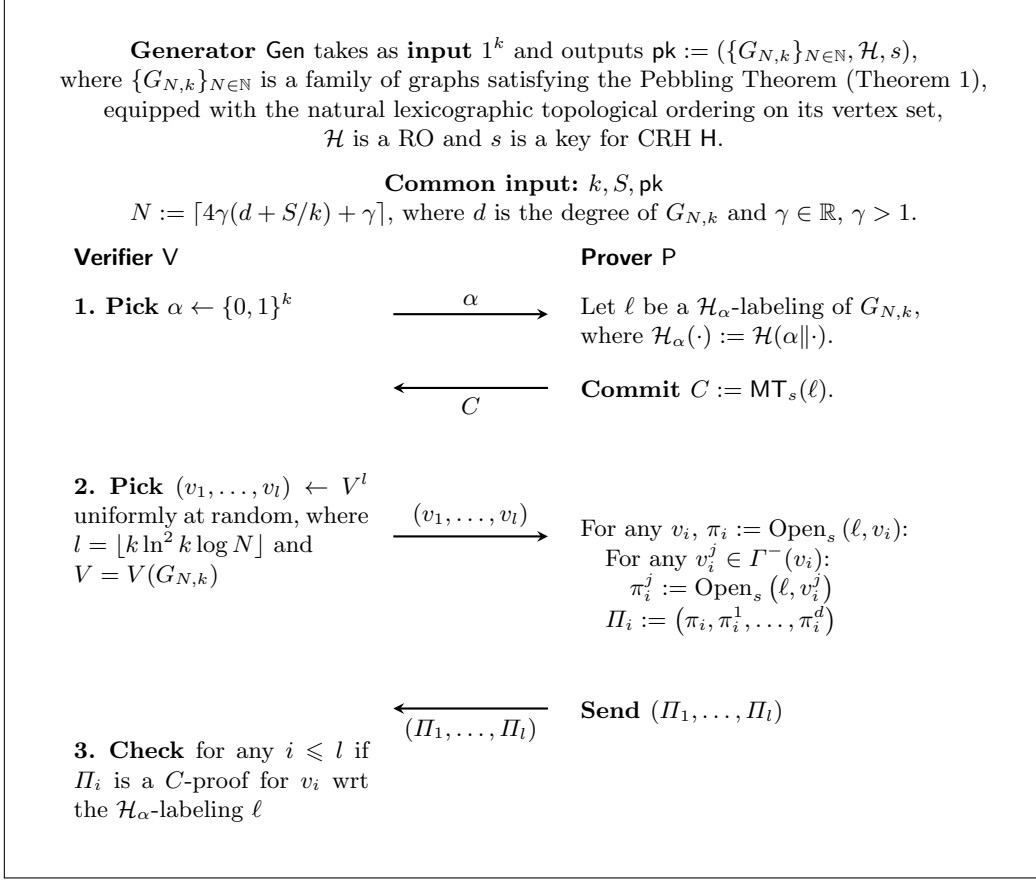


Fig. 3.1. The 4-message PoSpace protocol Σ_4 .

Theorem 2. *The protocol Σ_4 in Figure 3.1 is a PoSpace.*

We start giving the intuition behind the proof of the Theorem. Completeness is trivial. Succinctness follows easily from point (3) of the Pebbling Theorem (Theorem 1). For Soundness, we first prove that the protocol Σ_4 is a Proof-of-Knowledge (PoK) of a partial labeling with “few” faults. By the PoK property, we can extract from a winning adversary a partial labeling with few faults. Thus, with overwhelming probability, the adversary \mathcal{A} computes a partial labeling. We then exploit the binding property of the Merkle-Tree and show that the adversary has computed the labeling during the round (1) of the protocol. By appropriately fixing the randomness of the protocol, we ensure that the hint h is independent of \mathcal{H} , thus meeting all the hypothesis of the Labeling Lemma (Lemma 1). We obtain then a pebbling strategy that has pebbling space and pebbling time roughly upper-bounded by the respective space and time complexity of the adversary \mathcal{A} . Since the adversary uses strictly less space than S and \mathcal{A} is PPT then, by our choice of the parameters, there is a contradiction with the point (1) of the Pebbling Theorem (Theorem 1).

Proof. We focus on the *Soundness*. We divide the proof in two parts. First, in the PoK Lemma, we prove that the Protocol Σ_4 is a PoK for a partial labeling with “few” faults, and that the partial labeling has been computed during the round (1), i.e., after the verifier V sent the message α and before the adversary sent the

commitment message C . Then we sum up the PoK Lemma with the Labeling Lemma and the Pebbling Theorem to reach a contradiction assuming that there exists an winning PPT adversary.

We stress that the knowledge extractor doesn't need to be efficient since we do not rely on any computational assumption.

Lemma 2 (PoK Lemma). *Consider the protocol $\Sigma_4 = (\text{Gen}, \text{V}, \text{P})$, a PPT adversary \mathcal{A} , a PPT Turing Machine with advice \mathcal{A}' , a space parameter S , and a security parameter k . Sample a random $\text{pk} \leftarrow \text{Gen}(1^k)$ and let $h \leftarrow \mathcal{A}'(\text{pk}, 1^S)$. If $\Pr \left[\text{Snd}_{\Sigma_4, S}^{\mathcal{A}, \mathcal{A}'}(k) \right]$ is noticeable (where the probability is taken over the randomness of pk, h and all the randomness used during the protocol execution between \mathcal{A} and the verifier) then, for a noticeable probability over the choice of pk , there exist a first verifier message $\tilde{\alpha}$ and a adversary's randomness $\tilde{\rho}$ such that $\mathcal{A}(h)$ calculates a $\mathcal{H}_{\tilde{\alpha}}$ -labeling ℓ of $G_{N, k}$ with m faults such that the following holds:*

- the hint h is independent of $\mathcal{H}_{\tilde{\alpha}}$,
- $m = O\left(\frac{N}{\ln k}\right)$ and
- for any well-labeled node u in ℓ we have $(\tilde{\alpha} \| u \| \ell(u_1) \| \dots \| \ell(u_d)) \in \mathcal{Q}$,

where \mathcal{Q} is the set of queries to \mathcal{H} made by \mathcal{A} when fed with randomness $\tilde{\rho}$ during round (1) of the protocol Σ_4 , and $\{u_1, \dots, u_d\} = \Gamma^-(u)$.

For space reason the proof of the Lemma is deferred in Appendix C.

By contradiction, suppose there exists an adversary \mathcal{A} for the protocol Σ_4 .

By applying Lemma 2, we extract with noticeable probability a partial $\mathcal{H}_{\tilde{\alpha}}$ -labeling, ensure that $\mathcal{A}(h)$ computed that labeling during round (1) and the hint h is independent of $\mathcal{H}_{\tilde{\alpha}}$. Hence, satisfying the hypotheses of Lemma 1.

This gives us, with overwhelming probability, a pebbling sequence \mathcal{P} of $G_{N, k}$ with m wildcards having pebbling space $S' = \frac{S}{k} + d$, $m = O\left(\frac{N}{\ln k}\right)$. In addition, the pebbling time of \mathcal{P} is a lower bound for the execution time of \mathcal{A} during round (1). To apply Theorem 1, we fix N such that

$$N - l \geq 4S' + 2m + 1, \quad (2)$$

where l denotes the number of wildcards that are in $\mathbf{T}(G_{N, k})$.

Given $c \in (0, 1)$ and $\epsilon > 0$, we have that $(2 + \epsilon)m + l \leq cN$. If we find N such that

$$N \geq 4S' + cN + 1,$$

(hence $N \geq 4\gamma S' + \gamma$, where $\gamma = 1/(1 - c)$) then the inequality (2) will follow. (This is main reason why we have set $N := \lceil 4\gamma S' + \gamma \rceil$ in the Protocol Σ_4 .)

By applying Theorem 1, the execution time T of \mathcal{A} is such that

$$T \geq (N - l) \left(\frac{N - 2S' - m}{2S' + m + 1} \right)^k \geq c'(1 + \epsilon)^k, \quad (3)$$

where c' is a constant that is a lower bound for $N - l$. The value $(1 + \epsilon)^k$ derives from the fact that, eventually,

$$N - 2S' - m \geq (1 + \epsilon)(2S' + m + 1).$$

Equation (3) shows that the execution time of \mathcal{A} is exponential in k . This is not possible as, by hypothesis, \mathcal{A} is PPT. \square

On the Space Gap of the Protocol. The prover algorithm can be implemented basically in two ways. The most natural implementation is to first build the labeling for all the nodes in the graph then apply the merkle tree, keeping in memory the labeling which is reused during the second phase of the prover algorithm. Applying this algorithm the space gap of the prover is $O(\log S)$ while the time complexity is essentially dominated by the one labeling phase. A second way to implement the prover algorithm is by computing the labeling and the merkle tree *simultaneously* in this way the algorithm can reuse space implementing a strategy which has space gap $O(1)$. Anyway in the second implementation the prover has to build the labeling twice (the first time to build the commitment the second time during the challenge phase).

4 A 2-messages PoSpace protocol

We apply the standard *Fiat-Shamir* paradigm to the PoSpace scheme given in Section 3.1 by using two independent random oracles \mathcal{H}, \mathcal{L} :

- $\mathcal{H} : \{0, 1\}^* \rightarrow \{0, 1\}^k$ is used by the prover for the labeling of the graph;
- $\mathcal{L} : \{0, 1\}^k \rightarrow V^l$ given the commitment C as input, it yields the second verifier's message (of the protocol Σ_4).

Let $(\Sigma_4.\text{Gen}, \Sigma_4.\text{P}, \Sigma_4.\text{V})$ the PoSpace defined in Figure 3.1. We define in Figure 4.1 a 2-messages PoSpace: we call $\Sigma_2 = (\text{Gen}, \text{P}, \text{V})$ this protocol. Furthermore, let $\mathcal{H}' : \{0, 1\}^* \rightarrow \{0, 1\}^k$ be a RO then the function $f(x) := \Sigma_2.\text{P}(\text{pk}, 1^S, \mathcal{H}'(x))$ is a non-interactive PoSpace meeting the syntactic definition of PoWs in [15].

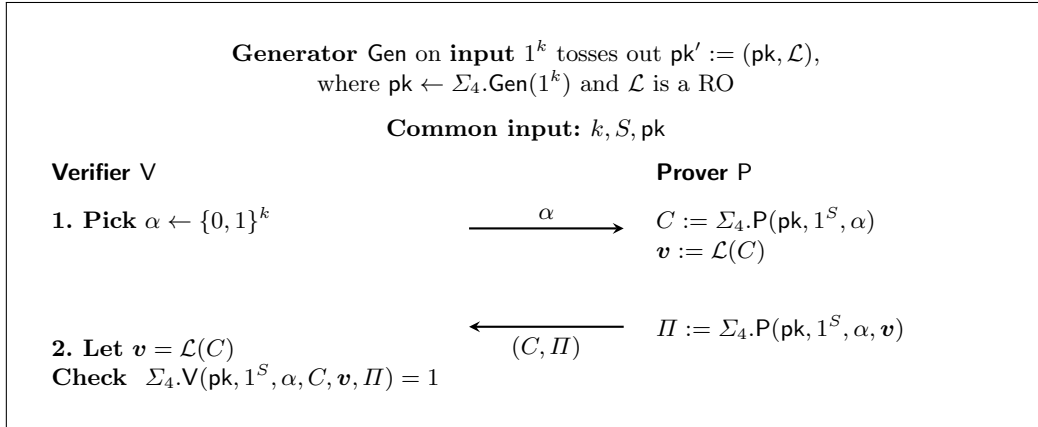


Fig. 4.1. The 2-messages PoSpace protocol $\Sigma_2 = (\text{Gen}, \text{P}, \text{V})$.

Theorem 3. *The Protocol Σ_2 in Figure 4.1 is a PoSpace.*

For space reason we defer the proof of the theorem to Appendix D.

5 Weak Proof-of-Space

The concept of proof of space can lead to multiple interpretations. The main interpretation formalized in the **PoSpace** definition requires that the prover can *handle* (i.e., read/write) space. In this section we provide a definition for a weaker alternative of **PoSpace** we call *weak-Proof-of-Space* (**wPoSpace**). This captures the property that the prover can just access space and formalizes what could effectively be achieved by properly adapting previous work on proof of storage [4, 5, 32] and on memory-bound PoW as defined in [1, 14] (where the adversary model contemplates the existence of cache memory). The definition of **wPoSpace** is similar to the Definition of **PoSpace** (Definition 2) (the only change is in the Soundness part). We will provide a protocol which is a **wPoSpace** but not a **PoSpace**, hence **wPoSpace** is a strictly weaker notion than **PoSpace**.

Definition 4 (**wPoSpace**). *Suppose we are given $\Sigma = (\text{Gen}, \text{P}, \text{V})$ where Gen is a PPT TM, P and V are interactive PPT TMs, $k \in \mathbb{N}$ is a security parameter, and $\text{pk} \leftarrow \text{Gen}(1^k)$. Suppose that the following points hold for all $S \in \mathbb{N}$, $S > k$:*

(Completeness) and (Succinctness) *the same as in the **PoSpace** definition,*
(weak-Soundness) *For any PPT \mathcal{A} adversary the following event:*

$$\mathbf{wSnd}_{\Sigma, S}^{\mathcal{A}}(k) := \langle \mathcal{A}, \text{V} \rangle(\text{pk}, 1^S) = 1 \wedge \text{Space}_{\mathcal{A}}(\langle \mathcal{A}, \text{V} \rangle, \text{pk}, 1^S) < S,$$

has negligible probability (as a function of k).

*Then we say that Σ is a Weak Proof of Space (**wPoSpace**).*

In the Soundness of **PoSpace**, the adversary can take advantage of an unbounded space machine which is then unavailable during the protocol execution. In the Soundness of **wPoSpace**, instead, this is disallowed. Notice that a **wPoSpace**'s adversary can make some precomputation before the execution of the protocol (i.e., before sending/receiving the first message), however, such a precomputation cannot exceed the space bound given.

Theorem 4. *There exists a protocol which is a **wPoSpace** but not a **PoSpace**.*

Proof. We start by providing a protocol which is not a **PoSpace**. Consider the protocol Σ_4 in Figure 3.1 where the first message α sent by V is always the same, say 0^k . We call Σ_3 this modified version of Σ_4 .

The protocol Σ_3 is not a **PoSpace**. For any k and $S \in \mathbb{N}$, consider the hint h which is the \mathcal{H}_{0^k} -labeling of $G_{N,k}$ plus the complete Merkle-Tree of that labeling. We define an adversary that sends the commitment C that is in h and, for any verifier's second message v , reads the right answer from h . That adversary needs to access only h in *read-only* mode, hence without using any additional working space.

The protocol Σ_3 is a **wPoSpace**. The structure of the proof is the same as the one of Theorem 2. We give a particular case of the PoK Lemma (Lemma 2) where the hint h is the empty string and $\tilde{\alpha}$ is 0^k . For the sake of clarity, we restate the PoK Lemma for this particular setting.

Lemma 3. *Given the protocol $\Sigma_3 = (\text{Gen}, \text{V}, \text{P})$, a PPT adversary \mathcal{A} , a space parameter S and a security parameter k such that $\text{pk} \leftarrow \text{Gen}(1^k)$. If $\Pr[\mathbf{wSnd}_{\Sigma_3, S}^{\mathcal{A}}(k)]$ is noticeable, then there exists a randomness $\tilde{\rho}$ such that \mathcal{A} fed with the randomness $\tilde{\rho}$ during the round (1) of the protocol Σ_3 calculates a \mathcal{H}_{0^k} -labeling ℓ of $G_{N, k}$ with m faults such that the following holds:*

- $m = O\left(\frac{N}{\ln k}\right)$ and
- for any well-labeled node u in ℓ we have $(\tilde{\alpha}\|u\|\ell(u_1)\|\dots\|\ell(u_d)) \in \mathcal{Q}$,

where \mathcal{Q} is the set of queries to \mathcal{H} made by \mathcal{A} when fed with randomness $\tilde{\rho}$ until the end of round (1) and $\{u_1, \dots, u_d\} = \Gamma^-(u)$.

By contradiction, suppose there exists an adversary \mathcal{A} for the protocol Σ_3 . By applying Lemma 3 we extract a partial \mathcal{H}_{0^k} -labeling, ensuring that \mathcal{A} computed that labeling during round (1). Hence satisfying the hypotheses of Lemma 1.

This gives us, with overwhelming probability, a pebbling sequence \mathcal{P} of $G_{N, k}$ with m wildcards and with pebbling space $S' = \frac{S}{k} + d$, $m = O\left(\frac{N}{\ln k}\right)$. The pebbling time of \mathcal{P} is a lower bound for the execution time of \mathcal{A} before round (2). The rest of the proof follows exactly the same structure of the proof of Theorem 2 and it is therefore omitted. \square

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A Proof of the Pebbling Theorem (Theorem 1)

This section is devoted to the proof of the Pebbling Theorem. For the sake of convenience we re-write the statement of that theorem here. For the definitions about pebbling games, pebbling space and pebbling time we refer the reader to Section 2.

Pebbling Theorem (Theorem 1). *There exist a family of efficiently sampleable directed acyclic graphs $\{G_{N,k}\}_{k,N \in \mathbb{N}}$ with constant fan-in d , N input nodes, N output nodes and $kN \log N$ nodes in total, such that:*

1. *Any pebbling sequence that pebbles Δ output nodes has pebbling space at most S pebbles and m wildcards, where $|\Delta| \geq 4S + 2m + 1$, has pebbling time T such that*

$$T \geq |\Delta| \left(\frac{N - 2S - m}{2S + m + 1} \right)^k.$$

2. *There exists a pebbling sequence that pebbles the graph $G_{N,k}$ has pebbling space $N + 2$ and needs time $O(kN \log N)$.*
3. *For any node $v \in V(G_{N,k})$, the incoming nodes of v and the position of v in first lexicographic topologically order of $G_{N,k}$ are computable in $O(k \log N)$.*

The family of graphs $G_{N,k}$ we build is made up by DAGs that are k layered stack of superconcentrators [30] with N inputs and N outputs.

Definition 5 (superconcentrator). *We say that a directed acyclic graph $G = (V, E)$ is an N -superconcentrator if and only if*

1. $|\mathbf{S}(G)| = |\mathbf{T}(G)| = N$,
2. *for each $S \subseteq \mathbf{S}(G)$ and for each $T \subseteq \mathbf{T}(G)$ such that $|S| = |T| = \ell$ there exists ℓ vertex-disjoint paths from S to T , i.e. paths such that no vertex in V is in two of them.*

Definition 6 (stack of superconcentrators). *Given an N -superconcentrator G we define the graph $G^{\times k}$, the stack of k copies of G , inductively as follows: $G^{\times 1} = G$ and $G^{\times k}$ is obtained from $G^{\times (k-1)}$ and G first renaming the vertexes of G so that they do not appear in the vertexes of $G^{\times (k-1)}$ obtaining a graph G' and then identifying $\mathbf{T}(G^{\times (k-1)})$ with $\mathbf{S}(G')$.*

Not any k layered stack of superconcentrators satisfy points 2 and 3 of the Pebbling Theorem. To obtain those properties we use the following well-known superconcentrator: the *Butterfly Graph* B_N built by putting together two FFT graphs with N inputs and outputs [33]. An example of such construction (for $N = 16$) is given in Figure A.1.

It is easy to see that the family of graphs $G_{N,k} := B_N^{\times k}$ satisfy points 2 and 3 of the Pebbling Theorem and it is a k layered stack of N -superconcentrators. It remains to prove only point 1.

For superconcentrator graphs we can prove a tradeoff between pebbling space and pebbling time, even for pebbling games with wildcards. This result is based on a variation of the “*Basic Lower Bound Argument*” (BLBA) by Tompa [33].

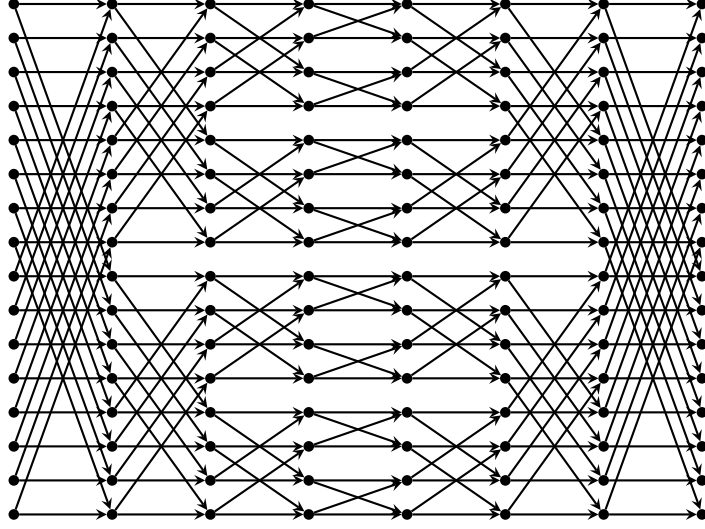


Fig. A.1. An example of 16-superconcentrator: the Butterfly Graph

Lemma 4 (BLBA with wildcards). *Suppose we are given an N -superconcentrator G , a set $M \subseteq \mathbf{T}(G)$ and a pebbling sequence $\mathcal{P} = (P_0, \dots, P_\ell)$ with m wildcards that pebbles M . Let A be the set of all elements of $\mathbf{S}(G)$ pebbled and unpebbled at some point in \mathcal{P} . If $|M| \geq |P_0| + |P_\ell| + m + 1$ then $|A| \geq N - |P_0| - |P_\ell| - m$.*

Proof. By contradiction suppose that $|A| < N - |P_0| - |P_\ell| - m$, this means that $|A^c| \geq |P_0| + |P_\ell| + m + 1$ and every element in A^c , by definition, is not pebbled and unpebbled. Take any $B \subseteq A^c$ such that $|B| = |M|$ then, as G is an N -superconcentrator, we have $\pi_1, \dots, \pi_{|M|}$ vertex disjoint paths from B to M . Let W a set of m fault for \mathcal{P} . By construction $|M| > |P_0 \cup P_\ell \cup W|$ so we have some path π from some element v of B to some element w of M such that $\pi \cap (P_0 \cup P_\ell \cup W) = \emptyset$. By definition of A^c , and hence of B , v is not pebbled and unpebbled and $v \notin P_0 \cup P_\ell \cup W$, so $v \notin P_i$ for each $i \in [\ell]$. As $\pi \cap (P_0 \cap W) = \emptyset$ then we must have that $\pi \cap P_i = \emptyset$ for each $i \in [\ell]$. But then the vertex w is not in $\bigcup_{i \in [\ell]} P_i$ contradicting the fact that \mathcal{P} pebbles M . \square

Theorem 5 (Pebbling Theorem (point 1)). *Let $G^{\times k}$ be a stack of k copies of an N -superconcentrator G , $\Delta \subseteq \mathbf{T}(G^{\times k})$ and \mathcal{P} a pebbling sequence for $G^{\times k}$ with m wildcards that pebbles Δ . Let S the pebbling space of \mathcal{P} and T the pebbling time of \mathcal{P} . If $|\Delta| \geq 4S + 2m + 1$ then*

$$T \geq |\Delta| \left(\frac{N - 2S - m}{2S + m + 1} \right)^k.$$

Proof. Let G_1, \dots, G_k be the k copies of G that form $G^{\times k}$. Suppose we want to pebble a set $M \subseteq \mathbf{T}(G_i)$ such that $|M| \geq 2S + m + 1$ and let A_1, \dots, A_n be disjoint subsets of M such that A_1 are the first $2S + m + 1$ elements of M to be pebbled in \mathcal{P} , A_2 are the second $2S + m + 1$ elements of M to be pebbled in \mathcal{P} and so on. Clearly we have that the number of these sets is $n = \left\lfloor \frac{|M|}{2S + m + 1} \right\rfloor$. By Lemma 4 we have that for each A_j there exists a set $\beta(A_j)$ in $\mathbf{S}(G_i)$, whose elements are pebbled and

unpebbled, of size at least $N - 2S - m$. So we have that the total number of elements in $\mathbf{S}(G_i)$ that have been pebbled and unpebbled is at least $(N - 2S - m) \left\lfloor \frac{|M|}{2S+m+1} \right\rfloor$. Notice now that each $\beta(A_j) \in \mathbf{T}(G_{i-1})$ so we can repeat the argument above to it. Provided that for each j , $|\beta(A_j)| \geq N - 2S - m \geq 2S + m + 1$ and this is implied by the fact that $N \geq |\Delta| \geq 4S + 2m + 1$. So starting from $\mathbf{T}(G_k) = \mathbf{T}(G^{\times k})$ we can prove by induction, going back to G_1 , that at least

$$|\Delta| \left(\frac{N - 2S - m}{2S + m + 1} \right)^k$$

actions of pebbling and unpebbling take place on $\mathbf{S}(G_1) = \mathbf{S}(G^{\times k})$. As each action is an elementary step in the pebbling game \mathcal{P} we have that the result follows. \square

B Proof of the Labeling Lemma (Lemma 1)

Before proceeding with the proof of the Lemma, we recall the definitions of min-entropy and conditional average min-entropy as stated in [13]. For any random variable X we define the min-entropy of X as

$$\mathbf{H}_\infty(X) := -\log \left(\max_{\mathcal{B} \text{ TM}} \Pr[\mathcal{B}() = X] \right)$$

and we define the conditional average min-entropy of X given another random variable Y as

$$\tilde{\mathbf{H}}_\infty(Y|X) := -\log \left(\max_{\mathcal{B} \text{ TM}} \Pr[\mathcal{B}(X) = Y] \right).$$

The following chaining rule for conditional average min-entropy holds [13]:

$$\tilde{\mathbf{H}}_\infty(Y|X) \geq \mathbf{H}_\infty(Y) - |X|.$$

Lemma 1. *Given a DAG G with degree d and a TM \mathcal{A} with advice h and access to a random oracle $\mathcal{H} : \{0, 1\}^* \rightarrow \{0, 1\}^k$ computing a \mathcal{H} -labeling ℓ with m faults of G . If h is independent of \mathcal{H} , with overwhelming probability, there exists a pebbling sequence $\mathcal{P} = (P_1, \dots, P_T)$ for the DAG G with m wildcards having pebbling space S such that:*

- $S \leq \frac{1}{k} \text{Space}(\mathcal{A}) + d$,
- $T \leq (d + 2)\sigma$, where σ is the number of queries of \mathcal{A} to \mathcal{H} .

In particular T is a lower bound for the execution time of \mathcal{A} .

Proof. Let W be the set of not well-labeled nodes by the partial labeling ℓ , in particular $|W| = m$. We look at the ordered set of queries $\mathcal{Q} = \{Q_1, \dots, Q_\tau\}$ of \mathcal{A} to \mathcal{H} of the form $Q_i := (w_i \parallel \ell(w_i^1) \parallel \dots \parallel \ell(w_i^d))$ where $\forall i w_i \in V(G)$ and $\forall j w_i^j \in \Gamma^-(w_i)$. Notice that by construction $\tau \leq \sigma$.

Let $S_i = S_{i-1} \cup \{w_i\} \cup \Gamma^-(w_i)$. As ℓ is a partial \mathcal{H} -labeling and \mathcal{H} a RO, we have that with overwhelming probability $\Gamma^-(w_i) \subseteq S_{i-1} \cup W \cup \mathbf{S}(G)$. Hence the sequence (S_1, \dots, S_σ) can be interpolated to a pebbling sequence adding source vertexes when needed. This would make a pebbling sequence \tilde{P} of length at most

$(d+1)\tau$. Interpolating $\tilde{\mathcal{P}}$ with suitable unpebbling moves after each S_i , i.e., removing all unnecessary pebbles following the informal rule “unpebble as soon as possible”, we can obtain a pebbling sequence \mathcal{P} using minimal space. This is the desired pebbling sequence $\mathcal{P} = (P_1, \dots, P_T)$ with as set of wildcards W . Notice that by construction $T = (d+2)\tau \leq (d+2)\sigma$, hence we have only to focus on bounding the pebbling space of \mathcal{P} .

Let $\tilde{S} = \text{Space}(\mathcal{A})$, suppose for simplicity that k divides \tilde{S} and, by contradiction, suppose that there are $\tilde{S}/k + d + 1$ pebbles at some time t^* in \mathcal{P} . This time t^* should correspond to a query to \mathcal{H} . After the unpebble operation, by the rule “unpebble as soon as possible”, there are left at least $\tilde{S}/k + 1$ pebbles at time $t^* + 1$. We look at the labels of the nodes of G having at time $t^* + 1$ one of the $\tilde{S}/k + 1$ pebbles: by the use of a RO \mathcal{H} , their labels correspond to an uniformly random string $L := \ell_1 \| \dots \| \ell_{\tilde{S}/k+1} \in \{0, 1\}^{\tilde{S}+k}$.

Let M be the content of the memory of \mathcal{A} at time $t^* + 1$. Note that M is a random variable in $\{0, 1\}^{\tilde{S}-1}$. By the chaining rule of the conditional average min-entropy, we have that

$$\tilde{\mathbf{H}}_\infty(L|M, h) = \tilde{\mathbf{H}}_\infty(L|M) > \mathbf{H}_\infty(L) - S = k, \quad (4)$$

where the first equality comes from the independence of \mathcal{H} from h .

We want to upperbound $\tilde{\mathbf{H}}_\infty(L|M, h)$. To do this we define \mathcal{B} a TM that on input M and h executes \mathcal{A} from the state where she was at time $t^* + 1$, writing in the \mathcal{A} 's working tape the string M and on the input tape the hint h and simulating the random oracle \mathcal{H} . Notice that, as we unpebble as soon as possible then, for any $1 \leq i \leq \tilde{S}/k + 1$, the following cases occur:

1. \mathcal{A} is going to query (as substring of queries to \mathcal{H}) the value ℓ_i with overwhelming probability. In fact if this is not the case then in \mathcal{P} it would be already deleted⁴. The overwhelming probability comes from the fact that \mathcal{A} could guess ℓ_i . Hence \mathcal{B} can collect all of those labels $\ell_1, \dots, \ell_{\tilde{S}/k+1}$ by looking at the queries made by \mathcal{A} to \mathcal{H} . Moreover \mathcal{B} can reply to all the queries to \mathcal{H} uniformly choosing strings in $\{0, 1\}^k$ and storing those values to reply accordingly in the future. This simulation is possible as h is independent from \mathcal{H} .
2. \mathcal{A} won't query \mathcal{H} with a string which corresponds to a successor of ℓ_i before having tossed out ℓ_i as a query to \mathcal{H} with overwhelming probability. Otherwise in \mathcal{P} it would already have been deleted. The overwhelming probability comes from the fact that there could be a collision in the RO \mathcal{H} . Hence the simulation of \mathcal{H} stores the labels $\ell_1, \dots, \ell_{\tilde{S}/k+1}$ before using them as replies to \mathcal{A} , therefore the simulation is indistinguishable from \mathcal{A} 's point of view. Eventually \mathcal{B} outputs all the labels collected.

The existence of \mathcal{B} implies that $-\log(1 - \nu(k)) \geq \tilde{\mathbf{H}}_\infty(L|M, h)$, where $\nu(k)$ is a negligible function. This give an immediate contradiction with equation (4). Hence the pebbling space of \mathcal{P} is at most $\tilde{S}/k + d$. \square

⁴ This is the reason why we apply this information-theoretical argument to time $t^* + 1$ and not to time t^* . In fact it would be the case that at time t^* we are pebbling a vertex w but none of the vertexes in $\Gamma^-(w)$ would be used again.

C Proof of the Labeling Lemma (Lemma 2)

Let

$$\mathcal{Q}' := \{\alpha \in \{0, 1\}^k \mid \mathcal{A}'(\text{pk}, 1^S) \text{ made at least a query to } \mathcal{H} \text{ with prefix } \alpha\}.$$

We say that a first message α is *good* iff

$$\Pr [\langle \mathcal{A}(h), \mathcal{V} \rangle(\text{pk}, 1^S) = 1 \mid \mathcal{V} \text{ sends } \alpha \text{ as first message}]$$

is noticeable. By Markov's inequality, the probability that “a uniformly random first message α is good” is noticeable. On the other hand, the probability that a uniformly random first message α is in \mathcal{Q}' is bounded by $\text{poly}(k, S)/2^k$. Thus, there exists $\tilde{\alpha}$ that is both good and not in \mathcal{Q}' . Since $\tilde{\alpha} \notin \mathcal{Q}'$ then $h \leftarrow \mathcal{A}'(\text{pk}, 1^S)$ is independent of $\mathcal{H}_{\tilde{\alpha}}$. Let $V = V(G_{N,k})$ be the vertex set of $G_{N,k}$ and let $W_C(\mathbf{v})$ be the following event:

“ $\mathcal{A}(h, \text{pk}, \tilde{\alpha})$ sends the commitment C at the end of round (1)
and makes the verifier \mathcal{V} accept
when challenged with the vector $\mathbf{v} \in V^l$ during round (2)”.

The event $W_C(\mathbf{v})$ depends on the public key pk , \mathcal{A} 's randomness, and the challenge $\mathbf{v} \leftarrow V^l$, where l is the length of the message as defined in the protocol Σ_4 .

A commitment string sent by $\mathcal{A}(h, \text{pk}, \tilde{\alpha})$ to the verifier \mathcal{V} at the end of round (1) is a *good commitment* iff $\Pr[W_C(\mathbf{v})]$ is noticeable. We have that with noticeable probability $\mathcal{A}(h, \text{pk}, \tilde{\alpha})$ sends a good commitment at the end of round (1).

It might be the case that $\mathcal{A}(h, \text{pk}, \tilde{\alpha})$, instead of computing the labeling through the RO queries, guesses the correct one. However, \mathcal{A} 's guess is correct with probability at most 2^{-k} . Hence, with noticeable probability during round (1):

- $\mathcal{A}(h)$ is not making any guesses and
- $\mathcal{A}(h)$ sends a good commitment.

Thus, there exists a randomness $\tilde{\rho}$ such that $\mathcal{A}(h, \text{pk}, \tilde{\alpha})$ satisfies the two properties above when fed with $\tilde{\rho}$. To simplify the notation, we call such an adversary $\tilde{\mathcal{A}}$.

So we have a deterministic adversary $\tilde{\mathcal{A}}$ that, with noticeable probability over pk and over the challenge \mathbf{v} , makes the verifier accept. Let C be the good commitment sent by $\tilde{\mathcal{A}}$. Now we focus on round (2). Let

$$E := \left\{ u \in V \mid \Pr_{\text{pk}, \mathbf{v}} \left[\tilde{\mathcal{A}} \text{ challenged on } \mathbf{v} \in V^l \wedge u \in \mathbf{v} \right] < r(k) \right\},$$

where $r(k) \in \text{poly}(k)$.

There exists a procedure to compute the set E (recall that the extraction process doesn't need to be efficient). Notice that by construction the nodes of $V \setminus E$ can be C -opened with noticeable probability, hence they C -open to fixed labels with noticeable probability (otherwise an efficient sampling procedure can invalidate the binding property of the underlying Merkle-Tree).

For each $u \in V \setminus E$, there exists a randomness ρ_u such that $\tilde{\mathcal{A}}$ sends to the verifier \mathcal{V} a C -proof for u that C -opens u to the most probable label. Here, it is assumed that $\mathcal{A}(h)$ is using ρ_u as source of randomness during round (2).

If we collect all those values, we obtain a $\mathcal{H}_{\tilde{\alpha}}$ -labeling with at most $|E|$ faults.

We now prove the second item of the lemma, i.e., the upper-bound for $m = |E|$, notice that m is a random variable that depends on \mathbf{pk} . For any $\tilde{\mathbf{pk}}$ let $\tilde{m} := m(\tilde{\mathbf{pk}})$, the following chain of inequalities hold:

$$\begin{aligned}
& \Pr_{\mathbf{v}} \left[W_C(\mathbf{v}) \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] \\
&= \Pr_{\mathbf{v}} \left[W_C(\mathbf{v}) \wedge \forall v_i \in \mathbf{v} \ v_i \notin E \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] + \\
&\quad + \Pr_{\mathbf{v}} \left[W_C(\mathbf{v}) \wedge \exists v_i \in \mathbf{v} \ v_i \in E \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] \\
&\leq \Pr_{\mathbf{v}} \left[\forall v_i \in \mathbf{v} \ v_i \notin E \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] + \sum_{u \in E} \Pr_{\mathbf{v}} \left[W_C(\mathbf{v}) \wedge u \in \mathbf{v} \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] \\
&\leq \left(1 - \frac{\tilde{m}}{|V|} \right)^l + \sum_{u \in E} \Pr_{\mathbf{v}} \left[\tilde{\mathcal{A}} \text{ challenged on } \mathbf{v} \in V^l \wedge u \in \mathbf{v} \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right] \\
&\leq e^{-\frac{\tilde{m}}{|V|} \cdot l} + \sum_{u \in E} \Pr_{\mathbf{v}} \left[\tilde{\mathcal{A}} \text{ challenged on } \mathbf{v} \in V^l \wedge u \in \mathbf{v} \mid \mathbf{pk} = \tilde{\mathbf{pk}} \right]
\end{aligned}$$

Taking the expected value over \mathbf{pk} , we obtain:

$$\begin{aligned}
\Pr_{\mathbf{pk}, \mathbf{v}} [W_C(\mathbf{v})] &\leq \mathbb{E}_{\mathbf{pk}} \left[e^{-\frac{m}{|V|} \cdot l} \right] + \sum_{u \in E} \Pr_{\mathbf{pk}, \mathbf{v}} \left[\tilde{\mathcal{A}} \text{ challenged on } \mathbf{v} \in V^l \wedge u \in \mathbf{v} \right] \\
&< \mathbb{E}_{\mathbf{pk}} \left[e^{-\frac{m}{|V|} \cdot l} \right] + |V| r(k).
\end{aligned}$$

There exists a constant $c \in \mathbb{N}$ such that, for $k \gg 1$, $\Pr [W_C(\mathbf{v})] \geq k^{-c}$. Hence, if we select $r(k) = (|V|2k^c)^{-1}$, we obtain that

$$\mathbb{E}_{\mathbf{pk}} \left[e^{-\frac{m}{|V|} \cdot l} \right] > \frac{1}{2k^c}.$$

Via the Paley-Zygmund inequality [31], we have:

$$\Pr_{\mathbf{pk}} \left[e^{-\frac{m}{|V|} \cdot l} > \frac{1}{4k^c} \right] \geq \frac{1}{8k^{2c}}.$$

So with noticeable probability over \mathbf{pk} :

$$\frac{m}{|V|} \cdot l < \ln 4 + c \ln k.$$

Notice that, in the graph family $\{G_{N,k}\}$, $|V| = kN \log N$. Thus, since $l = k \ln^2 k \log N$, we achieve the required upper-bound for m .

We now prove the third item of the Lemma, i.e., for any assignment ρ of the randomness of \mathcal{A} for the round (1) such that (a) the commitment C is a good commitment and (b) \mathcal{A} has not made any guess, we have that for any $u \in V \setminus E$, $Q_u := (\tilde{\alpha} \| u \| \ell(u_1) \| \dots \| \ell(u_d)) \in \mathcal{Q}$, where \mathcal{Q} is the set of queries made by \mathcal{A} to \mathcal{H} until the end of round (1) and $\{u_1, \dots, u_d\} = \Gamma^-(u)$.

If that would not be the case, we can define a reduction that violate the binding property of the Merkle Tree. The probability that a uniformly random assignment

of the randomness of \mathcal{A} has the properties (a) and (b) and a uniformly random first message of \mathcal{V} is good and not in \mathcal{Q}' is noticeable: let $p(k) \in \text{poly}(k)$ a lower-bound for that probability.

By contradiction suppose there exists a u such that the query $Q_u \notin \mathcal{Q}$. By the choice of $\tilde{\alpha}$, $Q_u \notin \mathcal{Q}'$ hence it must have been made in the round (2) of the protocol. Suppose for now that the reduction knows the values $\ell(u_1), \dots, \ell(u_d)$, then:

- (a) it can choose an assignment of the randomness ρ of \mathcal{A} for the round (1) and a first message $\alpha \in \{0, 1\}^k$ both uniformly at random. Then, it executes $\mathcal{A}(h, \text{pk}, 1^S, \alpha; \rho)$ to obtain a commitment C ;
- (b) \mathcal{V} challenges \mathcal{A} in round (2) with a uniformly random vector $\mathbf{v} \in V^l$ containing u . \mathcal{A} will make the query Q_u to \mathcal{H} and the reduction responds to it with a uniformly random string in $\{0, 1\}^k$;
- (c) it rewinds \mathcal{A} to the point corresponding to the end of round (1) of the protocol, then runs again point (b).

\mathcal{A} will C -open the node u in points (b) and (c) of the reduction with two different values with a probability at least $p(k) \cdot (r(k)^2 - 2^{-k})$.

In general the reduction doesn't know the values $\ell(u_1), \dots, \ell(u_d)$ and it can't figure out which of the queries made by \mathcal{A} in the round (2) of the protocol is Q_u . But \mathcal{A} is PPT so we can find a $q(k) \in \text{poly}(k, S)$ that upper-bounds the number of queries made by \mathcal{A} to \mathcal{H} during round (2).

\mathcal{A} will C -open the node u , by guessing the exact point in time Q_u is queried, with two different values with a probability at least $q(k)^{-1} \cdot p(k) \cdot (r(k)^2 - 2^{-k})$. Hence, this reduction violates the binding property of the Merkle Tree. \square

D The Protocol Σ_2 in Figure 4.1 is a PoSpace.

Completeness and *Succinctness* follow from the fact that Σ_4 is a PoSpace as proved in Theorem 2. To prove *Soundness*, we reduce an adversary \mathcal{A} for the scheme Σ_2 to an adversary \mathcal{B} for the scheme Σ_4 (of Figure 4.1). Suppose, by contradiction, there exists an adversary \mathcal{A} , a hint $h \leftarrow \mathcal{A}(\text{pk}')$ and a value S such that for k large enough:

$$\Pr \left[\mathbf{Snd}_{\Sigma_2, S}^{\mathcal{A}, \mathcal{A}'}(k) \right] \geq k^{-c}.$$

Let $q(k)$ a polynomial that upper-bounds the number of queries made by \mathcal{A} to the RO \mathcal{L} . For each $0 \leq i < q(k)$, let Q_i be the i -th query made by \mathcal{A} to the oracle \mathcal{L} . Without loss of generality, we can assume that all the Q_i 's are different and \mathcal{A} verifies her output certificate before sending it: if \mathcal{A} hasn't found a valid certificate, she outputs the empty string. Thus, either there exists an index m such that $(C, \Pi) \leftarrow \mathcal{A}(h, \text{pk}', \alpha; \rho)$, with $Q_m = C$ and $\Sigma_4.\mathcal{V}(\text{pk}, 1^S, \alpha, C, \mathcal{L}(C), \Pi) = 1$, or $\mathcal{A}(h, \text{pk}', \alpha; \rho)$ outputs the empty string. Moreover:

$$\Pr[(C, \Pi) \leftarrow \mathcal{A}(h, \text{pk}', \alpha) \wedge Q_m = C] \geq k^{-c} \cdot q(k)^{-1}.$$

Notice that the probability above states that the outputs of \mathcal{A} will be the same value asked by \mathcal{A} to \mathcal{L} at the m -th call. However, the value Q_m is a random variable that may depend on all the randomness used until the m -th call. We divide the

randomness used by \mathcal{A} into two parts ρ_1 and ρ_2 : ρ_1 is the randomness used by \mathcal{A} before she has made the m -th query to the oracle \mathcal{L} , ρ_2 is the remaining randomness.

By an averaging argument, there exists a setting of ρ_1 such that the following equation holds

$$\Pr_{\rho_2, \mathcal{H}, \mathcal{L}} [(C, \Pi) := \mathcal{A}(h, \text{pk}', \alpha; \rho_1 \| \rho_2) \wedge Q_m = C] \geq k^{-c} \cdot q(x)^{-1} \quad (5)$$

Now the value Q_m is a random variable that depends only on the randomness of the challenge α , the ROs \mathcal{H} and \mathcal{L} , and ρ_1 . Therefore, by repeating many times the TM $\mathcal{A}(h, \text{pk}', \alpha; \rho_1 \| \rho_2)$ for a uniformly random ρ_2 using the same oracles \mathcal{H} , \mathcal{L} , we'll get the same value Q_m as the m -th query to \mathcal{L} . We define the PPT TM with advice \mathcal{B}' that on input pk and advice ρ_1, m outputs the hint $\tilde{h} := (\mathcal{A}'(\text{pk}, \mathcal{L}), \mathcal{Q}', m, \rho_1)$, where \mathcal{Q}' is the set of RO queries made by \mathcal{A}' to \mathcal{H} and \mathcal{L} .

Define $\mathcal{B}(\tilde{h}, \text{pk}, 1^S)$:

- **Wait** for the message α ;
- Compute $\mathcal{A}(\tilde{h}, \text{pk}, \alpha; \rho_1)$ and reply to RO queries from \mathcal{A} via \mathcal{H} or \mathcal{L} respectively;
 until she makes the m -th RO query to \mathcal{L} . Let Q_m be such a query;
- **Pause** the machine \mathcal{A} ;
- **if** $Q_m \in \mathcal{Q}'$ **abort**.
- **Send** Q_m to $\Sigma_4.V$;
- **Wait** for the message v from $\Sigma_4.V$;
- choose ρ_2 uniformly at random;
- **Resume** \mathcal{A} and reply to the query Q_m with v and use as randomness ρ_2 ;
- **If** \mathcal{A} outputs the empty string, **send** the empty string **to** $\Sigma_4.V$;
 else let (C, Π) be the output of \mathcal{A} ;
- **if** $C = Q_m$ **then send** Π **to** $\Sigma_4.V$
 else send the empty string **to** $\Sigma_4.V$.

First, we show that the value Q_m has high min-entropy. Since $|\mathcal{Q}'| \in \text{poly}(k, S)$, this implies that \mathcal{B} will abort with negligible probability.

By the equation (5), there exists a \mathcal{H}_α -labeling ℓ with $m = O(N/\ln k)$ faults such that $Q_m = \text{MT}_s(\ell)$. Let \mathcal{R}' be the set of RO replies made by \mathcal{H} and \mathcal{L} to the RO queries of \mathcal{A}' . As α is uniformly random in $\{0, 1\}^k$ then

$$\tilde{\mathbf{H}}_\infty(\ell | \mathcal{R}', \alpha) \geq (|V(G_{N,k})| - m - 1) \cdot k.$$

In fact only with negligible probability in k the set \mathcal{Q}' contains queries to \mathcal{H} with prefix α . The min-entropy of Q_m is $\omega(\log k)$ because MT is collision resistant. The probability that Q_m is in \mathcal{Q}' is then negligible.

When \mathcal{B} doesn't abort the simulation of the protocol, Σ_2 is perfect. Also notice that \mathcal{B} needs space $S' = S + O(\log(\text{poly}(k)))$ where S is from the emulations of \mathcal{A} and $O(\log \text{poly}(k))$ for the auxiliary indexes. Moreover, if the event in equation (5) holds, then \mathcal{B} will not send the empty string to $\Sigma_4.V$. Hence, it follows that the adversary \mathcal{B} , accessing the hint function \tilde{h} and using space S' for k large enough, is such that:

$$\Pr \left[\mathbf{Snd}_{\Sigma_4, S'}^{\mathcal{B}, \mathcal{B}'}(k) \right] \geq k^{-c} \cdot q(k)^{-1} - \text{negl}(k).$$

Therefore, we reach the contradiction that \mathcal{B} is a winning adversary against protocol Σ_4 which was proved to be a PoSpace (Theorem 2). \square

E Merkle Tree

Consider the complete binary tree T over n leaves. Without loss of generality we can suppose that n is a power of 2 by using padding if necessary. Label the i -th leaf with l_i , then, use a collision resistant hash (CRH) function $H_s : \{0, 1\}^{2k} \rightarrow \{0, 1\}^k$ with random seed s to propagate the labeling \mathbf{l} of the leaves to a labeling ϕ along the tree T up to the root according to the following rules:

1. $\phi(v) := l_i$ if v is the i -th leaf of T ,
2. $\phi(v) := H_s(\phi(v_1) \parallel \phi(v_2))$ where v_1, v_2 are the two children of v .

Then the *commitment* for a string \mathbf{l} is the label $\phi(r)$ of the root of the tree: we denote it with $MT_s(\mathbf{l})$. The commitment is succinct in fact it has size k which is independent of n .

An *opening* for the i -th element in the sequence \mathbf{l} is an ordered sequence formed by elements of $\{0, 1\}^k$ that are intended to be the label l_i of the i -th leaf of T together with the labels $\phi(v_j)$ for each v_j that is a sibling of vertices in the path from the root of the tree to the i -th leaf. An example of opening is shown in Figure E.1.

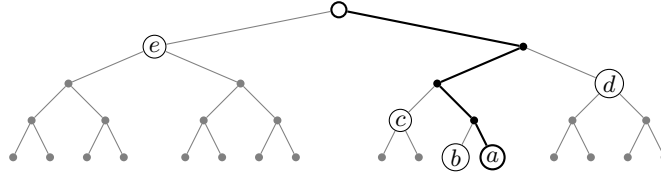


Fig. E.1. An example of opening: $\mathbf{c} = (a, b, c, d, e)$

We denote with $\text{Open}_s(\mathbf{l}, i)$ the opening formed by l_i together with all the $\phi(v_j)$. Notice that from $\text{Open}_s(\mathbf{l}, i)$ it is possible to compute $\phi(w)$ for each vertex w in the path from the root to the leaf at the i -th position. In particular it is efficient to compute $MT_s(\mathbf{l})$. The opening is succinct in fact it has size $k \cdot \log n$ which is (almost) independent of n . In general given an opening $\mathbf{c} = (c_1, \dots, c_{\log n})$ for some position i we can behave *as if* it is $\text{Open}_s(\mathbf{l}, i)$ and compute the label of the root according to the given \mathbf{c} and position i . If this value is equal to $MT_s(\mathbf{l})$ we say that \mathbf{c} *opens the i -th position to c_1* . As H is a CRH function, then for any i , it is guaranteed that $MT_s(\mathbf{l})$ can open the i -th position only to l_i .