

# Cuckoo Cycle: a graph-theoretic proof-of-work system

John Tromp

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## Abstract

We introduce the first graph-theoretic proof-of-work system, based on finding cycles in large random graphs. Such problems are arbitrarily scalable and trivially verifiable. Our implementation uses 1 bit per edge, and up to 1 bit per node. We hypothesize that using significantly less causes superlinear slowdown.

## 1 Introduction

A “proof of work” (PoW) system allows a verifier to check with negligible effort that a prover has expended a large amount of computational effort. Originally introduced as a spam fighting measure, where the effort is the price paid by an email sender for demanding the recipient’s attention, they now form one of the cornerstones of crypto-currencies.

As proof-of-work for new blocks of transactions, Bitcoin [1] adopted Adam Back’s hashcash [2] proof-of-work. This requires finding a nonce value such that application of a cryptographic hash function (twofold SHA256 in Bitcoin’s case) to this nonce (and the rest of the block header) results in a number with many leading 0s. The number of leading 0s is dynamically adjusted by the protocol so as to maintain a certain average block interval (10-minutes for Bitcoin).

Since Bitcoin, many other crypto-currencies have adopted hashcash, with various choices of underlying hash function. the most well-known being *scrypt* as used in Litecoin.

Primecoin [3] introduced the notion of a number-theoretic proof-of-work, as the first alternative to hashcash among crypto-currencies. This requires finding long chains of nearly doubled prime numbers, with a certain relation to the block header. Current algorithms use a two-step process of filtering candidates by *sieving*, and applying pseudo-primality tests to remaining candidates. These algorithms are somewhat complex and involve many trade-offs. Recently, another prime-number based crypto-currency, Riecoin, was introduced, based on finding clusters rather than chains of prime numbers.

## 2 Graph-theoretic proofs-of-work

We propose to base proofs-of-work on finding certain subgraphs in large pseudo-random graphs. In the Erdős-Rényi model, denoted  $G(N, M)$ , a graph is chosen uniformly at random from the collection of all graphs with  $N$  nodes and  $M$  edges. Instead, we choose edges deterministically from the output of a keyed hash function, whose key could be chosen uniformly at random. For a well-behaved hash function, these two classes of random graphs should have nearly identical properties. Formally, fix a keyed hash function  $h : \{0, 1\}^K \times \{0, 1\}_i^W \rightarrow \{0, 1\}_o^W$  /footnotehash functions generally have arbitrary length inputs, but here we fix the input width at  $W_i$  bits., and a small graph  $H$  as a target subgraph.

Now pick a large number  $N \leq 2^W$  as the number of nodes, and  $M \leq 2^{W_i-1}$  as the number of edges. Each key  $k \in \{0, 1\}^K$  generates a graph  $G_k = (V, E)$  where  $V = \{v_0, \dots, v_{N-1}\}$ , and

$$E = \{(v_{h(k,2i) \bmod N}, v_{h(k,2i+1) \bmod N}) | i \in [0, \dots, M-1]\} \quad (1)$$

The inputs  $i \in [0, \dots, M-1]$  are also called *nonces*. The graph has a *solution* if  $H$  occurs as a subgraph. Denote the number of edges in  $h$  as  $|H|$ . A proof of solution is an ordered list of  $|H|$  nonces that generate the edges of  $H$ 's occurrence in  $G$ . Such a proof is verifiable in constant time, independent of  $N$  and  $M$ .

A simple variation generates random bipartite graphs:  $G_k = (U \cup V, E)$  where  $U = \{u_0, \dots, u_{\frac{N}{2}-1}\}$ ,  $V = \{v_0, \dots, v_{\frac{N}{2}-1}\}$ , and

$$E = \{(u_{h(k,2i) \bmod \frac{N}{2}}, v_{h(k,2i+1) \bmod \frac{N}{2}}) | i \in [0, \dots, M-1]\} \quad (2)$$

The expected number of occurrences of  $H$  as a subgraph of  $G$  is a function of both  $N$  and  $M$ , and in many cases it is roughly a function of the ratio  $\frac{M}{N}$ . For fixed  $N$ , the function is monotonically increasing in  $M$ . To make the proof-of-work challenging, one chooses a value of  $M$  that yields less than one expected solution (but not much less).

### 3 Cuckoo Cycle

The simplest possible choice of subgraph is a fully connected one, or a *clique*. While an interesting choice, akin to the number-theoretic notion of a prime-cluster, as used in Riecoin, we leave its consideration to a future paper. In this paper we focus on what is perhaps the next-simplest possible choice, the *cycle*. Specifically, we propose the hash function siphash with a  $K = 128$  bit key,  $W_i = W_O = 64$  input and output bits,  $N < 2^{64}$  a 2-power,  $M = N/2$ , and  $H$  a 42-cycle (more on that in a later section). The reason for calling the resulting proof-of-work Cuckoo Cycle is that inserting numbers in a Cuckoo hashtable naturally leads to forming cycles in random bipartite graphs!

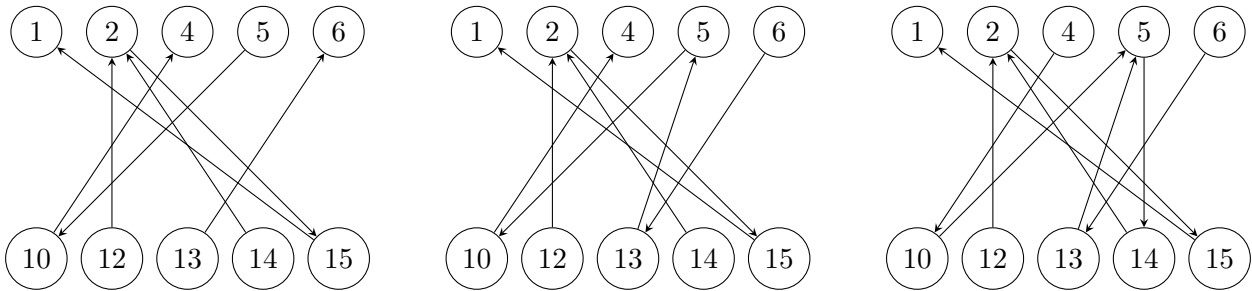
### 4 Cuckoo hashing

Introduced by Rasmus Pagh and Flemming Friche Rodler [4], a cuckoo hashtable consists of two same-sized tables each with its own hash function mapping a key to a table location, providing two possible locations for each key. Upon insertion of a new key, if both locations are already occupied by keys, then one is kicked out and inserted in its alternate location, possibly displacing yet another key, repeating the process until either a vacant location is found, or some maximum number of iterations is reached. The latter is bound to happen once cycles have formed in the *Cuckoo graph*. This is a bipartite graph with a node for each location and an edge for every key, connecting the two locations it can reside at. It also matches the bipartite graph defined above if the cuckoo hashtable were based on function  $h$ . In fact, the insertion procedure suggests a simple algorithm for detecting cycles.

### 5 Cycle detection in Cuckoo hashing

We enumerate the  $M$  nonces, but instead of storing the nonce itself as a key in the Cuckoo hashtable, we store the alternate key location at the key location, and forget about the nonce. We thus maintain the *directed* cuckoo graph, in which the edge for a key is directed from the location where it resides to its alternate location. Moving a key to its alternate location thus corresponds to reversing its edge. The outdegree of every node in this graph is either 0 or 1. When there are no cycles yet, the

graph is a *forest*, a disjoint union of trees. In each tree, all edges are directed, directly, or indirectly, to its *root*, the only node in the tree with outdegree 0. Initially there are just  $N$  singleton trees consisting of individual nodes which are all roots. Addition of a new key causes a cycle if and only if its two endpoints are nodes in the same tree, which we can test by following the path from each endpoint to its root. In case of different roots, we reverse all edges on the shorter of the two paths, and finally create the edge for the new key itself, thereby joining the two trees into one. The left diagram below shows the directed cuckoo graph for header ‘39’ on  $N = 8 + 8$  nodes after adding edges  $(1, 15), (2, 12), (4, 10), (2, 15), (6, 13), (5, 10)$  and  $(2, 14)$  (nodes with no incident edges are omitted for clarity). In order to add the 8th edge  $(5, 13)$ , we follow the paths  $5 \rightarrow 10 \rightarrow 4$  and  $13 \rightarrow 6$  to find different roots 4 and 6. Since the latter path is shorter, we reverse it to  $6 \rightarrow 13$  so we can add the new edge as  $(13 \rightarrow 5)$ , resulting in the middle diagram. In order to add to 9th edge  $(5, 14)$  we now find the path from 5 to be the shorter one, so we reverse that and add the new edge as  $(5 \rightarrow 14)$ , resulting in the right diagram.



When adding the 10th edge  $(4, 12)$ , we find the paths  $4 \rightarrow 10 \rightarrow 5 \rightarrow 14 \rightarrow 2 \rightarrow 15 \rightarrow 1$  and  $12 \rightarrow 2 \rightarrow 15 \rightarrow 1$  with equal roots. In this case, we can compute the length of the resulting cycle as 1 plus the sum of the path-lengths to the node where the two paths first join. In the diagram, the paths first join at 2, and the cycle length is computed as  $1 + 4 + 1 = 6$ .

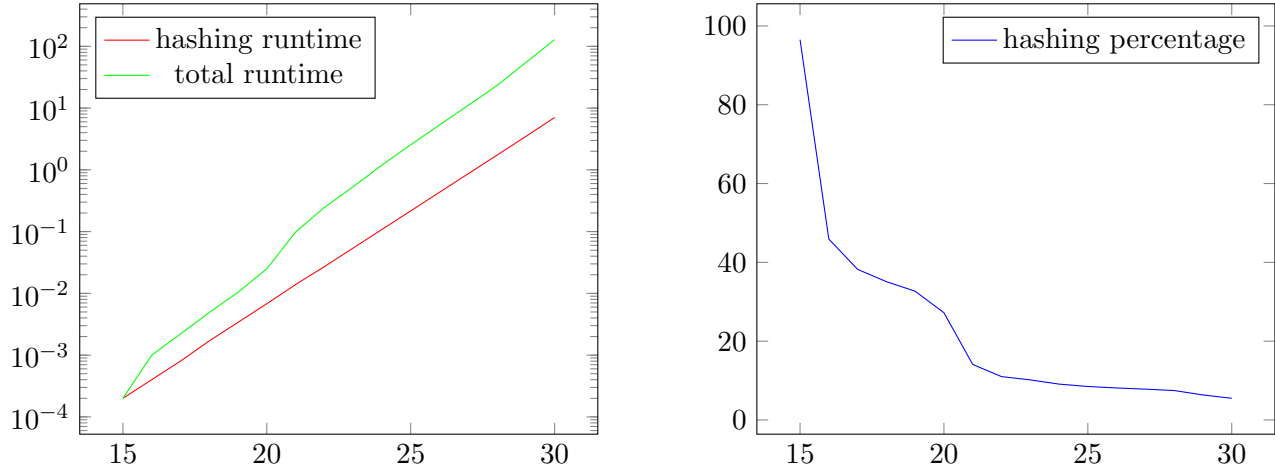
## 6 Union-find

The above representation of the directed cuckoo graph is an example of a *disjoint-set data structure* [5], and our algorithm is closely related to the well known union-find algorithm, where the find operation determines which subset an element is in, and the union operation joins two subsets into a single one. For each edge addition to the cuckoo graph we perform the equivalent of two find operations and one union operation. The difference is that the union-find algorithm is free to add directed edges between arbitrary elements. Thus it can join two subsets by adding an edge from one root to another, with no need to reverse any edges. Conversely, our algorithm can be seen as the first one that solves the union-find problem by maintaining a direction on all union operations while keeping the maximum outdegree at 1.

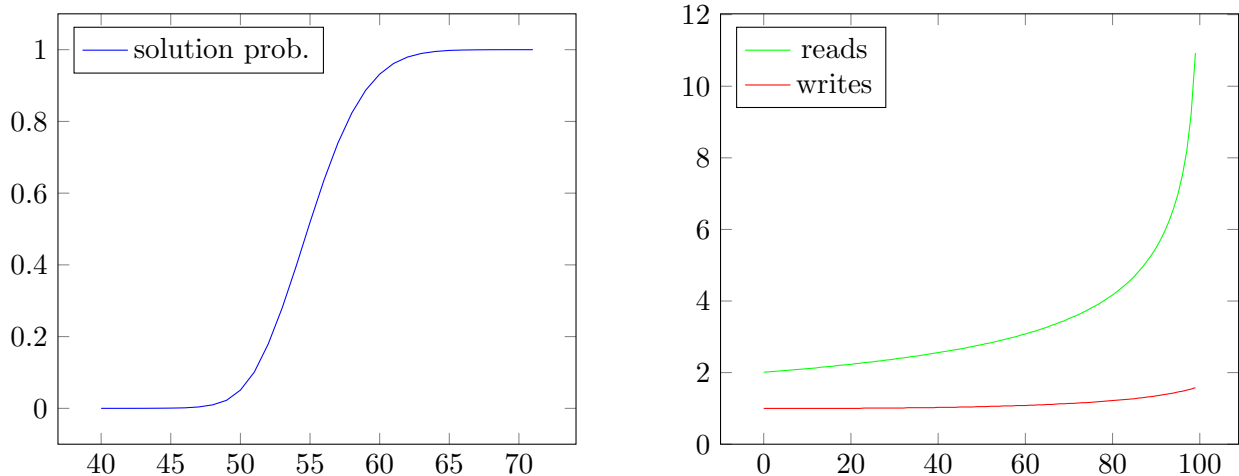
## 7 Cuckoo Cycle basic algorithm

The above algorithm for inserting edges and detecting cycles forms the basis for our basic proof-of-work algorithm. If a cycle of length  $L$  is found, then we solved the problem, and recover the proof by storing the cycle edges in a set and enumerating nonces once more to see which ones generate edges in the set. If a cycle of a different length is found, then we keep the graph acyclic by ignoring the edge. There is some probability of overlooking other  $L$ -cycles through that edge, but in the important case of having few cycles in the cuckoo graph to begin with, it hardly affects the rate of solution finding.

This algorithm is available online at <https://github.com/tromp/cuckoo> as either the C-program `simple_miner.cpp` or the Java program `SimpleMiner.java`. A proof verifier is available as `cuckoo.c` or `Cuckoo.java`, while the repository also has a Makefile, as well as the latest version of this paper. ‘make example’ reproduces the example shown above. The simple program uses 32 bits per node to represent the directed cuckoo graph, plus about 64KB per thread for 2 auxiliary arrays. The left plot below shows both the total runtime in seconds and the runtime of just the hash computation, as a function of (log)size. The latter is purely linear, while the former is superlinear due to increasing memory latency as the nodes no longer fit in cache. The right plot show this more clearly as the percentage of hashing to total runtime, ending up around 5%.



The left plot below shows the probability of finding a 42-cycle as a function of the percentage edges/nodes (relative easiness), while the right plot shows the average number of memory reads and writes per edge as a function of the percentage nonce/easiness (progress through main loop). Both were determined from 10000 runs at size  $2^{20}$ ; results at size  $2^{25}$  look almost identical. In total the program averages 3.3 reads and 1.1 writes per edge.



## 8 Difficulty control

Relative easiness (the ratio  $E/N$ ) determines a base level of difficulty, which may suffice for applications where difficulty is to remain fixed. The ratio  $E/N = 1$  is suitable when a practically guaranteed

solution is desired, For crypto currencies, where difficulty must scale in precisely controlled manner across a huge range, adjusting easiness is not suitable. The implementation default  $E/N = 1/2$  gives a solution probability of roughly 2.2%, while the average number of cycles found increases slowly with size; from 2 at  $2^{20}$  to 3 at  $2^{30}$ . For further control, a difficulty target  $0 < T < 2^{256}$  is introduced, and we impose the additional constraint that the sha256 digest of the cycle nonces in ascending order be less than  $T$ , thus reducing the success probability by a factor  $2^{256}/T$ .

## 9 Edge Trimming

Dave Andersen [6] suggested drastically reducing the number of edges our basic algorithm has to process, by repeatedly identifying nodes of degree one and eliminating its incident edge. Such *leaf edges* can never be part of a cycle. This works whenever the number of edges  $M$ , is at most half the number of nodes  $N$ , since the expected degree of a node is then at most 1.

This is implemented in our main algorithm in cuckoo\_miner.h It maintains a set of *alive* edges as a bit vector. Initially all edges are alive. In each of a given number of trimming rounds, it shrinks this set as follows. A vector of 2-bit degree counters, one per u-node, is initialized to all zeroes. Next, for all alive edges, compute its u-endpoint and increase the corresponding counter, capping the value at 2. Next, for all alive edges, compute its u-endpoint and if the corresponding counter is less than 2, set the edge to be not-alive. These steps are repeated for the other partition, of v-nodes. Preprocessor symbol PART\_BITS, whose value we'll denote as  $B$ , allows for trading-off node counter storage for time, by processing the nodes in multiple passes depending on the value of their least significant bits. The memory usage is  $M$  bits for the alive set and  $N/2^B$  for the counters.

After all edge trimming rounds, the counter memory is freed, and allocated to a custom cuckoo\_hashtable that presents the same interface as the simple array in the basic algorithm, but gets by with much fewer locations, as long as its *load*, the ratio of remaining edges to number of locations, is significantly less than 1.

The number of trimming rounds, which can be set with option `-n`, defaults to  $2^{1+(B+3)*(B+4)/2}$ , which achieves a load close to 50%.

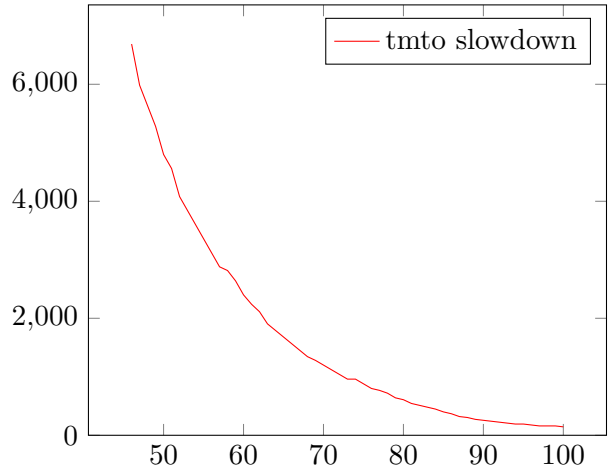
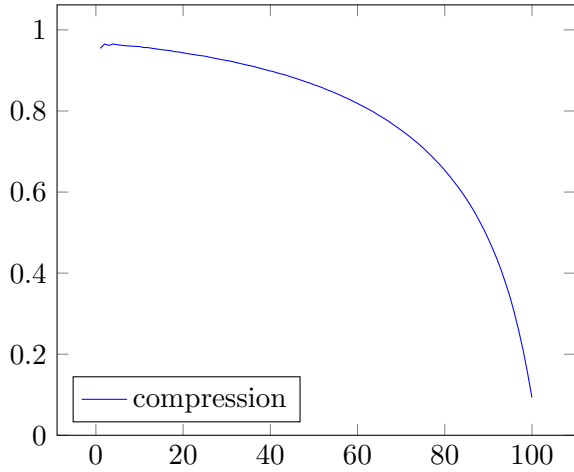
## 10 Memory-hardness

Reducing memory usage to a under 1 bit per edge is quite challenging. One approach is to maintain the set of alive edges in three parts:

- one part that has a density less than one-half and can be compressed using e.g. arithmetic coding.
- one part that is stored uncompressed.
- remaining edges are not stored and considered all alive.

Let's say we devote  $A$  percent of  $N/2$  bits (regular alive storage) to this, and another  $B$  percent to vertex degree counters. The left plot below shows how well a given fraction of edges can be compressed (the entropy of live probability) by repeatedly removing leaf edges within this fraction. Most of this removal is achieved in 4 rounds of trimming, with each round of trimming requiring  $2 \cdot 2 \cdot \lceil \frac{2 \cdot 100}{B} \rceil$  passes over all alive edges (one factor of 2 for  $U \cup V$ , another for writing and then reading degree counts). (Re-)compressing the current stored fraction frees up some of  $A$ , which allows us to transfer part of the unstored edges into a new uncompressed part. Repeating this process will ultimately trim all but a small fraction of edges. The plot on the right shows how many passes are needed as a function of

the total memory usage  $A + B$  where the split between  $A$  and  $B$  is always chosen optimally. Clearly this time-memory trade-off is highly nonlinear and impractical below 50%.

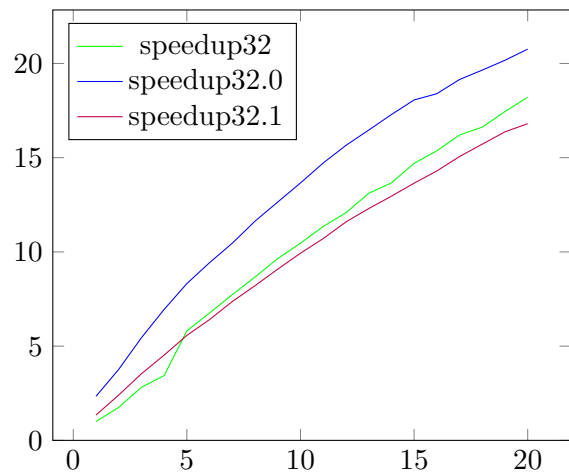
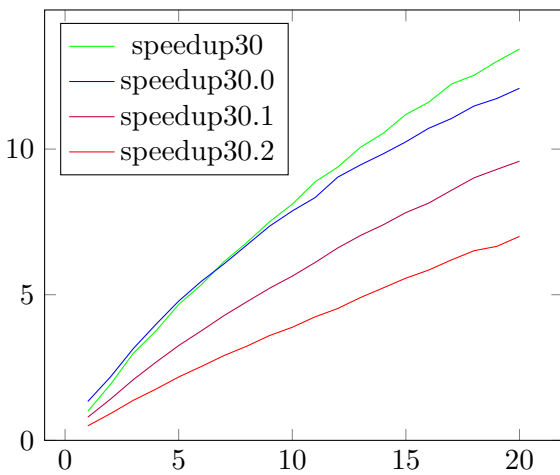


## 11 Parallelization

The implementation allows the number of threads to be set with option `-t`. For  $0 \leq t < T$ , thread  $t$  processes all nonces  $t \bmod T$ . Parallelization in the basic algorithm presents some minor algorithmic challenges. Paths from an edge's two endpoints are not well-defined when other edge additions and path reversals are still in progress. One example of such a path conflict is the check for duplicate edges yielding a false negative, if in between checking the two endpoints, another thread reverses a path through those nodes. Another is the inadvertant creation of cycles when a reversal in progress hamper's another thread's path following causing it to overlook root equality. Thus, in a parallel implementation, path following can no longer be assumed to terminate. Instead of using a cycle detection algorithm such as [7], our implementation notices when the path length exceeds MAXPATHLEN (8192 by default), and reports wether this is due to a path conflict.

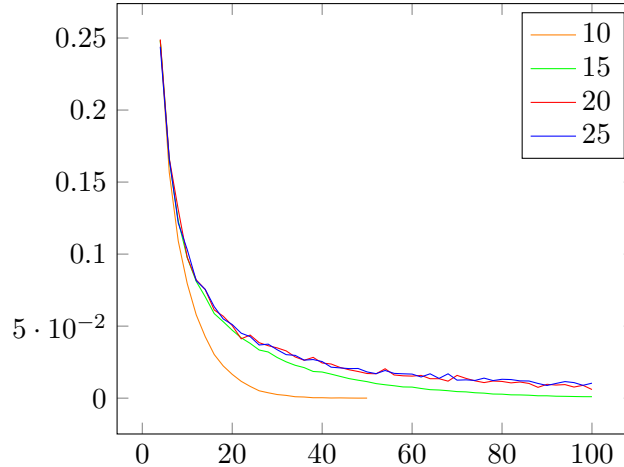
In the main algorithm, cycle detection only takes a small fraction of total runtime and the conflicts above could be avoided altogether by running the cycle detection single threaded. We therefore turn our attention to parallelization of edge trimming.

To be expanded...



## 12 Choice of cycle length

Extremely small cycle lengths risk the feasibility of alternative algorithms with better performance. For example, for  $L = 2$  the problem reduces to finding a birthday collision as in the Momentum proof-of-work. It is conceivable however that the Cuckoo representation is already optimal for  $L = 4$ . In order to keep proof size manageable, the cycle length should not be too large either. We consider 20-64 to be a healthy range, which averages to 42. The plot below shows the distribution of cycle lengths found for sizes  $2^{10}$ ,  $2^{15}$ ,  $2^{20}$ ,  $2^{25}$ , as determined from 100000,100000,10000, and 10000 runs respectively. The tails of the distributions beyond  $L = 100$  are not shown. For reference, the longest cycle found was of length 2120.



The main implementation can easily handle  $N = 2^{43}$  nodes which uses 1TB of memory.

## 13 Computation versus memory

Starting out at 32 leading zeroes in 2009, Bitcoin difficulty has steadily climbed and is currently at 64, representing an incredible  $2^{64}/10$  double-hashes per minute. This growth was enabled by the migration of hash computation from desktop processors (CPUs) to graphics-card processors (GPUs), to field-programmable gate arrays (FPGAs), and finally to custom designed chips (ASICs).

Downsides of this development include high investment costs, rapid obsolescence, centralization of mining power, and large power consumption. Although ASICs are the most energy-efficient way of computing hashes, the tiny amount of die-space needed for a single SHA256 circuit allows a huge number of them (e.g. 1440 on KnC's Neptune) to be crammed onto a single chip, consuming 100s of Watts and requiring ample cooling. Thus, energy costs dominate the economics of mining.

This has led people to look for alternative proof-of-work systems that, by requiring a nontrivial amount of memory, resist such massive parallelizability, and narrow the performance gap with commodity hardware. Memory chips, in the form of DRAM, have only a small portion of their circuitry active at any time<sup>1</sup> and thus require orders of magnitude less power.

Litecoin replaces the SHA256 hash function in hashcash by a single round version of the *scrypt* key derivation function. Its memory requirement of 128KB is a compromise between computation-hardness for the prover and verification efficiency for the verifier. Although designed to be GPU-resistant, GPUs are now at least an order of magnitude faster than CPUs for Litecoin mining. ASICs first appeared on the market in early 2014 and are expected to dominate Litecoin mining by the fourth quarter.

<sup>1</sup>this applies to each of the 8 or so *banks* that make up the memory in each chip, while operating in parallel

Momentum [8] proposes finding birthday collisions of hash outputs as proof-of-work, the simplest way to combine scalable memory usage with trivial verifiability. Its memory requirements are not very strict though, as Bloom filters or rainbow tables can identify collisions, and parallelizes well.

Adam Back [9] has a good overview of proof-of-work papers past and present.

## 14 Conclusion

Cuckoo Cycle is a novel graph-theoretic proof-of-work design that combines scalable memory requirements with instant verifiability. It's also the first proof-of-work in which memory latency dominates the runtime. This could lead to mining costs being dominated in turn by DRAM investments, changing the economics of mining in ways that require further study. More research is also needed to determine the effectiveness of GPUs and FPGAs at running Cuckoo Cycle.

## References

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## 15 Appendix A: cuckoo.h

```
// Cuckoo Cycle, a memory-hard proof-of-work
// Copyright (c) 2013–2014 John Tromp

#include <stdint.h>
#include <string.h>
#include <openssl/sha.h> // if openssl absent, use #include "sha256.c"

// proof-of-work parameters
```



```

#ifndef SIZESHIFT
#define SIZESHIFT 25
#endif
#ifndef PROOFSIZE
#define PROOFSIZE 42
#endif

#define SIZE (1UL<<SIZESHIFT)
#define HALFSIZE (SIZE/2)
#define NODEMASK (HALFSIZE-1)

typedef uint64_t u64;
typedef u64 nonce_t;
typedef u64 node_t;

typedef struct {
    u64 v[4];
} siphash_ctx;

#define U8TO64_LE(p) \
    (((u64)((p)[0])      ) | ((u64)((p)[1]) << 8) | \
     ((u64)((p)[2]) << 16) | ((u64)((p)[3]) << 24) | \
     ((u64)((p)[4]) << 32) | ((u64)((p)[5]) << 40) | \
     ((u64)((p)[6]) << 48) | ((u64)((p)[7]) << 56))

// derive siphash key from header
void setheader(siphash_ctx *ctx, const char *header) {
    unsigned char hdrkey[32];
    SHA256((unsigned char *)header, strlen(header), hdrkey);
    u64 k0 = U8TO64_LE(hdrkey);
    u64 k1 = U8TO64_LE(hdrkey+8);
    ctx->v[0] = k0 ^ 0x736f6d6570736575ULL;
    ctx->v[1] = k1 ^ 0x646f72616e64666dULL;
    ctx->v[2] = k0 ^ 0x6c7967656e657261ULL;
    ctx->v[3] = k1 ^ 0x7465646279746573ULL;
}

#define ROTL(x,b) ((u64)((x) << (b)) | ((x) >> (64 - (b))))
#define SIPROUND \
do { \
    v0 += v1; v2 += v3; v1 = ROTL(v1,13); \
    v3 = ROTL(v3,16); v1 ^= v0; v3 ^= v2; \
    v0 = ROTL(v0,32); v2 += v1; v0 += v3; \
    v1 = ROTL(v1,17); v3 = ROTL(v3,21); \
    v1 ^= v2; v3 ^= v0; v2 = ROTL(v2,32); \
} while(0)

// SipHash-2-4 specialized to precomputed key and 8 byte nonces
u64 siphash24(siphash_ctx *ctx, u64 nonce) {
    u64 v0 = ctx->v[0], v1 = ctx->v[1], v2 = ctx->v[2], v3 = ctx->v[3] ^ nonce;
    SIPROUND; SIPROUND;
    v0 ^= nonce;
    v2 ^= 0xff;
    SIPROUND; SIPROUND; SIPROUND; SIPROUND;
    return v0 ^ v1 ^ v2 ^ v3;
}

// generate edge endpoint in cuckoo graph
node_t sipnode(siphash_ctx *ctx, nonce_t nonce, int uorv) {
    return siphash24(ctx, 2*nonce + uorv) & NODEMASK;
}

```

```

}

void sipedge(siphash_ctx *ctx, nonce_t nonce, node_t *pu, node_t *pv) {
    *pu = sipnode(ctx, nonce, 0);
    *pv = sipnode(ctx, nonce, 1);
}

// verify that (ascending) nonces, all less than easiness, form a cycle in header-generated graph
int verify(nonce_t nonces[PROOFSIZE], const char *header, u64 easiness) {
    siphash_ctx ctx;
    setheader(&ctx, header);
    node_t us[PROOFSIZE], vs[PROOFSIZE];
    unsigned i = 0, n;
    for (n = 0; n < PROOFSIZE; n++) {
        if (nonces[n] >= easiness || (n && nonces[n] <= nonces[n-1]))
            return 0;
        sipedge(&ctx, nonces[n], &us[n], &vs[n]);
    }
    do { // follow cycle until we return to i==0; n edges left to visit
        unsigned j = i;
        for (unsigned k = 0; k < PROOFSIZE; k++) // find unique other j with same vs[j]
            if (k != i && vs[k] == vs[i]) {
                if (j != i)
                    return 0;
                j = k;
            }
        if (j == i)
            return 0;
        i = j;
        for (unsigned k = 0; k < PROOFSIZE; k++) // find unique other i with same us[i]
            if (k != j && us[k] == us[j]) {
                if (i != j)
                    return 0;
                i = k;
            }
        if (i == j)
            return 0;
        n -= 2;
    } while (i);
    return n == 0;
}

```