

# TWORAM: Efficient Oblivious RAM in Two Rounds with Applications to Searchable Encryption

No Author Given

No Institute Given

**Abstract.** We present TWORAM, an asymptotically efficient oblivious RAM (ORAM) protocol providing oblivious access (read and write) of a memory index  $y$  in exactly *two* rounds: The client prepares an encrypted query encapsulating  $y$  and sends it to the server. The server accesses memory  $M$  obliviously and returns encrypted information containing the desired value  $M[y]$ . The cost of TWORAM is only a multiplicative factor of security parameter higher than the tree-based ORAM schemes such as the path ORAM scheme of Stefanov et al. (CCS, 2013). TWORAM substantially improves the worst-case asymptotic complexity of existing round-optimal ORAM protocols and gives rise to interesting applications, and in particular to a 4-round searchable encryption scheme where search is sub-linear in the worst case and the search pattern is not leaked—the access pattern can also be concealed assuming the documents are stored in the obliviously accessed memory  $M$ .

## 1 Introduction

Oblivious RAM (ORAM) is a cryptographic primitive for accessing a remote memory  $M$  of  $n$  entries in a way that memory accesses do not reveal anything about the accessed index  $y \in \{1, \dots, n\}$ . Goldreich and Ostrovsky [17] were the first to show that ORAM can be built with  $\text{poly}(\log n)$  bandwidth overhead<sup>1</sup>, and since then, there has been a fruitful line of research on substantially reducing this overhead [9,29,35,37], in part motivated by the tree ORAM framework proposed by Shi et al. [32]. However, *most* existing practical ORAM protocols are highly-interactive, requiring the client to perform a “download-decrypt-compute-encrypt-upload” operation several times (typically  $O(\log n)$  rounds are involved). This can be a bottleneck for real-world applications where low latency is important.

In this paper we consider the problem of building a round-optimal ORAM scheme with improved efficiency. In particular, we propose TWORAM, an ORAM scheme enabling a client to obliviously access a memory location  $M[y]$  in two rounds, where the client sends an encrypted message to the server that encapsulates  $y$ , the server performs the oblivious computation, and sends a message back to the client, from which the client can retrieve the desired value  $M[y]$ .

**Complexity of our protocols.** TWORAM’s bandwidth overhead is  $O(\kappa \cdot p)$  where  $p$  is the bandwidth overhead of a tree ORAM scheme and  $\kappa$  is the security parameter. E.g.,

---

<sup>1</sup> We define *bandwidth overhead* as the number of bits transferred between the client and the server during a single memory access, including the data block.

Table 1: Comparison with prior work. We use  $n$  to denote the number of memory entries and  $\kappa$  the security parameter. For the comparison, the data block size is  $O(\log n)$  bits. TWORAM adds a multiplicative security parameter to the Path-ORAM overhead, yet it reduces the rounds to two.

approach	rounds per access	worst-case bandwidth overhead per access (in bits)
hierarchical (e.g., Kushilevitz et al. [24])	$O(\log n)$	$O(\log^3 n / \log \log n)$
tree-based (e.g., Path-ORAM [35])	$O(\log n)$	$O(\log^3 n)$
SR-ORAM [38] and Bucket-ORAM [10]	2	$\Omega(n)$
non-black-box Garbled RAM [12,14,28]	2	$O(\text{poly}(\log n, \kappa,  f ))$
black-box Garbled RAM [11]	2	$\Omega(\kappa^5 \cdot \log^7 n)$
TWORAM	2	$O(\kappa \cdot \log^3 n)$

for Path-ORAM [35], it is  $p = \log^3 n$  for a block of size  $O(\log n)$  bits. In other words, in order to obviously read a data block of  $O(\log n)$  bits using TWORAM, one needs to exchange, *in the worst case*, a total number of  $O(\kappa \cdot \log^3 n)$  bits with the server, just in two rounds.

### 1.1 Comparison to existing round-optimal ORAM schemes

We are not the first to construct a round-optimal ORAM scheme. However, our approach is asymptotically better than existing approaches on several parameters, as we detail in the following (also see Table 1). At the same time, we believe our protocols are simple to describe and thus lend themselves to further optimizations and potential practical implementations.

**Works with linear worst-case complexity.** Williams and Sion [38] and Fletcher et al. [10] devise round-optimal ORAM schemes that are efficient in an amortized sense, yet their worst-case bandwidth overhead is at least linear in  $n$ , due to an expensive reshuffling procedure that requires downloading a linear number of blocks. Such an overhead could make oblivious RAM unsuitable in many practical scenarios, such as real-time systems for searchable encryption considered in this paper. Our construction, on the other hand, is exponentially better in terms of worst case efficiency.

**Works with polylogarithmic worst-case complexity.** The only known approach to construct a round-optimal ORAM scheme with polylogarithmic worst-case complexity are generic constructions based on garbled RAM [11,12,14,28]. However, such generic approaches are prohibitively inefficient. For instance, for the non-black-box Garbled RAM approaches [28,14,12], the bandwidth overhead grows with  $\text{poly}(\log n, \kappa, |f|)$ , where  $|f|$  is the size of the circuit for computing the one-way function  $f$  and  $\kappa$  is the security parameter. This leads to inefficient constructions, that are only of theoretical interest. For the black-box Garbled RAM approach [11] the bandwidth overhead grows with  $\text{poly}(\log n, \kappa)$ , and is independent of  $|f|$ . However, the construction itself is asymptotically very inefficient. Specifically in [11] the authors do not provide details on how large the involved polynomials are, which will depend on the choice of various parameters. According to our back-of-the-envelope calculation, however, the polynomial

is at least  $\kappa^5 \cdot \log^7 n$ . A key reason for this inefficiency is that they require certain expensive ORAM operations, specifically “eviction,” to be performed inside a garbled circuit. We eliminate this source of inefficiency by moving these expensive ORAM operations outside of the garbled circuits.

## 1.2 Technical highlights

Our construction is inspired by the ideas from the recent, black-box garbled RAM work by Garg, Lu and Ostrovsky [11]. We specifically use those ideas and build on top of tree ORAM algorithms [32]. Our new ideas help avoid certain inefficiencies involved in the original construction of [11], yielding an asymptotically better protocol.

Our first step is to abstract away certain details of eviction-based tree ORAM algorithms, such as Path-ORAM [35], circuit ORAM [37] and Onion ORAM [9]. These algorithms work as follows: The memory  $M$  that must be accessed obliviously is stored as a sequence of  $L$  trees  $T_1, T_2, \dots, T_L$ . The actual data of  $M$  are stored encrypted in the tree  $T_L$ , while the other trees store *position map* information (also encrypted). Only  $T_1$  is stored on the client side. Roughly speaking, to access an index  $y$  in  $M$ , the client accesses  $T_1$  and sends a path index  $p_2$  to the server. The server then, successively accesses paths  $p_2, p_3, \dots, p_L$  in  $T_2, T_3, \dots, T_L$ . However the paths are accessed adaptively: in order to learn  $p_i$ , one needs to first access  $p_{i-1}$  in  $T_{i-1}$ , and have all the information (also known as buckets) stored in its nodes decrypted. This is where existing approaches require  $O(L)$  rounds of interaction: decryption can only take place at the client side, which means all the information on the paths must be communicated back to the client.

**TWORAM’s core idea.** In order to avoid the roundtrips described above, we do not use standard encryption. Instead, we hardcode the content of each bucket inside a garbled circuit [39]. In other words, after the trees  $T_2, T_3, \dots, T_L$  are produced, the client generates one garbled circuit per each internal node in each tree. The function of this garbled circuit is very simple: Informally, it takes as input an index  $x$ ; loops through the blocks  $\text{bucket}[i]$  contained in the current bucket until it finds  $\text{bucket}[x]$ , and returns the index  $\pi = \text{bucket}[x]$  of the next path to be followed. Note that the index  $\pi$  is returned in form of a *garbled input* for the next garbled circuit, so that the execution can proceed by the server until  $T_L$  is reached, and the final desired value can be returned to the client (see Figure 3 for a more formal description).

This simplified description ignores some technical hurdles. Firstly, security of the underlying ORAM scheme requires that the location where  $\text{bucket}[x]$  is found remains hidden. In particular, the garbled circuit which has the value  $\text{bucket}[x]$  inside it should not be identifiable by the server. We resolve this issue as follows. For every bucket that the underlying ORAM needs to touch, all the corresponding garbled circuits are executed in a specific order and the value of interest is carried along the way and output only by the final evaluated circuit in that tree.

Secondly, the above approach only works well for a single memory access, since the garbled circuits can only be used once. Fortunately, as we show in the paper, only a logarithmic number of garbled circuits are touched for each access. These circuits can be downloaded by the client who decodes the hardcoded values, performs the eviction

strategy locally (on plaintext data), and sends fresh garbled circuits back to the server. This step does not increase the number of rounds (from two to three), since sending the fresh garbled circuits to the server can be “piggybacked” onto the message the client prepares for the next memory access.

Finally, in order to ensure the desired efficiency, and to avoid a blowup of polynomial multiplicative factor in security parameter, we develop optimizations that help ensure that the sizes of the circuits garbled in our construction remain small and proportional to the underlying ORAM.

### 1.3 Application: 4-round searchable encryption with no search pattern leakage

An SSE scheme allows a client to outsource a database (defined as a set of document/keyword set pairs  $DB = (d_i, W_i)_{i=1}^N$ ) to a server in an encrypted format, where a search query for  $w$  returns  $d_i$  where  $w \in W_i$ .

Several recent work [26,21,3] demonstrate attacks against property-preserving encryption schemes (which also enable search on encrypted data), by taking advantage of the leakage associated with these schemes. Though these attacks do not lead to concrete attacks against existing SSE schemes, they underline the importance of examining the feasibility of solutions that avoid leakage. A natural building block for doing so is ORAM. We use TWORAM to obtain the first constant-round, and asymptotically efficient SSE that can hide search/access patterns.

Our construction combines TWORAM and a non-recursive Path-ORAM (i.e., one whose position map of the first level is not outsourced) in a way that a search for keyword  $w$  requires (i) a single access on TWORAM; (ii)  $|DB(w)|$  parallel accesses to the non-recursive Path-ORAM (note that an access to a non-recursive ORAM requires only two rounds).

In particular, we use TWORAM to store pairs of the form  $(w, (count_w, access_w))$ , where  $w$  is a keyword,  $count_w$  is the number of documents containing  $w$  and  $access_w$  is the number of times  $w$  has been accessed so far. The keyword/document pairs  $(w||i, d_i)$  (where  $d_i$  is the  $i$ -th document containing  $w$ ) are then stored in the non-recursive Path-ORAM where their position in the Path-ORAM tree (namely the random path they are mapped to) is determined on the fly by using a PRF  $F$  as  $F_k(w||i, access_w)$  (therefore there is no need to store the position map locally). To search for a keyword  $w$ , we first access TWORAM to obtain  $(count_w, access_w)$  (and increment  $access_w$ ), and then generate all positions to look up in the Path-ORAM using the PRF  $F$ . These lookups can all be performed in parallel and updating the paths can be piggybacked to the next keyword search.

The above yields a construction with 4 rounds of interaction. Note that naively using ORAM for SSE would incur  $|DB(w)|$  ORAM accesses which imply *at least*  $|DB(w)|$  roundtrips (depending on the number of rounds of the underlying ORAM). As we said before, our construction *does not leak the search pattern*, by providing randomly generated tokens every time a search is performed. If we choose to store all documents in the obliviously-accessed memory, the access pattern can also be concealed.

## 1.4 Other related work

**Oblivious RAM.** ORAM protocols with a non-constant number of roundtrips can be categorized into *hierarchical* [18,19,24,27], motivated by the seminal work of Goldreich and Ostrovsky [17], and *tree-based* [9,29,35,37], motivated by the seminal work of Shi et al. [32]. We note however, that, by picking the data block size to be very big (e.g.,  $\sqrt{n}$  bits), the number of rounds in tree-based ORAMs can be made constant, yet the bandwidth increases beyond polylogarithmic, so such a parameter selection is not interesting.

**Searchable encryption.** Song et al. [33] were the first to explore feasibility of symmetric searchable encryption. Since then, a large number of follow-up work have designed new SSE for both static data [8,6,4] and dynamic data [5,15,23,22,36,34]. The security definitions for SSE also evolved over time and eventually established in the work of [8,6]. Unlike our construction, *all these approaches use deterministic tokens*, and therefore leak the search patterns. The only proposed approaches that are constant-round and have randomized tokens (apart from constructing SSE through Garbled RAM) are the ones based on functional encryption [31]. However, such approaches incur a linear search overhead.

We also note that one can obtain SSE with no search pattern leakage by directly using interactive ORAM, such Path-ORAM [35], or variants of it optimized for binary search [13].

**Secure Computation for RAM Programs.** A recent line of work studies efficient secure two-party computation of RAM programs based on garbled circuits [20,1]. These constructions can also be used to design SSE that hide the search pattern—yet these approaches do not lead to constant-round SSE schemes, requiring the client to perform computation proportional to the size of the search result.

## 2 Definitions and preliminaries

In this section, we recall definitions and describe building blocks we use in this paper. We use the notation  $\langle C', S' \rangle \leftrightarrow \Pi \langle C, S \rangle$  to indicate that a protocol  $\Pi$  is executed between a client with input  $C$  and a server with input  $S$ . After the execution of the protocol the client receives  $C'$  and the server receives  $S'$ . For non-interactive protocols, we just use the left arrow notation ( $\leftarrow$ ) instead.

### 2.1 Garbled Circuits

Garbled circuits were first constructed by Yao [39] (see Lindell and Pinkas [25] and Bellare et al. [2] for a detailed proof and further discussion). A circuit garbling scheme is a tuple of PPT algorithms  $(\text{GCircuit}, \text{Eval})$ , where  $\text{GCircuit}$  is the circuit garbling procedure and  $\text{Eval}$  the corresponding evaluation procedure. More formally:

- $(\tilde{C}, \text{lab}) \leftrightarrow \text{GCircuit}(1^\kappa, C)$ :  $\text{GCircuit}$  takes as input a security parameter  $\kappa$ , and a circuit  $C$ . This procedure outputs a *garbled circuit*  $\tilde{C}$  and  $\text{lab}$ , which is a set of input labels for each input wire of  $C$ .

- $y \leftrightarrow \text{Eval}(\tilde{C}, \text{lab}_x)$ : Given a garbled circuit  $\tilde{C}$  and a sequence of input labels  $\text{lab}_x$  (which we will call garbled inputs<sup>2</sup>), Eval outputs  $y$ .

*Correctness.* For correctness, we require that for any circuit  $C$  and an input  $x$  for  $C$ , we have that  $C(x) = \text{Eval}(\tilde{C}, \text{lab}_x)$ , where  $(\tilde{C}, \text{lab}) \leftrightarrow \text{GCircuit}(1^\kappa, C)$ .

*Security.* For security, we require that for any PPT adversary  $A$ , there is a PPT simulator  $\text{Sim}$  such that the following distributions are computationally indistinguishable:

- $\text{Real}_A(\kappa)$ :  $A$  chooses a circuit  $C$ . The experiment runs  $(\tilde{C}, \text{lab}) \leftrightarrow \text{GCircuit}(1^\kappa, C)$  and sends  $\tilde{C}$  to  $A$ .  $A$  then outputs an input  $x$ . The experiment outputs  $(\tilde{C}, \text{lab}_x)$ .
- $\text{Ideal}_{A, \text{Sim}}(\kappa)$ :  $A$  chooses a circuit  $C$ . The experiment runs  $(\tilde{C}, \sigma) \leftrightarrow \text{Sim}(1^\kappa, C)$  and sends  $\tilde{C}$  to  $A$ .  $A$  then outputs an input  $x$ . The experiment runs  $\text{lab}_x \leftrightarrow \text{Sim}(1^\kappa, \sigma)$ , and outputs  $(\tilde{C}, \text{lab}_x)$ .

The above definition guarantees adaptive security, since the adversary gets to choose input  $x$  after seeing the garbled circuit  $\tilde{C}$ . We only know how to instantiate garbling schemes with adaptive security in the random oracle model. In the standard model, existing garbling schemes achieve a weaker static variant of the above definition where the adversary chooses both  $C$  and input  $x$  at the same time and before receiving  $\tilde{C}$ .

Concerning complexity, we note that if the cleartext circuit  $C$  has  $|C|$  gates, the respective garbled circuit has size  $O(|C|k)$ . This is because every gate in the circuit is typically replaced with a table of four rows, each row storing encryptions of labels (each encryption has  $k$  bits).

## 2.2 Oblivious RAM

We recall *Oblivious RAM* (ORAM), a notion introduced and first studied by Goldreich and Ostrovsky [16,30]. ORAM can be thought of as a compiler that encodes the memory into a special format such that accesses on the compiled memory do not reveal the underlying access patterns on the original memory. An ORAM scheme consists of protocols (SETUP, OBLIVIOUSACCESS).

- $\langle \sigma, \text{EM} \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, M \rangle, \perp)$ : SETUP takes as input the security parameter  $\kappa$  and a memory array  $M$  and outputs a secret state  $\sigma$  (for the client), and an encrypted memory  $\text{EM}$  (for the server).
- $\langle (M[y], \sigma'), \text{EM}' \rangle \leftrightarrow \text{OBLIVIOUSACCESS}(\langle \sigma, y, v \rangle, \text{EM})$ : OBLIVIOUSACCESS is a protocol between the client and the server, where the client's input is the secret state  $\sigma$ , an index  $y$  and a value  $v$  which is set to null in case the access is a read operation (not a write). Server's input is the encrypted memory  $\text{EM}$ . Client's output is  $M[y]$  and an updated secret state  $\sigma'$  and the server's output is an updated encrypted memory  $\text{EM}'$  where  $M[y] = v$ , if  $v \neq \text{null}$ .

<sup>2</sup> Traditionally, a garbling scheme has encode algorithm the takes  $\text{lab}$  and  $x$  as input and outputs the garbled inputs for  $x$ . For simplicity we use  $\text{lab}_x$  to mean encoding input  $x$ .

*Correctness.* Consider the following correctness experiment. Adversary A chooses memory M. Consider the encrypted database EM generated with  $\text{SETUP}$  (i.e.,  $\langle \sigma, \text{EM} \rangle \leftrightarrow \text{SETUP}\langle(1^\kappa, \text{M}), \perp\rangle$ ). The adversary then adaptively chooses memory locations to read and write. Denote the adversary's read/write queries by  $(y_1, v_1), \dots, (y_q, v_q)$  where  $v_i = \text{null}$  for read operations. A wins in the correctness game if  $\langle (M_i[y_i], \sigma_i), \text{EM}' \rangle$  are not the final outputs of the protocol  $\text{OBLIVIOUSACCESS}\langle(\sigma_{i-1}, y_i, v_i), \text{EM}_{i-1}\rangle$  for any  $1 \leq i \leq q$ , where  $M_i, \text{EM}_i, \sigma_i$  are the memory array, the encrypted memory array and the secret state, respectively, after the  $i$ -th access operation, and  $\text{OBLIVIOUSACCESS}$  is run between an honest client and server. The ORAM scheme is correct if the probability of A in winning the game is negligible in  $\kappa$ .

*Semi-Honest Security.* An ORAM Scheme is secure if for any adversary A, there exists a simulator Sim such that the following two distributions are computationally indistinguishable.

- $\text{Real}_A(\kappa)$ : A chooses M. The experiment then runs  $\langle \sigma, \text{EM} \rangle \leftrightarrow \text{SETUP}\langle(1^\kappa, \text{M}), \perp\rangle$ . A then adaptively makes read/write queries  $(y_i, v)$  where  $v = \text{null}$  on reads, for which the experiment runs the protocol

$$\langle (M[y_i], \sigma_i), \text{EM}_i \rangle \leftrightarrow \text{OBLIVIOUSACCESS}\langle(\sigma_{i-1}, y_i, v), \text{EM}_{i-1}\rangle.$$

Denote the full transcript of the protocol by  $t_i$ . Eventually, the experiment outputs  $(\text{EM}, t_1, \dots, t_q)$  where  $q$  is the total number of read/write queries.

- $\text{Ideal}_{A, \text{Sim}}(\kappa)$ : The experiment outputs  $(\text{EM}, t'_1, \dots, t'_q) \leftrightarrow \text{Sim}(q, |\text{M}|, 1^\kappa)$ .

### 3 TWORAM construction

We now describe our construction. We will use an abstract view of a tree-based ORAM scheme, and specifically that of *Path-ORAM* [35]. We start by describing this abstract view informally. Then we show how to turn the interactive Path-ORAM protocol (e.g., the one by Stefanov et al. [35]) into a two-round ORAM protocol, using the abstraction that we present below. We now give some necessary notation that we need for understanding our abstraction.

#### 3.1 Some necessary notation

Let  $n = 2^L$  be the size of the initial memory that we wish to access obliviously. This memory is denoted by  $A_L[1], A_L[2], \dots, A_L[n]$  where  $A_L[i]$  is the  $i$ -th *block* of the memory. Given location  $y$  that we wish to access, let  $y_L, y_{L-1}, \dots, y_1$  be defined as  $y_L = y$  and  $y_i = \text{ceil}(y_{i+1}/2)$ , for all  $i = L-1, L-2, \dots, 1$ . For example, for  $L = 4$  and  $y = 13$ , we have

- $y_1 = \text{ceil}(\text{ceil}(\text{ceil}(y/2)/2)/2) = 2$ .
- $y_2 = \text{ceil}(\text{ceil}(y/2)/2) = 4$ .
- $y_3 = \text{ceil}(y/2) = 7$ .
- $y_4 = 13$ .



Also define  $b_i = 1 - y_{i+1} \% 2$  to be a bit (namely  $b_i$  indicates if  $y_i$  is even or not). Finally, on input a  $2L$  bit value  $x$ ,  $\text{select}(x, 0)$  selects the first  $L$  bits, while  $\text{select}(x, 1)$  selects the last  $L$  bits. We note here that both  $y_i$  and  $b_i$  are functions of  $y$ , but we do not indicate this explicitly so that not to clutter notation.

### 3.2 Path-ORAM abstraction

We start by describing our abstraction of Path-ORAM construction. In Appendix A we describe formally how this abstraction can be used to implement the interactive Path-ORAM algorithm [35] (with  $\log n$  rounds of interaction). We note that the details in the appendix are provided only for helping better understanding. Our construction can be understood based on just the abstraction defined below.

Path-ORAM algorithms encode memory  $A_L$  in the form of  $L$  memories

$$A_L, A_{L-1}, \dots, A_1.$$

Each  $A_i$  has  $2^i$  entries, each one storing blocks of  $2L$  bits. Memories  $A_L, A_{L-1}, \dots, A_2$  are stored in trees  $T_L, T_{L-1}, \dots, T_2$  respectively. Memory  $A_1$  is kept locally by the client. The invariant that is maintained is that any block  $A_i[x]$  will reside in *some* leaf-to-root path of tree  $T_i$ , and specifically on the path that starts from leaf  $x_i$  in  $T_i$ . The value  $x_i$  itself can be retrieved by accessing  $A_{i-1}$ , as we detail in the following.

**Reading a value  $A_L[y]$ .** To read a value  $A_L[y]$ , one first reads  $A_1[y_1]$  from local storage and computes  $x_2 \leftarrow \text{select}(A_1[y_1], b_1)$  (recall definitions of  $y_1$  and  $b_1$  from Section 3.1). Then one traverses the path starting from leaf  $x_2$  in  $T_2$ . This path is denoted with  $T_2(x_2)$ . Block  $A_2[y_2]$  is guaranteed to be on  $T_2(x_2)$ . Then one computes  $x_3 \leftarrow \text{select}(A_2[y_2], b_2)$ , and continues in this way. In the end, one will traverse path  $T_L(x_L)$  and will eventually retrieve block  $A_L[y]$ . See Figure 1.

**Updating the paths.** Once the above process finishes, we need to make sure that we do not access the same leaf-to-root paths in case we access  $A_L[y]$  again in the future. Thus we set  $\text{select}(A_i[y_i], b_i) \leftarrow r_{i+1}$ , where  $r_{i+1}$  is a fresh random number that replaces  $x_{i+1}$  from above, and we ensure  $A_{i+1}[y_{i+1}]$  gets moved to a node on the leaf-to-root path  $T_{i+1}[r_{i+1}]$ . This is achieved by reading  $T_i(x_i)$  into a local data structure  $C_i$  called *stash*, and by *evicting* blocks from  $C_i$  based on the new assignments. In our abstraction, the stash  $C_i$  is viewed as an extension of the root of tree  $T_i$ . We note here that the details of the eviction process are not important to us, besides the fact that only changes along the read paths need to be made.

**Syntax.** A *Path-ORAM* consists of three procedures (INITIALIZE, EXTRACT, UPDATE) with syntax:

- $\mathcal{T} \leftarrow \text{INITIALIZE}(1^\kappa, A_L)$ : Given a security parameter  $\kappa$  and memory  $A_L$  as input, SETUP outputs a set of  $L - 1$  trees  $\mathcal{T} = \{T_2, T_3, \dots, T_L\}$  and an array of two entries  $A_1$ .  $A_1$  is stored locally with the client and  $T_2, \dots, T_L$  are stored with the server.
- $x_{i+1} \leftarrow \text{EXTRACT}(i, y, T_i(x_i))$  for  $i = 2, \dots, L$ . Given the tree number  $i$ , the final memory location of interest  $y$  and a leaf-to-root path  $T_i(x_i)$  (that starts from leaf  $x_i$ ) in tree  $T_i$ , EXTRACT outputs an index  $x_{i+1}$  to be read in the next tree  $T_{i+1}$ . The



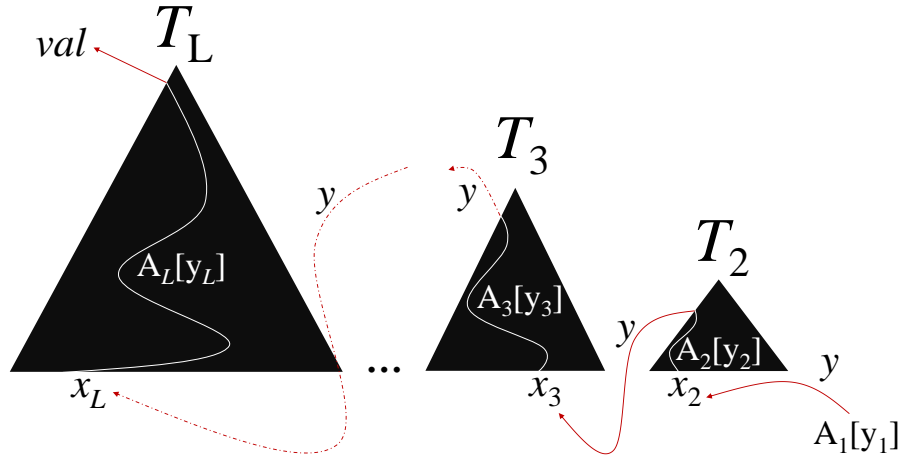


Fig. 1: Our Path-ORAM abstraction for reading a value  $val = A_L[y]$ .  $A_1[y_1]$  is read from local storage and defines  $x_2$ .  $x_2$  defines a path  $p_2$  in  $T_2$ . By traversing  $p_2$  the algorithm will retrieve  $A_2[y_2]$ , which will yield  $x_3$ , which defines a path  $p_3$  in  $T_3$ . Repeating this process yields a path  $p_L$  in  $T_L$ , traversing which yields the final value  $A_L[y_L] = A_L[y]$ . Note that  $y$  is passed from tree  $T_{i-1}$  to tree  $T_i$  so that the index  $y_i$  (and the bit  $b_i$ ) can be computed for searching for the right block on path  $p_i$ .

client can obtain  $x_2$  from local storage as  $x_2 \leftarrow \text{select}(A_1[y_1], b_1)$ . The obtained value  $x_2$  is sent to the server in order for the server to continue execution. Finally, the server outputs  $x_{L+1}$ , which is the desired value  $A_L[y]$ .

**EXTRACTBUCKET algorithm.** In Path-ORAM [35], internal nodes of the trees store more than one block  $(z, A_i[z])$ , in the form of *buckets*. We note that EXTRACT can be broken to work on individual buckets along a root-to-leaf path in a tree  $T_i$ . In particular, we can define the algorithm  $\pi \leftarrow \text{EXTRACTBUCKET}(i, y, b)$  where  $i$  is the tree of interest,  $y$  is the memory location that needs to be accessed, and  $b$  is a bucket corresponding to a particular node on the leaf-to-root path.  $\pi$  will be found at one of the nodes on the leaf-to-root path. Note that the algorithm EXTRACT can be implemented by repeatedly calling EXTRACTBUCKET for every  $b$  on  $T_i(x_i)$ .

- $\{A_1, T_2(x_2), \dots, T_L(x_L)\} \leftarrow \text{UPDATE}(y, op, val, A_1, T_2(x_2), \dots, T_L(x_L))$ . Procedure UPDATE takes as input the leaf-to-root paths (and the local storage) that were traversed during the access and accordingly updates these paths (and the local storage). Additionally, UPDATE ensures the new value  $val$  is written to  $A_L[y]$ , if operation  $op$  is a “write” operation.

An implementation of the above abstractions, for Path-ORAM [35], is given in Algorithms 1, 2, 3 in Appendix A.1. Note that the description of the UPDATE procedure [35] abstracts away the details of the eviction strategy. The SETUP and OBLIVIOUSACCESS protocols of the interactive Path-ORAM using these abstractions are given in Figures 6 and 7 respectively in the Appendix A.2. It is easy to see that the OBLIVIOUSACCESS

protocol has  $\log n$  rounds of interactions. By the proof of Stefanov et al. [35], we get the following:

**Corollary 1.** *The protocols SETUP and OBLIVIOUSACCESS from Figures 6 and 7 respectively in Appendix A.2 comprise a  $O(\log n)$ -round secure ORAM scheme (as defined in Section 2.2), assuming the encryption scheme used is CPA-secure.*

We recall that the bandwidth overhead for Path-ORAM [35] is  $O(\log^3 n)$  bits and the client storage is  $O(\log^2 n) \cdot \omega(1)$  bits, for a block size of  $2L = 2 \log n$  bits.

### 3.3 From $\log n$ rounds to two rounds

Existing Path-ORAM protocols implementing our abstraction require  $\log n$  rounds (see OBLIVIOUSACCESS protocol in Figure 7). The main reason for that is the following: In order for the server to figure out the index of leaf  $x_i$  from which the next path traversal begins, the server needs to access  $A_{i-1}[y_{i-1}]$ , which is stored *encrypted* at some node on the path starting from leaf  $x_{i-1}$  in tree  $T_{i-1}$ —see Figure 1. Therefore the server has to return all encrypted nodes on  $T_{i-1}(x_{i-1})$  to the client, who performs the decryption locally, searches for  $A_{i-1}[y_{i-1}]$  (via the EXTRACTBUCKET procedure) and returns the value  $x_i$  to the server (see Line 10 of the OBLIVIOUSACCESS protocol in Figure 7).

**Our approach.** To overcome this difficulty, we do not encrypt the blocks in the buckets. Instead, for each bucket stored at a tree node  $u$ , we prepare a garbled circuit that hard-codes, among other things, the blocks that are contained in the bucket. Subsequently, this garbled circuit executes the EXTRACTBUCKET algorithm on the hardcoded blocks and outputs either  $\perp$  or the next leaf index  $\pi$ , depending on whether the search performed by EXTRACTBUCKET was successful or not. The output, whatever that is, is fed as a garbled input to either the left child bucket or the right child bucket (depending on the currently traversed path) or the next root bucket (in case  $u$  is a leaf) of  $u$ . In this way, by the time the server has executed all the garbled circuits along the currently traversed path, he will be able to pass the index  $\pi$  to the next tree as a garbled input, and continue the execution in the same way without having to interact with the client. Therefore the client can obviously retrieve his value  $A_L[y]$  in only two rounds of communication.

Unfortunately, once these garbled circuits have been consumed, they cannot be used again since this would violate security of garbled circuits. To avoid this problem, the client downloads all the data that was accessed before, decrypts them, runs the UPDATE procedure locally, recomputes the garbled circuits that were consumed before, and stores the new garbled circuits locally. In the next access, these garbled circuits will be sent along with the query. Therefore the total number of communication rounds is equal to two (note that this approach requires permanent client storage—for transient storage, the client will have to send the garbled circuits immediately which would increase the rounds to three). We now continue with describing the bucket circuit that needs to be garbled for our construction.

**Naive bucket circuit.** To help the reader, in Figure 2 we describe a naive version of our bucket circuit that leads to an inefficient construction. Then we give the full-fledged

<p><b>Circuit</b> <math>C[u, \text{bucket}, \text{leftInputs}, \text{rightInputs}, \text{nState}](\text{cState})</math>  <b>Hardcoded parameters:</b> <math>[u = (i, j, k), \text{bucket}, \text{leftInputs}, \text{rightInputs}, \text{nState}]</math>.  <b>Inputs:</b> <math>\text{cState} = (p, y, \pi)</math>.  <b>Outputs:</b> Next node to be executed and garbled inputs for its bucket circuit, or final value <math>A_L[y]</math>.</p> <hr/> <pre> 1: <b>if</b> <math>\pi = \perp</math> <b>then</b> 2:   Set <math>\pi \leftarrow \text{EXTRACTBUCKET}(i, y, \text{bucket});</math>           <math>\triangleright \pi</math> will be the desired value <math>x_{i+1}</math> or <math>A_L[y]</math>. 3: <b>end if</b> 4: <b>if</b> <math>u</math> is not a leaf <b>then</b> 5:   Based on <math>p</math>, <b>return</b> either <math>(\text{left}(u), \text{leftInputs}_{(p,y,\pi)})</math> or <math>(\text{right}(u), \text{leftInputs}_{(p,y,\pi)})</math>; 6: <b>else</b> 7:   <b>if</b> <math>i = L</math> <b>then</b> 8:     <b>return</b> <math>\pi;</math>   <math>\triangleright</math> Found <math>A_L[y] = \pi</math>. 9:   <b>else</b> 10:    <b>return</b> <math>(\text{nextRoot}(u), \text{nState}_{(\pi,y,\perp)})</math>;           <math>\triangleright</math> Return garbled inputs for next root. 11:   <b>end if</b> 12: <b>end if</b> </pre>
--

Fig. 2: Formal description of the naive bucket circuit. Notation: Given  $\text{lab}$ , the set of *input labels* for a garbled circuit, we let  $\text{lab}_a$  denote the garbled input labels (i.e., the labels taken from  $\text{lab}$ ) corresponding to the input value  $a$ .

description of our bucket circuit in Figure 3. The naive bucket circuit has *hardcoded parameters, inputs and outputs*, which we detail in the following.

**Hardcoded parameters.** The circuit for node  $u$  hardcodes:

1. The node identifier  $u$  that consists of a triplet  $(i, j, k)$  where
  - $i \in \{2, \dots, L\}$  is the tree number where node  $u$  belongs to;
  - $j \in \{0, \dots, 2^{i-1}\}$  is the depth of node  $u$ ;
  - $k \in \{0, \dots, 2^j - 1\}$  is the order of node  $u$  in the specific level.

For example, the root of tree  $T_3$  will be denoted  $(3, 0, 0)$ , while its right child will be  $(3, 1, 1)$ .
2. The bucket information  $\text{bucket}$  (i.e., blocks  $(x, A_i[x], r)$  contained in node  $u$ —recall  $r$  is the path index in  $T_i$  assigned to  $A_i[x]$ );
3. The set of *input labels*  $\text{leftInputs}$ ,  $\text{rightInputs}$  and  $\text{nState}$  that can be used to compute the *garbled inputs* for the next circuit to be executed. Note that  $\text{leftInputs}$  and  $\text{rightInputs}$  are used to prepare the next garbled inputs when node  $u$  is an internal node (to go either to the left or the right child), while  $\text{nState}$  is used when node  $u$  is a leaf (to go to the next root).

**Inputs.** The inputs of the circuit is a triplet  $\text{cState}$  (current state), consisting of the following information:

1. The index of the leaf  $p$  from which the currently explored path begins;
2. The final location to be accessed  $y$ ;
3. The output from previous bucket  $\pi$  (can be the actual value of the next index to be explored or  $\perp$ ).

<b>Circuit</b> $C[u, \text{bucket}, \text{leftInputs}, \text{rightInputs}](\text{cState}, \text{nState})$ <b>Hardcoded parameters:</b> $[u = (i, j, k), \text{bucket}, \text{leftInputs}, \text{rightInputs}]$ . <b>Inputs:</b> $\text{cState} = (p, y, \pi), \text{nState}$ . <b>Outputs:</b> Next node to be executed and garbled inputs for its bucket circuit, or final value $A_L[y]$ .	
<hr/> 1: <b>if</b> $\pi = \perp$ <b>then</b> 2:     Set $\pi \leftarrow \text{EXTRACTBUCKET}(i, y, \text{bucket});$ $\triangleright \pi$ will be the desired value $x_{i+1}$ or $A_L[y]$ . 3: <b>end if</b> 4: <b>if</b> $u$ is not a leaf <b>then</b> 5:     Based on $p$ , <b>return</b> either $(\text{left}(u), \text{leftInputs}_{(p, y, \pi, \text{nState})})$ or $(\text{right}(u), \text{leftInputs}_{(p, y, \pi, \text{nState})})$ ; 6: <b>else</b> 7: <b>if</b> $i = L$ <b>then</b> 8: <b>return</b> $\pi;$ $\triangleright$ Found $A_L[y] = \pi$ . 9: <b>else</b> 10: <b>return</b> $(\text{nextRoot}(u), \text{nState}_{(\pi, y, \perp)});$ $\triangleright$ Return garbled inputs for next root. 11: <b>end if</b> 12: <b>end if</b>	

Fig. 3: Formal description of the final bucket circuit.

**Outputs.** The outputs of the circuit are the next node to be executed, along with its garbled inputs. For example, if the current node  $u$  is not a leaf (see Lines 4 and 5 in Figure 2), the circuit outputs the garbled inputs of either the left or the right child, whereas if the current node is a leaf (see Lines 6-12 in Figure 2), the circuit outputs the garbled inputs of the next root to be executed. Note that outputting the garbled inputs is easy, since the bucket circuit hardcodes the input labels of the required circuits.

**Final bucket circuit.** In the naive circuit presented before, we hardcode the input labels of the root node root of every tree  $T_i$  into all the nodes/circuits of tree  $T_{i-1}$ . Unfortunately, in every oblivious access, the garbled circuits of all roots are consumed (and therefore root's circuit as well), hence *all* the garbled circuits of tree  $T_{i-1}$  will have to be recomputed from scratch. This cost is  $O(n)$ , thus very inefficient. We would like to minimize the number of circuits in  $T_{i-1}$  that need to be recomputed and ideally make this cost proportional to  $O(\log n)$ .

To achieve that, we observe that, *instead of hardcoding the input labels*  $\text{nState}$  *in the garbled circuit of every node of tree*  $T_{i-1}$ , *we can just pass them as inputs*. The final circuit is given in Figure 3. Note that the only difference of the new circuit from the naive circuit is in the computation of the garbled inputs  $\text{leftInputs}_{(p, y, \pi, \text{nState})}$  and  $\text{rightInputs}_{(p, y, \pi, \text{nState})}$ , where  $\text{nState}$  is added in the subscript (see Line 5 of both Figure 3 and Figure 2).

### 3.4 Protocols SETUP and OBLIVIOUSACCESS of our construction

We now describe in detail the SETUP and OBLIVIOUSACCESS protocols of TWORAM.

**SETUP.** The SETUP protocol is described in Figure 4. Just like the setup for the interactive ORAM protocol (see Figure 6 in Appendix A.2), in TWORAM, the client does

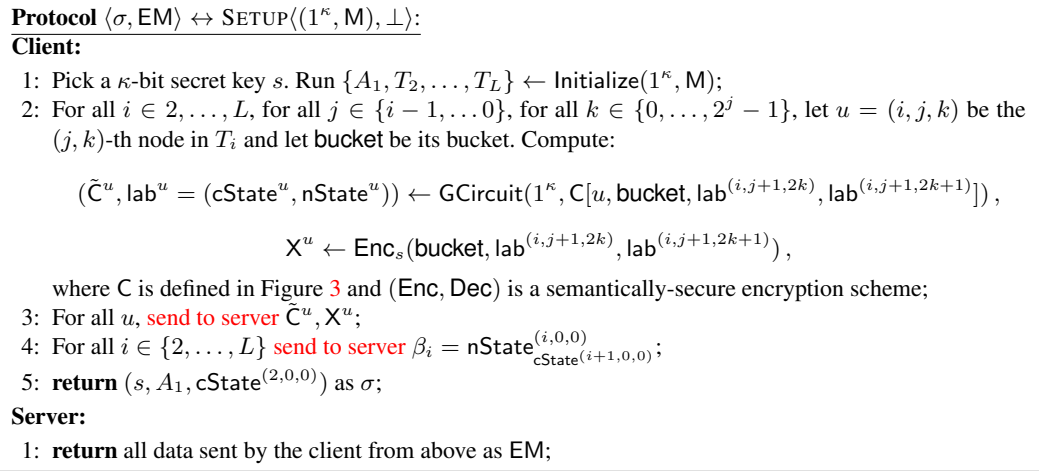


Fig. 4: SETUP protocol for TWORAM.

some computation locally in the beginning (using his secret key) and then outputs some “garbled information” that is being sent to the server. In particular:

1. After producing the trees  $T_2, T_3, \dots, T_L$  using algorithm INITIALIZE, the client prepares the garbled circuit of Figure 3 for all the nodes  $u \in T_i$ , for all trees  $T_i$ . It is important this computation takes place from the leaves towards the root (that is why we write  $j \in \{i-1, \dots, 0\}$  in Line 2 of Figure 4), since a garbled circuit of a node  $u$  hardcodes the input labels of the garbled circuits of its children—so these need to be readily available by the time  $u$ ’s garbled circuit is computed.
2. Apart from the garbled circuits, the client needs to prepare garbled inputs for the  $\text{nState}$  inputs of all the roots of the trees  $T_i$  (recall, that  $\text{nState}$  contains the set of input labels for the  $\text{cState}$  of the root of tree  $T_{i+1}$ , which are only initialized by the bucket circuits of the leaves of tree  $T_i$ ). These are essentially the  $\beta_i$ ’s computed in Line 4 of Figure 4.

**OBLIVIOUSACCESS.** The OBLIVIOUSACCESS protocol of TWORAM is described in Figure 5. The first step of the protocol is similar to that of the interactive scheme (see Figure 7 in Appendix), where the client accesses local storage  $A_1$  to compute the path index  $x_2$  that must be traversed in  $T_2$ . However, the main difference is that, instead of sending  $x_2$  directly, the client sends the *garbled input that corresponds to  $x_2$*  for the root circuit of tree  $T_2$ , denoted with  $\alpha$  in Figure 5.

We note here that  $\alpha$  is not enough for the first garbled circuit to start executing, and therefore the server complements this garbled input with  $\beta_2$  (see Server Line 1), the other half that was sent by the client before and that represents the input labels of the next root. Subsequently, the server starts executing the garbled circuits one-by-one, using the outputs of the first circuit, as garbled inputs to the second one, and so on. Eventually, the clients retrieves all the paths  $T_i(x_i)$ , and by decrypting  $T_L(x_L)$ , the client can retrieve the value he was looking for (see Client Line 2). Finally, the client

**Protocol**  $\langle\langle M[y], \sigma' \rangle, EM' \rangle \leftrightarrow \text{OBLIVIOUSACCESS}(\langle \sigma, y, val \rangle, EM)$ :

**Client:**

- 1: Compute  $x_2 \leftarrow \text{select}(A_1[y_1], b_1)$  and **send to server**  $\alpha = \text{cState}_{(x_2, y, \perp)}^{(2,0,0)}$ ;

**Server:**

- 1: Let  $\text{output} = \alpha || \beta_2$ , where  $\beta_2$  is defined in Line 4 of Protocol SETUP in Figure 4;
- 2: Set  $i = 2$  and  $j = 0$  and  $k = 0$ ;
- 3: **while**  $i \leq L$  **do**
- 4:    $(\text{nextNode}, \text{output}) \leftarrow \text{Eval}(\tilde{C}^{(i,j,k)}, \text{output})$ ;
- 5:   **if**  $j = i - 1$  and  $i < L$  **then** ▷ node  $(i, j, k)$  is a leaf of trees  $T_2, T_3, \dots, T_{L-1}$
- 6:      $i = i + 1$ ; ▷ go to next tree
- 7:      $\text{output} = \text{output} || \beta_i$ ; ▷ prepare the garbled inputs for the root of the next tree
- 8:   **end if**
- 9:   **if**  $\text{nextNode} = \text{left}(i, j, k)$  **then** ▷ node  $(i, j, k)$  is not a leaf; decide whether to go next
- 10:      $j = j + 1$ ;  $k = 2k$ ; ▷ go to left child
- 11:   **else**
- 12:      $j = j + 1$ ;  $k = 2k + 1$ ; ▷ go to right child
- 13:   **end if**
- 14: **end while**
- 15: Let  $x_2, \dots, x_L$  be the indices of the paths that have been accessed above;
- 16: **Send to client**  $\text{output}$  and all  $X^u$ 's corresponding to nodes  $u$  on paths  $T_2(x_2), T_3(x_3), \dots, T_L(x_L)$ ;

**Client:**

- 1: Decrypt all information contained in  $X^u$  and reconstruct  $T_2(x_2), T_3(x_3), \dots, T_L(x_L)$ ;
- 2: Retrieve block  $(y, A_L[y], x_L)$  from  $T_L(x_L)$ ; Set  $val = A_L[y]$ ;
- 3: Run  $\{A_1, T_2(x_2), \dots, T_L(x_L)\} \leftarrow \text{UPDATE}(y, op, val, A_1, T_2(x_2), \dots, T_L(x_L))$ ;
- 4: For all  $i \in \{2, \dots, L\}$ , for all  $j \in \{i - 1, \dots, 0\}$ , let  $u = (i, j, k)$  be the  $(j, k)$ -th node in  $T_i(x_i)$  and let bucket be its bucket. Compute:
 
$$(\tilde{C}^u, \text{lab}^u = (\text{cState}^u, \text{nState}^u)) \leftarrow \text{GCircuit}(1^\kappa, C[u, \text{bucket}, \text{lab}^{(i,j+1,2k)}, \text{lab}^{(i,j+1,2k+1)}]),$$

$$X^u \leftarrow \text{Enc}_s(\text{bucket}, \text{lab}^{(i,j+1,2k)}, \text{lab}^{(i,j+1,2k+1)}),$$
 where  $C$  is defined in Figure 3. **Send to server**  $\tilde{C}^u$  and  $X^u$ ;
- 5: Store locally  $(s, A_1, \text{cState}^{(2,0,0)})$  as  $\sigma'$ ;
- 6: For all  $i \in \{2, \dots, L\}$  **send to server**  $\beta_i = \text{nState}_{\text{cState}^{(i+1,0,0)}}^{(i,0,0)}$ ;
- 7: **return**  $(val, \sigma')$ .

**Server:**

- 1: **return** the data received by the client as  $EM'$ .

Fig. 5: OBLIVIOUSACCESS protocol for TWORAM.

runs the UPDATE procedure, re-garbles the circuits that got consumed and either sends them to the server, or waits until the next query (in which case TWORAM is two-round with permanent storage). We can now state the main result of our paper.

**Theorem 1.** *The protocols SETUP and OBLIVIOUSACCESS from Figures 4 and 5 respectively comprise a two-round secure ORAM scheme (as defined in Section 2.2), assuming the garbling scheme used is secure (as defined in Section 2.1) and the encryption scheme used is CPA-secure.*

The proof of the above theorem can be found in Appendix A.3. Concerning complexity, it is clear that the only overhead that we are adding on Path-ORAM [35] is a garbled circuit per bucket—this adds a multiplicative security parameter factor on all the complexity measures of Path-ORAM. E.g., the bandwidth overhead of our construction is  $O(\kappa \cdot \log^3 n)$  bits (for blocks of  $2 \log n$  bits).

### 3.5 Optimizations

Recall that in the garbling procedure of a circuit  $C$ , one has the following choices: (i) either to garble  $C$  in a way that during evaluation of the garbled circuit on  $x$  the output is the cleartext value  $C(x)$ ; (ii) or to garble  $C$  in a way that during evaluation of the garbled circuit on  $x$  the output is the garbled labels corresponding to the value  $C(x)$ . We now describe an optimization for a specific circuit  $C$  that we will be using in our construction that uses the above observation.

**General optimization.** Consider a circuit that performs the following task: It hardcodes two  $k$ -bit strings  $s_0$  and  $s_1$ , takes an input a bit  $b$  and outputs  $s_b$ . This cleartext circuit has size  $O(k)$ , so the garbled circuit for that will have size  $O(k^2)$ . To improve upon that we consider a circuit  $C'$  that takes as input bit  $b$  and outputs the same bit  $b$ ! This cleartext circuit has size  $O(1)$ . However, to make sure that the output of the garbled version of  $C'$  is always  $s_b$ , we garble  $C'$  by outputting the garbled label corresponding to  $b$ , namely  $s_b$  (i.e., using (ii) from above). In particular, during the garbling procedure we use  $s_0$  as the garbled label output for output  $b = 0$  and we use  $s_1$  as the garbled label output for the output  $b = 1$ . Note that the size of the new garbled circuit has size  $O(k)$ , yet it has exactly the same I/O behavior with the garbling of  $C$ , which has size  $O(k^2)$ .

- **Improving cState—not hard-coding input labels inside the bucket circuit.** In the construction we described, we include the input labels `leftInputs`, `rightInputs` in the circuit `C[u, bucket, leftInputs, rightInputs]`. Consequently, the size of the ungarbled version of this circuit grows with the size of `leftInputs` and `rightInputs` which is  $\kappa \cdot |\text{cState}|$ . We can easily use the general optimization described above, for each bit of `|cState|`, to make the size of the ungarbled version of our circuit only grow with `|cState|`.
- **Improving nState—input labels passing.** In the construction described previously, for each tree, an input value `nState` is passed from the root to a leaf node in the tree. However this value is used only at the leaf node. Recall that the `nState` value passed from the root to a leaf garbled circuits in the tree  $T_i$  is exactly the value  $\text{cState}^{i+1,0,0}$ , the input labels of the root garbled circuit of the tree  $T_{i+1}$ . Since each ungarbled circuit gets this value as input, therefore each of one of them needs to grow with  $\kappa \cdot |\text{cState}|$ .<sup>3</sup> We will now describe an optimization such that the size of the garbled version, rather than the clear version, grows linearly in  $\kappa \cdot |\text{cState}|$ . Note that in our construction the value  $\text{cState}^{i+1,0,0}$  is not used at all in the intermediate circuits as it gets passed along the garbled circuits for tree  $T_i$ . In order to avoid this wastefulness, for all nodes  $i \in \{1, \dots, L\}, j \in [i], k \in [2^j]$  we sample a value  $r^{(i,j,k)}$  of length  $\kappa \cdot |\text{cState}|$  and hardcode the values  $r^{(i,j,k)} \oplus r^{(i,j+1,2k)}$

<sup>3</sup> This efficiency is achieved when the first optimization is used.



and  $r^{(i,j,k)} \oplus r^{(i,j+1,2k+1)}$  inside the garbled circuit  $\tilde{C}^{i,j,k}$  which output the first of two values if the execution goes left and the second if the execution goes right. Note that a garbled circuits grows only additively in  $\kappa \cdot |\text{cState}|$  because of this change. This follows by using the first optimization. Additionally, we include the value  $\text{cState}^{i+1,0,0} \oplus r^{(i,0,0)}$  with the root node of the tree  $T_i$ . The leaf garbled circuit  $(i, i-1, k)$  in tree  $T_i$  is constructed assuming  $r^{(i,i-1,k)}$  is the sequence of input labels for the root garbled circuit of the tree  $T_{i+1}$ .<sup>4</sup> Let  $\alpha_0, \dots, \alpha_{i-1}$  be the strings output during the root to a leaf traversal in tree  $T_i$ . Now observe that  $\text{cState}^{i+1,0,0} \oplus r^{(i,0,0)} \oplus_{j \in [i]} \alpha_j$  is precisely  $\text{cState}^{i+1,0,0} \oplus r^{(i,i-1,k)}$  where  $k$  is the leaf node in the traversed path. At this point it is easy to see that given the output of the leaf garbled circuit for tree  $T_i$  one can compute the required input labels for the root of tree  $T_{i+1}$ .

The update mechanism in our construction can be easily adapted to work with this change. Here note that we would now include the values  $r^{(i,j,k)}, r^{(i,j+1,2k)}$  and  $r^{(i,j+1,2k+1)}$  in the ciphertext  $X^{(i,j,k)}$ . Also note that we will use fresh  $r^{(\cdot,\cdot,\cdot)}$  values whenever a fresh garbled circuit for a node is generated. The security argument now additionally uses the fact that the outputs generated by garbled circuits in two separate root to leaf traversals depend on completely independent  $r^{(\cdot,\cdot,\cdot)}$  values.

Note that the above modification leaks what value is passed by the executed leaf garbled circuit in tree  $T_i$  to the root garbled circuit in tree  $T_{i+1}$ . This can be deduced based on what bit values of  $\text{cState}^{i+1,0,0} \oplus r^{(i,0,0)}$  are revealed. This can be tackled by randomly permuting the labels in  $\text{cState}^{i+1,0,0}$  and passing the information on this permutations along with in the tree to leaf garbled circuits. Note that the size of this information is small.

Taken together these two optimizations reduce the size of each garbled circuit to  $O(\kappa \cdot (|\text{bucket}| + |\text{cState}|))$ . Since  $|\text{bucket}| > |\text{cState}|$  this expression reduces to  $O(\kappa \cdot |\text{bucket}|)$ . This implies that the overhead of our construction is just  $\kappa$  times the overhead of the underlying Path ORAM scheme.

## 4 Searchable encryption construction using TWORAM

The natural way of designing an SSE scheme that does not leak the search and access patterns using an ORAM scheme is to first use a data structure for storing keyword-document pairs, setup the data structure in memory using an ORAM setup and then read/write from it using ORAM operations. Since ORAM hides the read/write access patterns, but it does not hide the number of memory accesses, one needs to ensure that in the data structure used, the number of memory accesses for each operation is data-independent. Fortunately, there are hash table implementations that have this property and our abstraction of a hash table captures this as well.

We start by giving some definitions and then we describe constructions that can be instantiated using any ORAM scheme. We then show how to obtain a significantly

<sup>4</sup> Note that here the first optimization allows us to ensure that the size of the garbled leaf circuit, rather than the clear leaf circuit, grows with the length of  $r^{(i,i-1,k)}$  as these hard-codings are performed.

more efficient instantiation using a combination of TWORAM and a non-recursive Path-ORAM scheme.

**Hash tables definition.** A hash table is a data structure commonly used for mapping keys to values [7]. It often uses a hash function  $h$  that maps a key to an index (or a set of indices) in a memory array  $M$  where the value associated with the key may be found. In particular,  $h$  takes as input a keyword  $key$  and outputs a set of indices  $i_1, \dots, i_c$  for a fixed constant  $c$ . The value associated with  $key$  is in one of the locations  $M[i_1], \dots, M[i_c]$ . The keyword is not in the table if it is not in one of those locations. Similarly, to write a new  $(key, value)$  pair into the table,  $(key, value)$  is written into the first empty location among  $i_1, \dots, i_c$ . More formally, we define a hash table  $H = (hsetup, hlookup, hwrite)$  using a tuple of algorithms and a parameter  $c$  denoting an upper bound on the number of locations to search.

- $(h, M) \leftrightarrow hsetup(S, size)$ :  $hsetup$  takes as input an initial set  $S$  of keyword-value pairs and a maximum table size  $size$  and outputs a hash function  $h$  and a memory array  $M$ .
- $value \leftrightarrow hlookup(key)$ :  $hlookup$ , computes  $i_1, \dots, i_c \leftrightarrow h(key)$ , looks for a key-value pair  $(key, \cdot)$  in  $M[i_1], \dots, M[i_c]$ . If such a pair is found it returns the second component of the pair (i.e., the value), else it returns  $\perp$ .
- $hwrite(key, value)$ :  $hwrite$  computes  $i_1, \dots, i_c \leftrightarrow h(key)$ , if  $(key, value)$  already exists in one of those indices in  $M$  it does nothing, else it stores  $(key, value)$  in the first empty index.

**Searchable encryption definition.** A database  $D$  is a set of document/keyword-set pair

$$DB = (d_i, W_i)_{i=1}^N.$$

Let  $W = \cup_{i=1}^N W_i$  be the universe of keywords. A keyword search query for  $w$  should return all  $d_i$  where  $w \in W_i$ . We denote this subset of  $DB$  by  $DB(w)$ . A searchable symmetric encryption scheme consists of protocols  $SSE_{SETUP}$ ,  $SSE_{SEARCH}$  and  $SSE_{ADD}$ . The following formalization first appeared in [8,6].

- $\langle \sigma, EDB \rangle \leftrightarrow SSE_{SETUP}(\langle 1^\kappa, DB \rangle, \perp)$ :  $SSE_{SETUP}$  takes as client's input a database  $DB$  and outputs a secret state  $\sigma$  (for the client), and an encrypted database  $EDB$  which is outsourced to the server.
- $\langle (DB(w), \sigma'), EDB' \rangle \leftrightarrow SSE_{SEARCH}(\langle \sigma, w \rangle, EDB)$ :  $SSE_{SEARCH}$  is a protocol between the client and the server, where client's input is the secret state  $\sigma$  and the keyword  $w$  he is searching for. Server's input is the encrypted database  $EDB$ . Client's output is the set of documents containing  $w$ , i.e.  $DB(w)$  as well as an updated secret state  $\sigma'$  and the server obtains an updated encrypted database  $EDB'$ .
- $\langle \sigma', EDB' \rangle \leftrightarrow SSE_{ADD}(\langle \sigma, d \rangle, EDB)$ :  $SSE_{ADD}$  is a protocol between the client and the server, where client's input is the secret state  $\sigma$  and a document  $d$  to be inserted into the database. Server's input is the encrypted database  $EDB$ . Client's output is an updated secret state  $\sigma'$  and the server's output is an updated encrypted database  $EDB'$  which now contains the new document  $d$ .

*Correctness.* Consider the following correctness experiment. An adversary  $A$  chooses a database  $DB$ . Consider the encrypted database  $EDB$  generated using  $SSESETUP$  (i.e.,  $\langle EDB, K \rangle \leftrightarrow SSESETUP(\langle 1^\kappa, DB \rangle, \perp)$ ). The adversary then adaptively chooses keywords to search and documents to add to the database. Denote the searched keywords by  $w_1, \dots, w_t$ .  $A$  wins in the correctness game if  $\langle (DB_i(w_i), \sigma_i), EDB_i \rangle \neq SSESEARCH(\langle (\sigma_{i-1}, w_i), EDB_{i-1} \rangle)$  for any  $1 \leq i \leq t$ , where  $DB_i, EDB_i$  are the database and encrypted database, respectively, after the  $i$ -th search, and  $SSESEARCH$  and  $SSEADD$  are run between an honest client and server. The SSE scheme is correct if probability of  $A$  in winning the game is negligible in  $\kappa$ .

*Semi-Honest Security.* Security of SSE schemes is parametrized by a leakage function  $\mathcal{L}$ , which explains what the adversary (the server) learns about the database and the search queries, while interacting with a secure SSE scheme. A SSE Scheme is  $\mathcal{L}$ -secure if for any PPT adversary  $A$ , there exist a simulator  $Sim$  such that the following two distributions are computationally indistinguishable.

- $Real_A(\kappa)$ :  $A$  chooses  $DB$ . The experiment then runs

$$\langle EDB, \sigma \rangle \leftrightarrow SSESETUP(\langle 1^\kappa, DB \rangle, \perp).$$

$A$  then adaptively makes search queries  $w_i$ , which the experiment answers by running the protocol  $\langle DB_{i-1}(w_i), \sigma_i \rangle \leftrightarrow SSESEARCH(\langle (\sigma_{i-1}, w_i), EDB_{i-1} \rangle)$ . Denote the full transcripts of the protocol by  $t_i$ . Add queries are handled in a similar way. Eventually, the experiment outputs  $(EDB, t_1, \dots, t_q)$  where  $q$  is the total number of search/add queries made by  $A$ .

- $Ideal_{A, Sim, \mathcal{L}}(\kappa)$ :  $A$  chooses  $DB$ . The experiment runs  $(EDB', st_0) \leftrightarrow Sim(\mathcal{L}(DB))$ . On any search query  $w_i$  from  $A$ , the experiment adds  $(w_i, search)$  to the history  $H$ , and on an add query  $d_i$  it adds  $(d_i, add)$  to  $H$ . It then runs  $(t'_i, st_i) \leftrightarrow Sim(st_{i-1}, \mathcal{L}(DB_{i-1}, H))$ . Eventually, the experiment outputs  $(EDB', t_1, \dots, t'_q)$  where  $q$  is the total number of search/add queries made by  $A$ .

**Leakage.** The level of security one obtains from a SSE scheme depends on the leakage function  $\mathcal{L}$ . Ideally  $\mathcal{L}$  should only output the total number  $\sum_{w \in W} |DB(w)|$  of  $(w, d)$  pairs, the total number of unique keywords  $|W|$  and  $|DB(w)|$  for any searched keyword  $w$ . Achieving this level of security is only possible if the  $SSESEARCH$  operation outputs the documents themselves to the client. If instead (as is common for applications with large document sizes), it returns document identifiers which the client then uses to retrieve the actual documents, any SSE protocol would also leak the access pattern.

#### 4.1 SSE from any ORAM

*First approach.* The common way of storing a database of documents in a hash table is to insert a key-value pair  $(w, d)$  into the table for any keyword  $w$  in a document  $d$ . Searching for a document with keyword  $w$  then reduces to looking up  $w$  in the table. If there is more than one document containing a keyword  $w$ , a natural solution is to create a bucket  $B_w$  storing all the documents containing  $w$  and storing the bucket in position  $pt_w$  of an array  $A$ . One then inserts  $(w, pt_w)$  in a hash table. Now, to search for

a keyword  $w$ , we first look up  $(w, pt_w)$ , and then access  $A[pt_w]$  to obtain the bucket  $B_w$  of all the desired documents. A subtle issue is that the distribution of bucket sizes would leak information about the database even before any keyword is searched. As a result, for this approach to be fully-secure, one needs to pad each bucket to an upperbound on the number of searchable documents per keyword.

Next we describe the SSE scheme more formally. Given a hash table  $H = (\text{hsetup}, \text{hlookup}, \text{hwrite})$ , and an ORAM scheme  $ORAM = (\text{SETUP}, \text{OBLIVIOUSACCESS})$ , we construct an SSE scheme  $(\text{SSESETUP}, \text{SSESEARCH}, \text{SSEADD})$  as follows.

1.  $\langle \sigma, \text{EDB} \rangle \leftrightarrow \text{SSESETUP}(\langle 1^\kappa, \text{max}, \text{DB} \rangle, \perp)$ : Given an initial set of documents  $\text{DB}$ , client lets  $S$  be the set of key-value pairs  $(w, pt_w)$  where  $pt_w$  is an index to an array of buckets  $A$  such that  $A[pt_w]$  stores the bucket of all documents in  $\text{DB}$  containing  $w$ . Each bucket is padded to the maximum size  $\text{max}$  with dummy documents.

Client first runs  $\text{hsetup}(S, \text{size})$  to obtain  $(h, M)$ .  $\text{size}$  is the anticipated maximum size of hash table  $H$ . Then client and server run  $\langle \sigma_1, \text{EM} \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, M \rangle, \perp)$ . Client and server also run  $\langle \sigma_2, \text{EA} \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, A \rangle, \perp)$

Note that server's output is  $\text{EDB} = (\text{EM}, \text{EA})$  and client's output is  $\sigma = (\sigma_1, h, \sigma_2)$ .

2.  $\text{SSESEARCH}(\langle (\sigma, w), \text{EDB} \rangle)$ : Client computes  $i_1, \dots, i_c \leftarrow h(w)$ . Then, client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j, \text{null}), \text{EM} \rangle$  for  $j \in \{1, \dots, c\}$  for client to obtain  $M[i_j]$ . If client does not find  $(w, pt_w)$  in one of the retrieved locations it lets  $pt_w = 0$ , corresponding to a dummy access to the index 0 in  $A$ .

Client and server then run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, pt_w, \text{null}), \text{EA} \rangle)$  for client to obtain the bucket  $B_w$  stored in  $A[pt_w]$ . Client outputs all the non-dummy documents in  $B_w$ .

3.  $\text{SSEADD}(\langle (\sigma, d), \text{EDB} \rangle)$ : For every  $w$  in  $d$ , client computes  $i_1, \dots, i_c \leftarrow h(w)$  and client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j, \text{null}), \text{EM} \rangle$  for  $j \in \{1, \dots, c\}$  for client to obtain  $M[i_j]$ . If  $(w, pt_w)$  is in the retrieved locations let  $i_j^*$  be the location it was found at. If not, let  $pt_w$  be the first empty location in  $A$ , and let  $i_{*j}$  be the first empty location from the retrieved ones in  $M$ . Client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j^*, (w, pt_w)), \text{EM} \rangle$ .

Client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, pt_w, \text{null}), \text{EA} \rangle)$  to retrieve  $A[pt_w]$ . Let  $B_w$  be the retrieved bucket. Client inserts  $d$  in the first dummy entry of  $B_w$ , denoting the new bucket by  $B'_w$ . Client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, pt_w, B'_w), \text{EA} \rangle)$ .

The main disadvantage of the above construction is that we need to anticipate an upperbound on the bucket sizes, and pad all buckets to that size. Given that in practice there are often keywords that appear in a large number of documents, and keywords that only appear in a few, the padding will lead to inefficiency. Our next solution addresses this issue but instead has a higher round complexity.

*Second approach.* Instead of storing all documents matching a keyword  $w$  in one bucket, we store each of them separately in the hash table, using a different keyword. In particular, we can store the key-value pair  $(w||i, d)$  in the hash table for the  $i$ th document  $d$  containing  $w$ . This works fine except that it requires looking up  $w||\text{count}$  for an incremental counter  $\text{count}$  until the keyword is no longer found in the table.

To make this approach cleaner and the write operations more efficient, we maintain two hash tables, one for storing the counter representing the number of documents containing the keyword, and one storing the incremental key-value pairs as described above. To lookup a keyword  $w$ , one first looks up the counter  $count$  in the first table and then makes  $count$  lookup queries to the second table.

We describe the above SSE scheme in more detail below. Given a hash table  $H = (\text{hsetup}, \text{hlookup}, \text{hwrite})$  and an ORAM scheme  $ORAM = (\text{SETUP}, \text{OBLIVIOUSACCESS})$ , we construct an SSE scheme  $(\text{SSESETUP}, \text{SSESEARCH}, \text{SSEADD})$  as follows:

1.  $\langle \sigma, \text{EDB} \rangle \leftrightarrow \text{SSESETUP}(\langle 1^\kappa, \text{DB} \rangle, \perp)$ : Given an initial set of documents  $\text{DB}$ . Let  $S_1$  be the set of  $(w, count_w)$  pairs and  $S_2$  be the set of key-value pairs  $(w||i, d_i)$  for  $1 \leq i \leq count_w$  where  $count_w$  is the number of documents containing  $w$ , and  $d_i$  denotes the  $i$ th document in  $\text{DB}$  containing  $w$ .  
Client runs  $\text{hsetup}(S_i, size_i)$  to obtain  $(h_i, M_i)$ .  $size_i$  is the anticipated maximum size of the hash table  $H_i$ . Then client and server run  $\langle \sigma_i, \text{EM}_i \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, M_i \rangle, \perp)$ . Note that server's output is  $\text{EDB} = (\text{EM}_1, \text{EM}_2)$  and client's output is  $\sigma = (\sigma_1, \sigma_2, h_1, h_2)$ .
2.  $\text{SSESEARCH}(\langle (\sigma, w), \text{EDB} \rangle)$ : Client computes  $i_1, \dots, i_c \leftarrow h_1(w)$  and client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j, \text{null}), \text{EM}_1 \rangle)$  for  $j \in \{1, \dots, c\}$  for client to obtain  $(w, count_w)$  among the retrieved locations. If such a pair is not found, client lets  $count_w = 0$ .  
For  $1 \leq k \leq count_w$ , client computes  $i_1^k, \dots, i_c^k \leftarrow h_2(w||k)$  and client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, i_j^k, \text{null}), \text{EM}_2 \rangle)$  for  $j \in \{1, \dots, c\}$  for client to obtain  $M_2[i_j^k]$ . Client outputs  $d$  for all  $d$  where  $(w||k, d)$  is in the retrieved locations from  $M_2$ .
3.  $\text{SSEADD}(\langle (\sigma, d), \text{EDB} \rangle)$ : For every  $w$  in  $d$ , client computes  $i_1, \dots, i_c \leftarrow h_1(w)$  and client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j, \text{null}), \text{EM}_1 \rangle)$  for  $j \in \{1, \dots, c\}$  for client to obtain  $M_1[i_j]$ . If  $(w, count_w)$  is in the retrieved locations let  $i_j^*$  be the location it was found at. If not, let  $count_w = 0$  and let  $i_j^*$  be the first empty location from the retrieved ones. Client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_1, i_j^*, (w, count_w + 1)), \text{EM}_1 \rangle)$  to increase the counter by one.  
Client then computes  $i'_1, \dots, i'_c \leftarrow h_2(w||count_w + 1)$  and client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, i'_j, \text{null}), \text{EM}_2 \rangle)$  to retrieve  $M_2[i'_j]$  for  $j \in \{1, \dots, c\}$ . Let  $i'_k$  be the first empty location among them. Client and server run  $\text{OBLIVIOUSACCESS}(\langle (\sigma_2, i'_k, (w||count + 1)), \text{EM}_2 \rangle)$ .

The main disadvantage of our second approach is that for each search, it requires  $count_w$  ORAM accesses to retrieve all matching documents. This means that the bandwidth/computation overhead of ORAM scheme is multiplied by  $count_w$  which can be large for some keywords. More importantly, it would require  $O(count_w)$  rounds since the ORAM accesses cannot be parallelized in our constant-round ORAM construction. In particular, note that each memory garbled circuit in the construction can only be used once and needs to be replaced before the next memory access. Finally, the constant-round ORAM needs to store a memory array that is proportional to the number of  $(w, d)$  tuples associated with the database, which is significantly larger than the number of unique keywords, increasing the storage overhead of the resulting SSE scheme.

Next, we address all these efficiency concerns, showing a construction that only requires a single ORAM access using our constant-round construction.

## 4.2 SSE from Path-ORAM

The idea is to not only store a per-keyword counter  $count_w$  as before, but also to store a  $access_w$  that represents the number of search/add queries performed on  $w$  so far. Similar to the previous approach, the tuple  $(w, (count_w, access_w))$  is stored in a hash table that is implemented using our constant-round ORAM scheme TWORAM. The  $count_w$  is incremented whenever a new document containing  $w$  is added and the  $access_w$  is incremented after each search/add query for  $w$ .

The tuples  $(w||i, d_i)$  for all  $d_i$  containing  $w$  are then stored in a one-level (non-recursive) Path-ORAM. In order to avoid storing a large client-side position map for this non-recursive Path-ORAM, we generate/update the positions pseudorandomly using a PRF  $F_K(w||i||access_w)$ . Since each document  $d_i$  has a different index and each search/add query for  $w$  will increment  $access_w$ , the pseudorandomness property of  $F$  ensures that this way of generating the position maps is indistinguishable from generating them at random. Now the client only needs to keep the secret key  $K$ . Note that since we are using a one-level Path-ORAM to store the documents, we can handle multiple parallel accesses without any problems, hence obtaining a constant-round search/add complexity. Furthermore, we only access TWORAM (which uses garbled circuits) once per keyword search to retrieve the tuple  $(w, (count_w, access_w))$ , so TWORAM's overhead is not multiplied by  $count_w$  for each search/add query. Similarly, the storage overhead of TWORAM is only for a memory array of size  $|W|$  (number of unique keywords in documents) which is significantly smaller than the number of keyword-document pairs needed in the general approach.

We need to make a few small modifications to the syntax of the abstraction for Path-ORAM here. First, since we generate the position map on the fly using a PRF, it is convenient to modify the syntax of the UPDATE procedure to take the new random position as input, instead of internally generating it in our original syntax. Also, since we are not extracting an index  $y$  from the Path-ORAM and instead are extracting a tuple of the form  $(w||i, d_i)$ , we will pass  $w||i$  as input in place of  $y$  in the EXTRACT and UPDATE operations.

We now describe the SSE scheme in detail. Given a hash table  $H = (\text{hsetup}, \text{hlookup}, \text{hwrite})$ , our constant-round ORAM scheme  $\text{TWORAM} = (\text{SETUP}, \text{OBLIVIOUSACCESS})$ , a single-level (non-recursive) Path-ORAM scheme with procedures  $(\text{INITIALIZE}, \text{EXTRACT}, \text{UPDATE})$ , and a PRF function  $F$ , we construct an SSE scheme  $(\text{SSESETUP}, \text{SSESEARCH}, \text{SSEADD})$  as follows:

1.  $\langle \sigma, \text{EDB} \rangle \leftrightarrow \text{SSESETUP}(\langle 1^\kappa, \text{DB} \rangle, \perp)$ : Given an initial set of documents  $\text{DB}$ , let  $S$  be the set of  $(w, (count_w, access_w = 0))$  where  $count_w$  is the number of documents containing  $w$ , and  $access_w$  denotes the number of times the keyword  $w$  has been searched/added.  
Client runs  $\text{hsetup}(S, \text{size})$  to obtain  $(h, M)$ .  $\text{size}$  is the anticipated maximum size of the hash table  $H$ . Then client and server run  $\langle \sigma_s, \text{EM} \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, M \rangle, \perp)$ .

Let  $A_L$  be an initially empty memory array with a size that estimates an upper bound on total number of  $(w, d)$  pairs in DB. Client runs  $\mathcal{T} \leftarrow \text{INITIALIZE}(1^\kappa, A_L)$ , and only sends the tree  $T_L$  for the last level to server, and discards the rest.

Client generates a PRF key  $K \leftarrow \{0, 1\}^\kappa$ .

For every item  $(w, (count_w, access_w))$  in  $S$ , and for  $1 \leq i \leq count_w$  (in parallel):

- (a) Client lets  $val_{w,i} = (w||i, d_i)$  where  $d_i$  denotes the  $i$ th document in DB containing  $w$ .
- (b) Client lets  $x_{w,i} = F_K(w||i|access_w)$  and sends  $x_{w,i}$  to server who returns the encrypted buckets on path  $T_L(x_{w,i})$  which client decrypts itself.
- (c) Client then runs  $\{T_L(x_{w,i})\} \leftarrow \text{UPDATE}(w||i, write, val_{w,i}, T_L(x_{w,i}), x'_{w,i} = F_K(w||i|access_w+1))$  to insert  $val_{w,i}$  into the path along its new path  $T_L(x'_{w,i})$ . Client then encrypts the updated path  $T_L(x_{w,i})$  and sends it to server who updates  $T_L$ .

Note that server's output is  $\text{EDB} = (\text{EM}, T_L)$  and client's output is  $\sigma = (\sigma_s, h, K)$ .

2.  $\text{SSESEARCH}(\sigma, w, \text{EDB})$ : Client computes  $i_1, \dots, i_c \leftarrow h(w)$  and client and server run  $\text{OBLIVIOUSACCESS}((\sigma_s, i_j, \text{null}), \text{EM})$  for  $j \in \{1, \dots, c\}$ . If client finds  $(w, (count_w, access_w))$  in one of the retrieved locations, let  $i_j^*$  be the location it was found at. If such a pair is not found the search ends here. Client and server run  $\text{OBLIVIOUSACCESS}((\sigma_s, i_j^*, (w, count_w, access_w + 1)), \text{EM})$  to increase the  $access_w$  by one.

For  $1 \leq i \leq count_w$  (in parallel):

- (a) Client lets  $x_{w,i} = F_K(w||i|access_w)$  and sends  $x_{w,i}$  to server who returns  $T_L(x_{w,i})$  which client decrypts.
- (b) Client runs  $(w||i, d_i) \leftarrow \text{EXTRACT}(L, w||i, T_L(x_{w,i}))$ , and outputs  $d_i$ . Client then runs  $\{T_L(x_{w,i})\} \leftarrow \text{UPDATE}(w||i, read, (w||i, d_i), T_L(x_{w,i}), x'_{w,i} = F_K(w||i|access_w + 1))$  to update the location of  $(w||i, d_i)$  to  $x'_{w,i}$ . Client then encrypts the updated path and sends it to server to update  $T_L$ .

3.  $\text{SSEADD}(\sigma, d, \text{EDB})$ :

For every  $w$  in  $d$ :

- (a) Client computes  $i_1, \dots, i_c \leftarrow h(w)$  and client and server run  $\text{OBLIVIOUSACCESS}((\sigma_s, i_j, \text{null}), \text{EM})$  for  $j \in \{1, \dots, c\}$ . If client finds  $(w, (count_w, access_w))$  in one of the retrieved locations, let  $i_j^*$  be the location it was found at. Else, it lets  $i_j^*$  be the first empty location among the retrieved ones.
- (b) Client and server run  $\text{OBLIVIOUSACCESS}((\sigma_s, i_j^*, (w, (count_w+1, access_w+1))), \text{EM})$  to increase  $count_w$  and  $access_w$  by one.
- (c) Client lets  $x_{w, count_w} = F_K(w||count_w|access_w)$  and sends  $x_{w, count_w}$  to server who returns encrypted  $T_L(x_{w, count_w})$  back. Client decrypts the path.
- (d) Client lets  $x' = F_K(w||count_w+1|access_w+1)$  and runs  $\{T_L(x_{w, count_w})\} \leftarrow \text{UPDATE}(w||i, write, (w||count_w+1, d), T_L(x_{w, count_w}), x')$  to update the path. Client then encrypts the updated path and sends it to server to update  $T_L$ .

Before stating the security theorem for the above SSE scheme, we first need to make the leakage function associated with the scheme more precise. The leakage function  $\mathcal{L}(\text{DB}, H)$  for our scheme outputs the following (DB is the database and  $H$  is the search/add history):  $|W|$ , number unique keywords in all documents;  $|\text{DB}(w)|$  for every  $w$  searched;  $\sum_{w \in W} |\text{DB}(w)|$  i.e. the number of  $(w, d)$  pairs where  $w$  is in  $d$ . See Appendix A.4 for the proof.



**Theorem 2.** *The above SSE scheme is  $\mathcal{L}$ -secure (cf. definition of section ??), if TWORAM is secure (cf. definition in Section 4),  $F$  is a PRF, and the encryption used in the one-level Path-ORAM is CPA-secure.*

*Efficiency.* The setup cost for our SSE scheme is the sum of the setup cost for TWORAM for a memory of size  $|W|$ , and the setup for a one-level Path-ORAM of size  $n = \sum_{w \in W} |\text{DB}(w)|$  which is  $O(n \log n \log \log n)$ .

The bandwidth cost for each search/add query  $w$  is the cost of one ORAM read in TWORAM plus  $O(|\text{DB}(w)| * (\log n \log \log n))$  for  $n = \sum_{w \in W} |\text{DB}(w)|$ .

## References

1. Arash Afshar, Zhangxiang Hu, Payman Mohassel, and Mike Rosulek. How to efficiently evaluate ram programs with malicious security. In *Advances in Cryptology–EUROCRYPT 2015*, pages 702–729. Springer, 2015.
2. Mihir Bellare, Viet Tung Hoang, and Phillip Rogaway. Foundations of garbled circuits. In Ting Yu, George Danezis, and Virgil D. Gligor, editors, *ACM CCS 12: 19th Conference on Computer and Communications Security*, pages 784–796, Raleigh, NC, USA, October 16–18, 2012. ACM Press.
3. David Cash, Paul Grubbs, Jason Perry, and Thomas Ristenpart. Leakage-abuse attacks against searchable encryption. In *Proceedings of the 22nd ACM SIGSAC Conference on Computer and Communications Security, Denver, CO, USA, October 12-6, 2015*, pages 668–679, 2015.
4. David Cash, Stanislaw Jarecki, Charanjit Jutla, Hugo Krawczyk, Marcel Rosu, and Michael Steiner. Highly-scalable searchable symmetric encryption with support for boolean queries. In *CRYPTO*, 2013.
5. Y. Chang and M. Mitzenmacher. Privacy preserving keyword searches on remote encrypted data. In *Applied Cryptography and Network Security (ACNS '05)*, volume 3531 of *Lecture Notes in Computer Science*, pages 442–455. Springer, 2005.
6. M. Chase and S. Kamara. Structured encryption and controlled disclosure. In *Advances in Cryptology - ASIACRYPT '10*, volume 6477 of *Lecture Notes in Computer Science*, pages 577–594. Springer, 2010.
7. Thomas H. Cormen, Clifford Stein, Ronald L. Rivest, and Charles E. Leiserson. *Introduction to Algorithms*. McGraw-Hill Higher Education, 2nd edition, 2001.
8. R. Curtmola, J. Garay, S. Kamara, and R. Ostrovsky. Searchable symmetric encryption: Improved definitions and efficient constructions. In *ACM Conference on Computer and Communications Security (CCS '06)*, pages 79–88. ACM, 2006.
9. Srinivas Devadas, Marten van Dijk, Christopher W. Fletcher, Ling Ren, Elaine Shi, and Daniel Wichs. Onion ORAM: A constant bandwidth blowup oblivious ram. In *TCC*, 2016.
10. Christopher Fletcher, Muhammad Naveed, Ling Ren, Elaine Shi, and Emil Stefanov. Bucket oram: Single online roundtrip, constant bandwidth oblivious ram. Cryptology ePrint Archive, Report 2015/1065, 2015. <http://eprint.iacr.org/>.
11. Sanjam Garg, Steve Lu, and Rafail Ostrovsky. Black-box garbled RAM. Cryptology ePrint Archive, Report 2015/307, 2015. <http://eprint.iacr.org/2015/307>.
12. Sanjam Garg, Steve Lu, Rafail Ostrovsky, and Alessandra Scafuro. Garbled RAM from one-way functions. In *47th Annual ACM Symposium on Theory of Computing*, pages 449–458. ACM Press, 2015.

13. Craig Gentry, Kenny A Goldman, Shai Halevi, Charanjit Julta, Mariana Raykova, and Daniel Wichs. Optimizing oram and using it efficiently for secure computation. In *Privacy Enhancing Technologies*, pages 1–18. Springer, 2013.
14. Craig Gentry, Shai Halevi, Steve Lu, Rafail Ostrovsky, Mariana Raykova, and Daniel Wichs. Garbled RAM revisited. In Phong Q. Nguyen and Elisabeth Oswald, editors, *Advances in Cryptology – EUROCRYPT 2014*, volume 8441 of *Lecture Notes in Computer Science*, pages 405–422, Copenhagen, Denmark, May 11–15, 2014. Springer, Berlin, Germany.
15. Eu-Jin Goh. Secure indexes. Cryptology ePrint Archive, Report 2003/216, 2003. <http://eprint.iacr.org/2003/216/>.
16. Oded Goldreich. Towards a theory of software protection and simulation by oblivious RAMs. In Alfred Aho, editor, *19th Annual ACM Symposium on Theory of Computing*, pages 182–194, New York City, New York, USA, May 25–27, 1987. ACM Press.
17. Oded Goldreich and Rafail Ostrovsky. Software protection and simulation on oblivious rams. *J. ACM*, 43(3):431–473, 1996.
18. Michael T. Goodrich and Michael Mitzenmacher. Privacy-preserving access of outsourced data via oblivious RAM simulation. In *Automata, Languages and Programming - 38th International Colloquium, ICALP 2011, Zurich, Switzerland, July 4-8, 2011, Proceedings, Part II*, pages 576–587, 2011.
19. Michael T. Goodrich, Michael Mitzenmacher, Olga Ohrimenko, and Roberto Tamassia. Privacy-preserving group data access via stateless oblivious RAM simulation. In *Proceedings of the Twenty-Third Annual ACM-SIAM Symposium on Discrete Algorithms, SODA 2012, Kyoto, Japan, January 17-19, 2012*, pages 157–167, 2012.
20. S. Dov Gordon, Jonathan Katz, Vladimir Kolesnikov, Fernando Krell, Tal Malkin, Mariana Raykova, and Yevgeniy Vahlis. Secure two-party computation in sublinear (amortized) time. In *the ACM Conference on Computer and Communications Security, CCS'12, Raleigh, NC, USA, October 16-18, 2012*, pages 513–524, 2012.
21. Mohammad Saiful Islam, Mehmet Kuzu, and Murat Kantarcioglu. Access pattern disclosure on searchable encryption: Ramification, attack and mitigation. In *19th Annual Network and Distributed System Security Symposium, NDSS 2012, San Diego, California, USA, February 5-8, 2012*, 2012.
22. Seny Kamara and Charalampos Papamanthou. Parallel and dynamic searchable symmetric encryption. In *Financial Cryptography (FC)*, 2013.
23. Seny Kamara, Charalampos Papamanthou, and Tom Roeder. Dynamic searchable symmetric encryption. In *the ACM Conference on Computer and Communications Security, CCS'12, Raleigh, NC, USA, October 16-18, 2012*, pages 965–976, 2012.
24. Eyal Kushilevitz, Steve Lu, and Rafail Ostrovsky. On the (in)security of hash-based oblivious ram and a new balancing scheme. In *Proceedings of the Twenty-third Annual ACM-SIAM Symposium on Discrete Algorithms, SODA '12, 2012*.
25. Yehuda Lindell and Benny Pinkas. A proof of security of Yao's protocol for two-party computation. *Journal of Cryptology*, 22(2):161–188, April 2009.
26. Chang Liu, Liehuang Zhu, Mingzhong Wang, and Yu-An Tan. Search pattern leakage in searchable encryption: Attacks and new construction. *Inf. Sci.*, 265:176–188, May 2014.
27. Steve Lu and Rafail Ostrovsky. Distributed oblivious ram for secure two-party computation. In *Proceedings of the 10th Theory of Cryptography Conference on Theory of Cryptography, TCC'13, 2013*.
28. Steve Lu and Rafail Ostrovsky. How to garble RAM programs. In Thomas Johansson and Phong Q. Nguyen, editors, *Advances in Cryptology – EUROCRYPT 2013*, volume 7881 of *Lecture Notes in Computer Science*, pages 719–734, Athens, Greece, May 26–30, 2013. Springer, Berlin, Germany.

29. Tarik Moataz, Travis Mayberry, and Erik-Oliver Blass. Constant communication ORAM with small blocksize. In *Proceedings of the 22nd ACM SIGSAC Conference on Computer and Communications Security, Denver, CO, USA, October 12-6, 2015*, pages 862–873, 2015.
30. Rafail Ostrovsky. Efficient computation on oblivious RAMs. In *22nd Annual ACM Symposium on Theory of Computing*, pages 514–523, Baltimore, Maryland, USA, May 14–16, 1990. ACM Press.
31. Emily Shen, Elaine Shi, and Brent Waters. Predicate privacy in encryption systems. In *TCC*, pages 457–473, 2009.
32. Elaine Shi, T.-H. Hubert Chan, Emil Stefanov, and Mingfei Li. Oblivious RAM with  $o((\log n)^3)$  worst-case cost. In *Advances in Cryptology - ASIACRYPT 2011 - 17th International Conference on the Theory and Application of Cryptology and Information Security, Seoul, South Korea, December 4-8, 2011. Proceedings*, pages 197–214, 2011.
33. Dawn Xiaodong Song, David Wagner, and Adrian Perrig. Practical techniques for searches on encrypted data. In *2000 IEEE Symposium on Security and Privacy, Berkeley, California, USA, May 14-17, 2000*, pages 44–55, 2000.
34. Emil Stefanov, Charalampos Papamanthou, and Elaine Shi. Practical dynamic searchable encryption with small leakage. In *21st Annual Network and Distributed System Security Symposium, NDSS 2014, San Diego, California, USA, February 23-26, 2014*, 2014.
35. Emil Stefanov, Marten van Dijk, Elaine Shi, Christopher W. Fletcher, Ling Ren, Xiangyao Yu, and Srinivas Devadas. Path ORAM: an extremely simple oblivious RAM protocol. In *2013 ACM SIGSAC Conference on Computer and Communications Security, CCS'13, Berlin, Germany, November 4-8, 2013*, pages 299–310, 2013.
36. Peter van Liesdonk, Saeed Sedghi, Jeroen Doumen, Pieter H. Hartel, and Willem Jonker. Computationally efficient searchable symmetric encryption. In *Secure Data Management*, pages 87–100, 2010.
37. Xiao Shaun Wang, T.-H. Hubert Chan, , and Elaine Shi. Circuit ORAM: On tightness of the goldreich-ostrovsky lower bound. In *Proceedings of the 2015 ACM SIGSAC Conference on Computer and Communications Security*, pages 191–202, 2015.
38. Peter Williams and Radu Sion. Single round access privacy on outsourced storage. In *the ACM Conference on Computer and Communications Security, CCS'12, Raleigh, NC, USA, October 16-18, 2012*, pages 293–304, 2012.
39. Andrew Chi-Chih Yao. Protocols for secure computations (extended abstract). In *23rd Annual Symposium on Foundations of Computer Science*, pages 160–164, Chicago, Illinois, November 3–5, 1982. IEEE Computer Society Press.

## A More Details on Path ORAM

### A.1 Path ORAM abstraction algorithms

### A.2 Path ORAM protocols with $\log n$ rounds of interaction using the abstraction

### A.3 Proof of security for TWORAM

Now we prove TWORAM is a secure realization of an oblivious RAM scheme as described in Section 2.2. We start by arguing correctness. Note that the garbled circuits implement the exact same procedures as are required in our abstraction. Therefore the correctness of our scheme follows directly from the correctness of the underlying Path ORAM scheme and garbled circuits construction. Next we argue security. In other words we need to argue that for any adversary  $A$ , there exists a simulator  $\text{Sim}$  for which the following two distributions are computationally indistinguishable.

---

**Algorithm 1** Setting up path ORAM data structures.

---

```
1: procedure  $\mathcal{T} \leftarrow \text{INITIALIZE}(1^\kappa, A_L)$ 
2:   Store block  $(x, A_L[x], \pi_L(x))$  at leaf  $\pi_L(x)$  of tree  $T_L$ , where  $\pi_L$  is a  $n$ -permutation;
3:   for  $i = L$  down to 3 do
4:     Let  $A_{i-1}$  be an array of  $2^{i-1}$  entries such that  $A_{i-1}[x] = \pi_i(2x - 1) \parallel \pi_i(2x)$  for
        $x = 1, \dots, 2^{i-1}$ ;
5:     Store block  $(x, A_{i-1}[x], \pi_{i-1}(x))$  at leaf  $\pi_{i-1}(x)$  of tree  $T_{i-1}$ , where  $\pi_{i-1}$  is a  $2^{i-1}$ -
       permutation;
6:   end for
7:   Let  $A_1$  be an array of 2 entries such that  $A_1[x] = \pi_2(2x - 1) \parallel \pi_2(2x)$  for  $x = 1, 2$ ;
8:   return  $\{A_1, T_2, \dots, T_L\}$ ;
9: end procedure
```

---

---

**Algorithm 2** Extraction algorithm for buckets.

---

```
1: procedure  $\pi \leftarrow \text{EXTRACTBUCKET}(i, y, b)$ 
2:   Search bucket  $b$  to retrieve block  $(y_i, A_i[y_i], p)$ ;
3:   if found then
4:     return  $\pi \leftarrow \text{select}(A_i[y_i], b_i)$ ;  $\triangleright \pi$  is the index of the path to be explored in  $T_{i+1}$ .
5:   else
6:     return  $\perp$ ;
7:   end if
8: end procedure
```

---

---

**Algorithm 3** Update algorithm. It takes as input  $L - 1$  paths and local storage  $A_1$ , then assigns random indices to the paths that were read before and performs the eviction, outputting new paths.

---

```
1: procedure  $\{A_1, T_2(x_2), \dots, T_L(x_L)\} \leftarrow \text{UPDATE}(y, val, A_1, T_2(x_2), \dots, T_L(x_L))$ 
2:    $\text{select}(A_1[y_1], b_1) \leftarrow r_2$ ;  $\triangleright r_i$  is random in  $[1, 2^{i+1}]$ .
3:   for  $i = 2$  to  $L$  do
4:      $T_i.\text{root} \leftarrow T_i.\text{root} \cup \text{readPath}(T_i(x_i))$ ;  $\triangleright T_i.\text{root}$  serves as the stash  $C_i$ .
5:     Update block  $(y_i, A_i[y_i], x_i)$  to  $(y_i, A_i[y_i], r_i)$  in  $T_i.\text{root}$ ;
6:      $\text{select}(A_i[y_i], b_i) \leftarrow r_{i+1}$ ;  $\triangleright$  if  $i = L$  if  $val \neq \text{null}$  then set  $A_L[y] \leftarrow val$  else do
       not change.
7:      $[T_i.\text{root}, T_i(x_i)] \leftarrow \text{evictPath}(T_i.\text{root})$ ;
8:   end for
9:   return  $A_1, T_2(x_2), T_3(x_3), \dots, T_L(x_L)$ ;
10: end procedure
```

---

**Protocol**  $\langle \sigma, EM \rangle \leftrightarrow \text{SETUP}(\langle 1^\kappa, M \rangle, \perp)$ :

**Client:**

- 1: Pick a  $\kappa$ -bit  $s$ ; Run  $\{A_1, T_2, \dots, T_L\} \leftarrow \text{INITIALIZE}(1^\kappa, M)$ ;
- 2: For all  $i > 1$ , for all  $u \in T_i$ , set  $B_u \leftarrow \text{Enc}_s(\text{bucket}_u)$ , where  $\text{Enc}_s(\cdot)$  is a CPA-secure encryption;
- 3: For all  $i > 1$ , for all  $u \in T_i$ , **send to server** data  $B_u$ ;
- 4: **return**  $s$  and  $A_1$  as  $\sigma$ ;

**Server:**

- 1: **return** all data  $B_u$  sent by the client from above as  $EM$ ;

*Fig. 6:* Formal description of the SETUP protocol for the interactive ORAM.

**Protocol**  $\langle (M[y], \sigma'), EM' \rangle \leftrightarrow \text{OBLIVIOUSACCESS}(\langle \sigma, y, val \rangle, EM)$ :

**Client(1):**

- 1: Compute  $x_2 \leftarrow \text{select}(A_1[y_1], b_1)$ . **Send to server** index  $x_2$ ; ▷ run **Server**(2)

**Server( $i$ ):**

- 1: For all  $u \in T_i(x_i)$  **send to client**  $B_u$ ; ▷ run **Client**( $i$ )

**Client( $i$ ):**

- 1:  $\pi = \perp$ ;
- 2: **for**  $u \in T_i(x_i)$  **do**
- 3:      $\text{bucket}_u \leftarrow \text{Dec}_s(B_u)$ ;
- 4:     **if**  $\pi = \perp$  **then**
- 5:          $\pi \leftarrow \text{EXTRACTBUCKET}(i, y, \text{bucket}_u)$ ;
- 6:          $x_{i+1} = \pi$ ;
- 7:     **end if**
- 8: **end for**
- 9: **if**  $i < L$  **then**
- 10:     **Send to server** new index  $x_{i+1}$ ; ▷ run **Server**( $i + 1$ )
- 11: **else**
- 12:      $\{A_1, T_2(x_2), \dots, T_L(x_L)\} \leftarrow \text{UPDATE}(y, val, A_1, T_2(x_2), \dots, T_L(x_L))$ ;
- 13:     For all  $i > 1$ , for all  $u \in T_i(x_i)$ , **send to server**  $B_u \leftarrow \text{Enc}_s(\text{bucket}_u)$ ; ▷ run **Server**( $L$ )
- 14:     **return**  $x_{L+1}$  as  $M[y]$  and  $A_1$  as  $\sigma'$ ;
- 15: **end if**

**Server( $L$ ):**

- 1: **return** the data received by the client as  $EM'$ ;

*Fig. 7:* Formal description of the OBLIVIOUSACCESS protocol for the interactive ORAM.

- $\text{Real}_A^{\Pi}(\kappa)$ : A chooses  $M$ . The experiment then runs  $\langle \sigma, \text{EM} \rangle \leftrightarrow \text{SETUP}((1^\kappa, M), \perp)$ . A then provides read/write queries  $(y_i, v)$  where  $v = \text{null}$  on reads, for which the experiment runs the protocol  $\langle (M[y_i], \sigma_i), \text{EM}_i \rangle \leftrightarrow \text{OBLIVIOUSACCESS}(\langle \sigma_{i-1}, y_i, v \rangle, \text{EM}_{i-1})$ . Denote the full transcript of the protocol by  $t_i$ . Eventually, the experiment outputs  $(\text{EM}, t_1, \dots, t_q)$  where  $q$  is the total number of read/write queries.
- $\text{Ideal}_{\text{Sim}}^{\Pi}(\kappa)$ : The experiment outputs  $(\text{EM}, t'_1, \dots, t'_q) \leftarrow \text{Sim}(q, |M|, 1^\kappa)$ .

*Our Simulator.* Note that the simulator needs to provide to the server, for all  $u \in \{1, \dots, L\}$   $\tilde{C}^u, X^u$  and for all  $i \in \{2, \dots, L\}$   $\beta_i := \text{nState}_{\text{cState}^{(i+1,0,0)}}^{(i,0,0)}$ . Furthermore replacement circuits need to be provided as read/write queries are implemented. Our simulator  $\text{Sim}$  generates these as follows:

- For each  $u = (i, j, k)$ , let  $(\tilde{C}^u, \text{lab}^u \leftarrow \text{GCircuit}(1^\kappa, P[u, b_u, \text{lab}_0^{(i,j+1,2k+b)}]))$ , where  $b_u$  is random bit and  $P$  is a circuit that, if  $j = i$  outputs  $(\text{nextRoot}, \text{lab}_0^{(i+1,0,0)})$ , else if  $b = 0$  then it outputs  $(\text{left}, \text{lab}_0^{(i,j+1,2k+b)})$  and  $(\text{right}, \text{lab}_0^{(i,j+1,2k+b)})$  otherwise.
- Each  $X^u$  is generated as an encryption of a zero-string, namely  $\text{Enc}_s(\mathbf{0})$ . Similarly for all  $i \in \{2, \dots, L\}$   $\beta_i := \text{nState}_0^{(i,0,0)}$ .

Note that as the provided garbled circuits are executed, replacement circuits need to be given and they are generated in the same manner as above.

*Proof of Indistinguishability.* The proof follows by a hybrid argument.

- $H_0$ : This hybrid corresponds to the honest execution  $\text{Real}_A^{\Pi}(\kappa)$  as done honestly.
- $H_1$ : This hybrid is same as  $H_0$  except that we now generate all the  $X^u$  values as encryptions of zero-strings of appropriate length. Specifically, for each  $u$  we set  $X^u \leftarrow \text{Enc}_s(\mathbf{0})$ .

The indistinguishability between  $H_0$  and  $H_1$  follows from the security of the encryption scheme  $(\text{Enc}, \text{Dec})$ .

- $H_2$ : In this hybrid the simulator maintains the entire Path ORAM tree internally but does not include it in the provided garbled circuits. In other words garbled circuits are generated as follows:

- For each  $u = (i, j, k)$ , let  $(\tilde{C}^u, \text{lab}^u \leftarrow \text{GCircuit}(1^\kappa, P[u, b_u, \text{lab}_0^{(i,j+1,2k+b)}]))$ , where  $b_u$  is 0 or 1 depending on whether the execution as per ORAM would go left or right and  $P$  is a circuit that, if  $j = i$  outputs  $(\text{nextRoot}, \text{lab}_0^{(i+1,0,0)})$ , else if  $b = 0$  then it outputs  $(\text{left}, \text{lab}_0^{(i,j+1,2k+b)})$  and  $(\text{right}, \text{lab}_0^{(i,j+1,2k+b)})$  otherwise.
- Each  $X^u$  is generated as an encryption of a zero-string, namely  $\text{Enc}_s(\mathbf{0})$ . Similarly for all  $i \in \{2, \dots, L\}$   $\beta_i := \text{nState}_0^{(i,0,0)}$ .

The indistinguishability between  $H_1$  and  $H_2$  follows by a sequence of hybrids where each garbled circuit is replaced by a simulated garbled circuit. Here these hybrids must be performed in sequence in which garbled circuits are consumed. Note that for the unconsumed garbled circuits the input labels aren't provided (or hardcoded inside any other circuit) and hence they can also be simulated.

- $H_3$ : Same as  $H_2$ , except that each  $b_u$  is now chosen uniformly random, independent of the Path ORAM execution. Note that this is same as the simulator.  
The indistinguishability between  $H_2$  and  $H_3$  from the security of the Path ORAM scheme.

This concludes the proof.  $\square$

#### A.4 Proof of Security for the SSE Scheme

We prove Theorem 2 on security of the SSE scheme next, Following the definition of section 4, we first describe a simulator Sim who generates the transcripts for the ideal distribution  $\text{Ideal}_{A, \text{Sim}, \mathcal{L}}^I(\kappa)$ . Sim takes as input  $\mathcal{L}(\text{DB}, H)$ , and does the following: To generate full transcripts of the constant round ORAM scheme for the adversary A, Sim runs  $\text{Sim}'$ , the simulator that exists for that scheme due its security (see definition of section 2.2). That is, he runs  $(\text{EM}, t_1, \dots, t_q) \leftarrow \text{Sim}'(q, |M|, 1^\kappa)$ , where he drives  $|M|$  from  $|W|$ . To simulate the transcripts of the path-ORAM component, it generates a one-level path ORAM tree  $T_L$  for a memory array of size  $\sum_{w \in W} |\text{DB}(w)|$  filled with all 0 values. For each read/add query, it replaces the PRF-generated paths by uniformly random paths, and generates freshly generated ciphertexts of 0 for updated paths. Sim knows the number of paths to retrieve/update for each query from the leakage function which outputs  $|\text{DB}(w)|$  for every query  $w$ . This completes the description of the simulator. We now need to show that  $\text{Ideal}_{A, \text{Sim}, \mathcal{L}}^I(\kappa)$  is indistinguishable from  $\text{Real}_A^I(\kappa)$ , which constitutes the first in the sequence of our Hybrids:

*Proof of Indistinguishability.* The proof follows by a hybrid argument.

- $H_0$ : This hybrid corresponds to the honest execution  $\text{Real}_A^I(\kappa)$  for the SSE scheme which we repeat here for completeness. A chooses DB. The experiment then runs  $\langle \text{EDB}, \sigma \rangle \leftrightarrow \text{SSESETUP}(\langle 1^\kappa, \text{DB} \rangle, \perp)$ . A then adaptively makes search queries  $w_i$ , which the experiment answers by running the protocol  $\langle \text{DB}_{i-1}(w_i), \sigma_i \rangle \leftrightarrow \text{SSESEARCH}(\langle \sigma_{i-1}, w_i \rangle, \text{EDB}_{i-1})$ . Denote the full transcripts of the protocol by  $t_i$ . Add queries are handled in a similar way. Eventually, the experiment outputs  $(\text{EDB}, t_1, \dots, t_q)$  where  $q$  is the total number of search/add queries made by A.
- $H_1$ : Similar to  $H_0$ , except that the portions of  $t_i$ 's corresponding to the constant-round ORAM are instead generated by  $\text{Sim}'(q, |M|, 1^\kappa)$  where  $\text{Sim}'$  is the simulator in the proof of the ORAM scheme.  
The indistinguishability of  $H_0$  and  $H_1$  follows from security of the ORAM scheme.
- $H_2$ : Similar to  $H_1$  except that all ciphertexts in the path ORAM tree are replaced by encryptions of 0, and all updated ciphertexts will be fresh encryption of 0.  
The indistinguishability of  $H_2$  and  $H_1$  follows from the semantic security of the encryption scheme used in the path ORAM.
- $H_3$ : Similar to  $H_2$  except that all PRF-generated positions are replaced by uniformly random positions. Note that  $H_3$  is essentially  $\text{Ideal}_{A, \text{Sim}, \mathcal{L}}^I(\kappa)$ .  
The indistinguishability of  $H_3$  and  $H_2$  follows from the pseudorandomness of the the PRF.

This concludes the proof  $\square$ .