Arithmetic Coding and Blinding Countermeasures for Ring-LWE

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Abstract. In this work we apply information theoretically optimal arithmetic coding and a number of novel side-channel blinding countermeasure techniques to create BLZZRD, a practical, compact, and more quantum-resistant variant of the BLISS Ring-LWE Signature Scheme. We show how the hash-based random oracle can be modified to be more secure against quantum preimage attacks while decreasing signature size at any given security level. Most lattice-based cryptographic algorithms require non-uniformly distributed ciphertext, signature, and public/private key data to be stored and transmitted; hence there is a requirement for compression. Arithmetic Coding offers an information theoretically optimal compression for stationary and memoryless sources, such as the discrete Gaussian distributions often used in Lattice-based cryptography. We show that this technique gives better signature sizes than the previously proposed advanced Huffman-based compressors. We further demonstrate that arithmetic decoding from an uniform source to target distribution is also an optimal Gaussian sampling method in the sense that a minimal amount of true random bits is required. Performance of the new Binary Arithmetic Coding (BAC) sampler is comparable to other mainstream samplers. The same code, tables, or circuitry can be utilised for both tasks, eliminating the need for separate sampling and compression components. We also describe a simple blinding technique that can be applied to anti-cyclic polynomial multiplication to mask timing- and power consumption side-channels in ring arithmetic. We further show that Gaussian sampling can also be blinded by a split-and-permute technique while reducing the size of required CDF tables.

Keywords: Ring-LWE, BLISS, BLZZRD, Arithmetic coding, Lattice Side Channels.

1 Introduction

Recent years have seen an increased focus on Lattice-based and other "quantum-resistant" public key cryptography, which has been also fuelled by official governmental warnings and endorsements [1,4,5,23].

However, standardisation efforts for these new algorithms are only starting [6] and post-quantum algorithms clearly have not yet reached the level of maturity that is expected from traditional cryptosystems such as RSA or Elliptic Curve systems. Lattice-based public key algorithms have been especially criticised for their key and signature/ciphertext sizes, and for their lack of resistance against side-channel attacks. This paper describes a number of novel implementation techniques that address these issues.

The BLISS (Bimodal Lattice Signature Scheme) was proposed in CRYPTO 2013 by Ducas, Durmus, Lepoint, and Lyubashevsky [8] and offers some of the most compact and efficient lattice-based signatures currently available. A recent survey of practical lattice-based signature schemes [14] found BLISS to offer state-of-the-art performance and recommended it for practical use. The scheme has been implemented in FPGA hardware [26], on 8-bit embedded AVR target [20], and has been distributed as a part of the strongSwan production-quality IPsec VPN suite. Relevant hardware implementation techniques are also considered in [28]. We use BLISS as basis for our evolutionary new Ring-LWE signature implementation, dubbed BLZZRD.

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Structure of this paper and our main contributions. After describing the notation (Section 2.1) and the original BLISS scheme (Section 2.2 and Appendix A), we move to our discoveries:

- 1. **Grover's Attack on Random Oracle** can be mounted with a Quantum Computer against many signature algorithms. The attack can be applied against the random oracle component of BLISS (Section 2.3). We show how to modify the algorithm and its security parameters to counter this attack with small overhead (Section 2.4).
- 2. **Arithmetic Coding** is ideally suited for compression of stationary distributions (Section 3.1) arising in Lattice cryptography, achieving near-optimal efficiency. We show how a Binary Arithmetic Coder (BAC) can be implemented with limited precision (Section 3.2 and Appendix B), and used to compress Ring-LWE signatures (Section 3.3).
- 3. **Gaussian sampling can be implemented with a BAC.** The same arithmetic coding tables, code, and circuitry can also be used to efficiently implement Gaussian sampling, another major operation required in Lattice cryptosystems (Section 3.4). The resulting sampler is optimal in the sense that it requires a minimum number of true random bits.
- 4. **Polynomial Blinding** is an efficient ring-arithmetic side-channel countermeasure. It can be used to hide details of time or energy consumption of individual arithmetic operations via randomisation. This countermeasure can be implemented with a minimal impact on performance or footprint of the scheme (Section 4.1).
- 5. **Split Shuffled Sampling** is a general side-channel countermeasure for Gaussian samplers. Here two or more Gaussian sample vectors are randomised and added to each other, masking the properties of individual samples that may leak during the random sampling process (Section 4.2).

Some efficient implementation tricks for tasks such as computation of the discrete Gaussian distribution are also described. We further take advantage of recent advances in security proof techniques, indicating a smaller precision requirement which results leads to smaller, faster implementations.

2 BLISS and BLZZRD

BLISS is a Ring-LWE public key signature algorithm proposed by Ducas, Durmus, Lepoint, and Lyubashevsky [8]. We use the more recent BLISS-B variant [7] as basis of our work. We refer the reader to these works for security analysis and other design rationale of the original proposals. Since BLZZRD is an evolutionary improvement with compressed signatures and side-channel resistant countermeasures, we concentrate on these specific implementation techniques in present work.

2.1 Conventions and Notation

Arithmetic is performed in a cyclotomic ring $\mathbf{R} = \mathbb{Z}_q[x]/(x^n+1)$ where q is a small prime. Such a ring is anti-circulant as $x^n \equiv -1 \pmod{x^n+1}$. Arithmetic in this ring can be performed efficiently via Number Theoretic Transforms (NTT, finite field FFT) when n divides q-1 and n is a power of 2.

Non-boldface f_i denotes individual coefficients of polynomial $\mathbf{f} = \sum_{i=0}^{n-1} f_i x^i$. Hence $\mathbf{f} \in \mathbf{R}$ is a ring element, while $f_i \in \mathbb{Z}_q$. We use $\mathbf{a} * \mathbf{b} = \sum_{i=0}^{n-1} \sum_{j=0}^{n-1} a_i b_j x^{i+j}$ to denote the product of polynomials \mathbf{a} and \mathbf{b} . For anti-circulant rings of degree n we always have $f_{i+n} = -f_i$. We may therefore reduce $f_i = (-1)^{\lfloor i/n \rfloor} f_{i \mod n}$ for all $i \in \mathbb{Z}$.

We interchangeably use polynomials as zero-indexed vectors; dot product is defined as $\mathbf{a} \cdot \mathbf{b} = \sum_{i=0}^{n-1} a_i b_j$, the Euclidean norms are $\|\mathbf{a}\|^2 = \mathbf{a} \cdot \mathbf{a}$, $\|\mathbf{a}\|_2 = \sqrt{\mathbf{a} \cdot \mathbf{a}}$, and sup norm $\|\mathbf{a}\|_{\infty}$ is the largest absolute value $\max\{|a_0|, |a_1|, \dots, |a_{n-1}|\}$. $\lfloor x \rceil = \lfloor x + \frac{1}{2} \rfloor$ denotes the closest integer to x.

A discrete Gaussian distribution with mean μ , deviation σ and variance σ^2 is denoted $\mathcal{N}_{\mathbb{Z}}(\mu, \sigma^2)$. We use exclusively zero-centered ($\mu = 0$) distributions. See Section 3.1 for definitions and further discussion.

In pseudocode we use \land , \lor , \neg symbols to denote bitwise Boolean manipulation of two's complement integers. A binary modular reduction operator $\mod n$ returns an integer in range [0, n-1].

2.2 Description of BLISS

Appendix A contains an algorithmic description of the BLISS signature scheme. Our description differs somewhat from the original description which is even more dense. We used the original reference implementation¹ and the strongSwan "production grade" implementation² to verify the correctness of our interpretation. Various parameters, symbols, and security claims used in descriptions are given in Table 5. These are the parameters used by both the publications and can also be found in the implementations.

Key Generation. The BLISS-B key generation is perhaps the simplest part of the scheme. Algorithm 3 describes the process. The BLISS-B key generation procedure differs from original BLISS in that keys are not rejected based on an additional norm.

Signature Algorithm. Algorithm 4 describes the signature generation method. Here we note that the purpose of the random oracle H is to take the input (\mathbf{w}, \mathbf{M}) and hash it into a vector of length n that has exactly κ ones, with other entries being zero (the vector can also be interpreted as a polynomial in the ring). The reference implementation is an ad hoc construction based on SHA-512 [34]; the strongSwan implementation now uses the MGF1 construction [15]. We use a two-stage random oracle described in Section 2.4. The BLISS-B signature method differs from BLISS in steps 8-15 of Algorithm 4; this is the Greedy Sign Choices algorithm for minimising the norm $\|\mathbf{c} \cdot \mathbf{t}\| + \|\mathbf{c} \cdot \mathbf{u}\|$. Otherwise the algorithms are equivalent and BLISS can even be used to verify BLISS-B signatures without modification.

Signature Verification. The BLISS signature verification process is given as Algorithm 5. We note that verification is very fast; only two quick checks for norm bounds and a single ring multiplication is required in addition to a call to the random oracle.

2.3 Security of the Random Oracle

Is there a trivial way to forge a signature $(\mathbf{t}, \mathbf{z}, \mathbf{c})$ that will pass signature verification (Algorithm 5) for some public key a and message \mathbf{M} ? We can easily choose arbitrary \mathbf{t} and \mathbf{z} that pass pass steps 1 and 2 (norm bounds). What is left is the matching of the oracle outputs $\mathbf{c} \stackrel{?}{=} \mathbf{c}^{\star}$. There are $\binom{n}{\kappa}$ possible ways for $H(\mathbf{w}, \mathbf{M})$ to construct a vector with κ entries as 1, and remaining $n - \kappa$ entries being zero.

Examining the older BLISS reference implementations we find that \mathbf{c} may not transmitted or used as vector (or a sorted list) but as an unsorted output of the oracle; in this case there are $\frac{n!}{(n-\kappa)!}$ possibilities. To get from list (ordered set) matching to vector (unordered set) matching complexity, one can simply rearrange the input \mathbf{c} values in the signature to match the output from the oracle for the forged message \mathbf{M} (note that this is not as easy with our revised Oracle described in Section 2.4.) These entropies (given in Table 1) appear to be consistent with security claims.

¹ Original BLISS reference implementations are available from: http://bliss.di.ens.fr/

² strongSwan BLISS page: https://wiki.strongswan.org/projects/strongswan/wiki/BLISS

Table 1. Complexity of exhaustive forgery attack on the hash-based random oracle H, based on entropy. The parameters used by new variant given below. The new variant does not communicate the c vector itself, but an intermediate hash that can be used to deterministically generate it. BLZZRD does not contain an equivalent of "toy" BLISS-0.

Variant / Attack	I	II	III	IV
Original BLISS n, κ	512, 23	512, 23	512, 30	512, 39
Orig. c entropy, bits	131.82	131.82	161.04	195.02
Orig. bits on wire $(\kappa \log_2 n)$	207	207	270	351
Orig. Grover's search security	66	66	80	96
BLZZRD n, κ	512, 58	512, 58	512, 82	512, 113
New c entropy, bits	256.81	256.81	320.58	385.30
New intermediate hash θ , bits	256	256	384	384
New Grover's search security	128	128	160	192

This is reasonable in classical computing where $O(2^H)$ preimage search is required where H is the entropy of target range; however the signer herself can create a signature that matches two different messages with relative ease. BLISS signatures are therefore not collision resistant. We note that preimage search is one of the things that quantum computers do well. This leads us the following simple theorem:

Theorem 1 (Exhaustive Quantum Forgery). A quantum adversary can forge an arbitrary BLISS signature with $O\left(\sqrt{\binom{n}{\kappa}}\right)$ complexity.

Proof. With fixed \mathbf{t} and \mathbf{c} in Algorithm 5 one can mount an exhaustive preimage search to find some message \mathbf{M} or small-norm vector $\Delta \mathbf{z} = \mathbf{z} - \mathbf{z}'$ that satisfies $\mathbf{c} = H(\mathbf{w} + \Delta \mathbf{z}, \mathbf{M})$. This search with Grover's Algorithm [12,13] will require $\frac{\pi}{4}\sqrt{\binom{n}{k}}$ lookups. This is optimal for a quantum search [36].

We observe that with the suggested parameters and security levels in Tables 5 and 1, the security level against a quantum adversary falls short of the target; for example BLISS-I and BLISS-II with claimed 128-bit security would fall with complexity of roughly 2^{66} .

2.4 New Random Oracle

With n=512, a trivial encoding of ${\bf c}$ requires 9κ bits for transmitting the indexes. In BLZZRD the ${\bf c}$ vector itself (or the nonzero index set) is not transmitted. An intermediate function $H_i({\bf w},{\bf M})$ is first used to compute a θ -bit random hash c_{θ} . The intermediate then can be converted to the ${\bf c}$ vector with ones at exactly κ positions via H_o oracle function:

$$\mathbf{c} = H(\mathbf{w}, \mathbf{M}) = H_o(H_i(\mathbf{w}, \mathbf{M})). \tag{1}$$

BLZZRD uses the SHA3 hash function and SHAKE XOF [33,34]. H_i is implemented with SHA3-256 or SHA3-384, depending on corresponding θ value:

$$c_{\theta} = H_i(\mathbf{w}, \mathbf{M}) = \mathsf{SHA3} - \theta(\mathbf{w} \mid \mathbf{M}).$$
 (2)

Contents of $\mathbf{w} = (w_0 \mid w_1 \mid \dots \mid w_{n-1})$ are encoded as network byte order 16-bit integers. To produce the \mathbf{c} indexes, SHA3's SHAKE256 extendable-output function (XOF) is used. The c_θ intermediate hash is used to seed the XOF, from which an arbitrary number of 16-bit big-endian values can be extracted. These are masked to index range [0, n-1] and rejected if already contained in \mathbf{c} . This process is repeated until κ ones are found for the \mathbf{c} vector.

Note that SHAKE256 accepts an arbitrary-sized input "key" and has an internal chaining capacity of 512 bits. Its collision resistance as a hash function is at 256-bit security level, and this is also its security level against a quantum preimage attack; more than adequate for 128-, 160-, and 192- bit security goals.

When transmitting or comparing signatures (Algorithms 4 and 5) we may use c_{θ} instead of the full c vector or index set. This leads to more compact signatures. See Table 1 for numerical values for θ ; it has been chosen to be the double of the of the security parameter to counter the Quantum Forgery Attack of Theorem 1. The new κ parameter is chosen so that $\binom{n}{\kappa} > 2^{\theta}$.

The increased κ has an impact on both signature creation and verification speed, and also the rejection ratio (Step 18 in Algorithm 4.) Experimentally the slowdown is less than 30% in all cases. This is somewhat arbitrary due to the fashion that the constant M is determined in [8].

3 Discrete Gaussians and Arithmetic Coding

Obtaining random numbers from a Gaussian (Normal) or other non-uniform distributions is called sampling. Cryptographically secure sampling is required by many Lattice-based cryptographic algorithms; see [9] for an overview.

3.1 Gaussian Sampling

A random sampler from a zero-centered discrete Gaussian distribution $\mathcal{N}_{\mathbb{Z}}(0,\sigma^2)$ returns integer $x\in\mathbb{Z}$ with probability given by density function $\rho_{\sigma}(x)$. This probability mass of discrete Gaussian distribution at x is exactly proportional to $\rho_{\sigma}(x)\propto \exp(-\frac{x^2}{2\sigma^2})$, where σ is a deviation parameter (See Figure 1). For $\sigma\gtrapprox 2$ we can approximate it to high precision with

$$\rho_{\sigma}(x) \approx \frac{1}{\sigma\sqrt{2\pi}} e^{-\frac{x^2}{2\sigma^2}}.$$
 (3)

Tailcut. As |x| grows, $\rho_{\sigma}(x)$ eventually diminishes into statistical insignificance. We may choose a cryptographic threshold parameter such as $\epsilon=2^{-128}$ and simply ignore all $|x|>\tau$ when $2\sum_{x>\tau}\rho_{\sigma}(x)\leq\epsilon$. Therefore the probability that a random x from the distribution satisfies $-\tau< x<\tau$ is greater than $1-\epsilon$. We call τ the "tailcut parameter". It's typical numerical value for $\epsilon=2^{-128}$ bound is related to deviation by roughly $\tau=13.2\sigma$.

Efficient computation of density tables. Evaluation of transcendental functions (such as exp) is slow if generic algorithms are used. We can derive two parameters $b = \exp(-\frac{1}{2\sigma^2})$ and $c = 1/\sigma\sqrt{2\pi}$ for any given deviation σ . Equation 3 can now be written as $\rho_{\sigma}(x) = cb^{(x^2)}$. Since $x^2 \in \mathbb{Z}$, square-and-multiply exponentiation algorithms (analogous to those used for modular exponentiation) may be used to efficiently compute the power $b^{(x^2)}$ at arbitrary $x \in \mathbb{Z}$. All arithmetic can be kept at fixed point range [0,1] for convenience.

When tabulating the density, due to symmetry $\rho_{\sigma}(x) = \rho_{\sigma}(-x)$ we consider the sequence $t_0, t_1, t_2, ...$ with $t_i = \rho_{\sigma}(i)$. The observe that the ratio of consecutive values satisfies $\frac{t_{i+1}}{t_i} = u_i = b^{2i+1}$ and furthermore $\frac{u_{i+1}}{u_i} = b^2$. We arrive at the following recurrence:

$$t_0 = c$$
 $u_0 = b$ Initialise.
 $t_i = t_{i-1}u_{i-1}$ $u_i = b^2u_{i-1}$ For $i \ge 1$. b^2 computed only once. (4)

The algorithm of Equation 4 computes consecutive discrete Gaussian density function values with only two multiplications per point (b^2 stays constant).

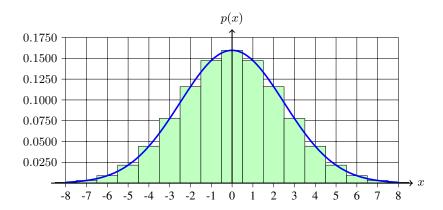


Fig. 1. The green bars illustrate the probability mass for integers $x \in \mathbb{Z}$ with a $\sigma = 2.5$ discrete Gaussian distribution (Equation 3). Blue line is the corresponding continuous probability density function.

Gaussian sampling algorithms. We first analyse the "inversion sampler" which is one way of using uniform random bits to select an element from the target distribution by inverting its cumulative distribution. Since $\mathcal{N}_{\mathbb{Z}}(0,\sigma^2)$ is symmetric we can define a cumulative sequence $s_i = \sum_{x=-i}^i \rho_{\sigma}(x)$. It can be computed as an extension of sequences of Equation 4 via $s_0 = t_0$ and $s_i = s_{i-1} + 2t_i$. Clearly the sum of all probabilities converges to $s_{\infty} = 1$.

For cryptographic applications we can assume a source of unbiased random bits $z_i \in \{0,1\}$. A sequence of n random bits can be viewed as real-valued $z \in [0,1]$ via binary fraction $z = 0.z_1z_2z_3\ldots z_n$. When n is finite, $z_1, z_2, ...z_n$ only defines a range of size 2^{-n} . For uniformly $(n = \infty)$ random $z \in [0,1]$ the corresponding sampled integer x can be derived with additional random sign bit via

$$x = \begin{cases} 0 & \text{if } z < s_0, \\ \pm i & \text{if } s_{i-1} \le z < s_i \text{ for } i \ge 1. \end{cases}$$
 (5)

This corresponds to "inversion sampling", and can be implemented via a binary search into monotonically increasing table s_i by first randomising a large number of bits to create a high-precision real number z.

Methods such as Inversion Sampling [24], Knuth-Yao Sampling [17], The Ziggurat Method [3,10,21,22], Kahn-Karney Sampling [16], and "Bernoulli" sampling [8] have been proposed for lattice cryptography.

3.2 Arithmetic Coding

Information theory [31,32] tells us that the average entropy of a stationary and memoryless discrete source such as $\Omega = \mathcal{N}_{\mathbb{Z}}(0, \sigma^2)$ is

$$H(\Omega) = H\left(\mathcal{N}_{\mathbb{Z}}(0, \sigma^2)\right) = -\sum_{x = -\infty}^{\infty} \rho_{\sigma}(x) \log_2 \rho_{\sigma}(x) \text{ bits/symbol.}$$
 (6)

Equation 6 gives us a lower bound for bits required to represent any discrete Gaussian sequence; this is also the expected number of random entropy bits required in sampling.

Theorem 2. Arithmetic coding is optimal for a static and memoryless discrete source Ω in the sense that it is able to encode n symbols from such a source into $nH(\Omega) + O(1)$ data bits.

Proof. See Section 1.5 of [30].
$$\Box$$

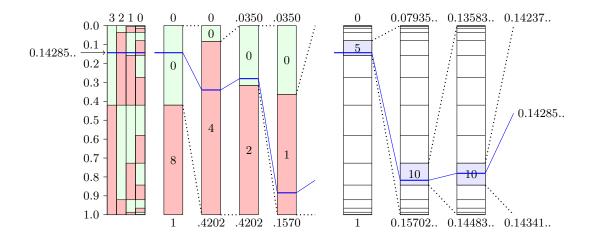


Fig. 2. In this toy example we are encoding discrete Gaussian with $\sigma=2.5$ with tail cutting applied at ≈ 0.001 level; the range is $-7\dots7$ and the integers fit 4 bits. For convenience the distribution is centred at 8 average (high bit flip). Left side shows the operation of binary sample decoding; this corresponds to binary search within the cumulative distribution. On each step either min or max bound is set to divisor value. Only one scaling multiplication and comparison is required. Four bits yields the output 0+4+0+1=5. Right side illustrates decoding of multiple samples. Output (5,10,10) corresponds to (-3,2,2) when adjusted to zero centre. After decoding 3 samples the bounds min $= 0.14237..._{10} = 0.0010010010011110..._{2}$ and max $= 0.14285..._{10} = 0.0010010010010..._{2}$ already match in 8 binary fraction bits. Both bounds and the input fraction can be shifted left, discarding the integer bits.

We implemented a Binary Arithmetic Code (BAC) [11] encoder and decoder for a static (non-dynamic) distribution. Implementation details of the codec were inspired by [30]. Our implementation is in C language and uses 64-bit fixed point precision, and is highly portable. Importantly, we pose no specific limits on buffer carry bit backpropagation, such as "bit stuffing" used in Q-Coder hardware architecture [25].

Pseudocode for our arithmetic coding algorithms is included in Appendix B. The coding and decoding procedure is illustrated by Figure 2. Exhaustive testing with various distributions was performed to verify the correct operation of the codec. The C implementation is about 250 lines, including the table to construct relevant discrete Gaussian distributions.

3.3 Compressing signatures

We have implemented arithmetic coding compression and decompression for the t component of signatures generated with new BLZZRD parameters. The z vector was also modelled as discrete Gaussian with a small σ . See Table 2 for our parameters and experimental results. Rather than transmitting the c vector indexes, an intermediate hash of θ bits is transmitted. The last line gives the experimental average signature sizes, including overhead required to encode parameter and array sizes.

Comparison with a Huffman Code. The extended version of [26] describes an advanced "block" Huffman encoding technique for the BLISS-1 parameter set. The codec achieves good performance (for a Huffman code) by assigning codes to quadruplets $\left(\left\lfloor\frac{\operatorname{abs}(t_{2i})}{2^8}\right\rfloor,\left\lfloor\frac{\operatorname{abs}(t_{2i+1})}{2^8}\right\rfloor,z_{2i},z_{2i+1}\right)$ rather than individual t_i and z_i values. The lower 8 bits of t_i values is stored as they are, and up to four sign bits are stored for nonzero entries.

We note that the 64-entry Huffman tree given in Appendix B of [26] for BLISS-I can only encode values in range $-1023 \le t_i \le 1023$. Since t_i comes from Gaussian distribution with $\sigma = 215$, there

Table 2. BLZZRD	signature cor	npression usin	g Binary	Arithmetic Coding.

Parameter / Variant	BLZZRD-I	BLZZRD-II	BLZZRD-III	BLZZRD-IV
Security target, bits	128	128	160	192
Revised κ	58	58	82	113
Arith. code σ for t	215	107	250	271
Arith. code σ for \mathbf{z}	0.4792	0.4352	0.646	1.136
Intermediate hash θ	256	256	384	384
Avg. compressed t	5079.2	4563.8	5190.6	5250.2
Avg. compressed z	486.0	326.9	779.3	1206.1
Encoding overhead	32	32	32	32
Signature average, bits	5843.2	5178.7	6385.9	6872.3

is a probability of $P=1-\sum_{x=-1023}^{1023}\rho_{\sigma}(x)\approx 2^{-18.98}$ that individual t_i values will overflow and a probability of $1-(1-P)^n\approx 2^{-9.98}$ (or roughly one in thousand) that an individual ${\bf t}$ signature vector cannot be compressed using the proposed code.

Ignoring these frequent overflow conditions, our exhaustive testing has showed that the block Huffman codec compresses (\mathbf{t}, \mathbf{z}) to an average of 5650.1 bits/signature while the arithmetic coder achieves only a slightly better result, 5565.2 bits/signature. Furthermore, [26] states that "c does not contain any easily removable redundancy", and therefore $\kappa \log_2(n) = 207$ bits are used. This is not strictly accurate as our new oracle (Section 2.4) requires a smaller number of bits. In case of the original BLISS-I security parameters only $\theta = 128$ bits would would be required, a 38% drop. For BLZZRD-I, we have $\kappa = 58$ and $\theta = 256$, indicating a reduction to less than half of the corresponding plain BLISS encoding size.

3.4 Sampling with an Arithmetic Decoder

An interesting consequence of the information theoretic optimality of an arithmetic coder is that when the decoder is fed uniform random bits, it will output random samples in the desired target distribution. Therefore the same code, circuitry, and tables that are used to compress signatures can double as a random sampler as well. We have utilised the algorithm 10 in Appendix B as a Gaussian sampler in this fashion. With precision P=64 we may reasonably expect to reach a security level close to 128-bit level, based on the "Valiant-Valiant" sampling Theorem and conjectures [29].

Table 3 gives performance characteristics of our initial implementation. The binary search CDF sampler used tables of size 2^{12} and an additional sign bit, whereas the BAC sampler used tables of size 2^{13} without special coding for sign bits. The random usage was calculated from average number of random bytes used to sample vectors of length n=512. The table also includes numbers for a blinded sampler (Section 4.2). Precision of P=64 was used in all cases.

We note that Knuth-Yao can be also be expected to utilise a small number of bits, shown to average H+2 bits per sample [17]. Furthermore Knuth-Yao always uses a whole number of bits per sample, whereas the BAC sampler can be utilised to create vectors of samples where bits of entropy are "shared" between individual samples. For BLZZRD parameters the actual randomness usage of Knuth-Yao is about 16% higher than the BAC sampler for vectors of length n=512. We note that Knuth-Yao, as interpreted in works such as [27], is suitable primarily for small deviations used in Ring-LWE encryption ($\sigma\approx3$), not for larger σ required by BLISS / BLZZRD.

We conclude that even though the BAC sampler requires a scaling multiplication for binary step, it is still comparable in speed to the inverse CDF sampler. The main advantage of the BAC sampler is that it uses a very small number of true random bits; the actual random usage is typically within 1.3% of the theoretical minimum for vector sizes used on Lattice cryptography.

Table 3. Sampling with an Arithmetic Coder vs a simple inverse CDF sampler on a Core i7-5600U @ 2.6 GHz. Note that P=64 precision was also used for BLZZRD-III and BLZZRD-IV, even though it is really not sufficient above 128-bit security level. The same precision was used for all samplers.

Parameter / Variant	BLZZRD-I	BLZZRD-II	BLZZRD-III	BLZZRD-IV
Deviation σ	215	107	250	271
Entropy $H\left(\mathcal{N}_{\mathbb{Z}}(0,\sigma^2)\right)$	9.7953	8.7886	10.013	10.129
BAC Sampler				
Random bits / Sample	9.9261	8.9194	10.144	10.260
Samples / Second	8,900,000	9,600,000	8,800,000	8,800,000
CDF Sampler				
Random bits / Sample	64	64	64	64
Samples / Second	8,672,000	8,408,000	8,704,000	8,440,000
Blinded CDF ($m=2$).				
Random bits / Sample	128	128	128	128
Samples / Second	3,400,000	3,400,000	3.400,000	3,400,000

4 Randomised Side-Channel Countermeasures for Ring-LWE

Blinding is a standard countermeasure against both the timing attack [18] and emissions-based attacks such as Differential Power Analysis [19] for traditional public key cryptosystems. Blinding countermeasures add randomness to private key operations, making determination of secrets from observations more difficult for the attacker. In case of RSA, there are two kinds of blinding, base blinding and exponent blinding. In case of ECC, scalar blinding can be used.

Examining private key operations Ring-LWE cryptosystems, we note that there are two main components that can introduce side-channel vulnerabilities to the implementation; ring polynomial arithmetic and Gaussian sampling. We have implemented analogous blinding countermeasures against both. For BLISS/BLZZRD (Algorithm 4), special note should be made to implement the GreedySC component using side-channel resistant techniques as well.

4.1 Blinded Polynomial Multiplication

Basic arithmetic in \mathbb{Z}_q and especially the modular reduction by small prime q may easily introduce emissions to a Ring-LWE implementation. For example the arithmetic operations described for a low-resource Ring-LWE implementation in [20] contain a number of conditional execution cases.

The simplest form of polynomial blinding is to multiply polynomials with random constants $a, b \in \mathbf{Z}_q$. This can be done in regular or NTT domain; the results are equivalent. One can set $a = b^{-1}$ or multiply the result by $c = (ab)^{-1}$:

$$\mathbf{h} = a\mathbf{f} * b\mathbf{g}$$
$$\mathbf{f} * \mathbf{g} = (ab)^{-1}\mathbf{h}.$$

Note that anti-cyclic NTT multiplication requires each polynomial to be "pre-processed" by multiplying entries with tabulated roots of unity ω^i anyway. Therefore by choosing $a=\omega^i, a=\omega^j$, this type of blinding can be done with virtually no extra cost. The normalisation constant becomes $c=\omega^{-i-j}$.

An another type of blinding of anti-cyclic polynomial multiplication can be achieved via circularly "shifting" the polynomials. As noted in Section 2.1, we may write a polynomial as $\mathbf{f} = \sum_{i=0}^{n-1} f_i x^i$.

Shifting by j positions is equivalent to computing

$$x^{j}\mathbf{f} = \sum_{i=0}^{n-1} f_{i}x^{i+j} = \sum_{i=0}^{n-1} f_{i-j}x^{i}.$$
 (7)

Here the coefficients are therefore simply rotated in anti-cyclic fashion. Both constant multiplication and shifting by c are captured by function PolyBlind:

Algorithm 1 PolyBlind(\mathbf{v}, s, c) returns vector \mathbf{v} of length n shifted by $s, 0 \le s < n$ positions and multiplied with a constant c.

```
1: for i=0,1,\ldots n-s-1 do
2: v_i' \leftarrow (cv_{(i+s)}) \bmod q Lower half, original sign.
3: end for
4: for i=n-s,\ldots n-1 do
5: v_i' \leftarrow (q-cv_{(i+s-n)}) \bmod q Upper half, changed sign.
6: end for
Output: Blinded vector \mathbf{v}'.
```

Algorithm PolyBlind is very fast. The inverse operation PolyBlind' $(\mathbf{v}, -s, c^{-1})$, distinguished by a negative shift value s, is equally easy to construct. With that function, we have

PolyBlind'(PolyBlind(
$$\mathbf{v}, s, c$$
), $-s, c^{-1}$) = \mathbf{v} for all $\mathbf{v}, 0 \le s < n, 0 < c < q$. (8)

Due to isometries of the anti-circulant ring, we can use a total of four blinding parameters: a, b (constants) and r, s (shift values) in the blinded scheme to compute the polynomial product $\mathbf{f} * \mathbf{g}$:

$$\begin{split} \mathbf{f}' &= \mathsf{PolyBlind}(\mathbf{f}, r, a) \ \ for \ random \ \ 0 \leq r < n, 0 < a < q \\ \mathbf{g}' &= \mathsf{PolyBlind}(\mathbf{g}, s, b) \ \ for \ random \ \ 0 \leq s < n, 0 < b < q \\ \mathbf{h}' &= \mathbf{f}' * \mathbf{g}' \\ \mathbf{f} * \mathbf{g} &= \mathsf{PolyBlind}'\left(\mathbf{h}', -(r+s), (ab)^{-1}\right). \end{split}$$

One may choose a and b from tabulated roots of unity; $a = \omega^i, a = \omega^j$ and avoid computing the inverse since $(ab)^{-1} = \omega^{-(i+j)}$. This type of blinding has a relatively small performance penalty.

If roots of unity are used as constants, the total amount of "noise;; entropy introduced constants and shifting is $4 \log_2(n) = 36$ bits, increasing the number of required observations by a significant factor.

4.2 Blinded Gaussian Sampling

We note that the BLISS/BLZZRD algorithm always samples vectors of n variables at once. We define a function VectorSample $(n,\sigma)=\mathcal{N}^n_{\mathbb{Z}}(0,\sigma^2)$ that produces a vector of n samples from discrete Gaussian with deviation parameter σ . Step 1 of signature Algorithm 4 can be written as

$$(\mathbf{t}, \mathbf{u}) \leftarrow (\mathsf{VectorSample}(n, \sigma), \mathsf{VectorSample}(n, \sigma)).$$
 (9)

If VectorSample is implemented naively as a loop, emissions from the implementation may reveal information about the random numbers being sampled and attacker may determine which elements of the random vector have specific features, as is done in the cache attack of [2]. This may be alleviated to

Table 4. Parameters for $Z \approx X + kY$ split samplers, where Z is the target distribution $\mathcal{N}_{\mathbb{Z}}(0, \sigma)$, k is a small integer constant, and X and Y come from $\mathcal{N}_{\mathbb{Z}}(0,\sigma')$ with $\sigma'=\frac{1}{\sigma}\sqrt{1+k^2}$. This is an approximation; $\epsilon=\frac{1}{2}||Z-X+kY||_1$ gives the statistical distance (total variation distance) between the two. Here we want $\epsilon^2<\frac{1}{n}$ where n is a security parameter indicating the number of samples required to distinguish the distribution by "Valiant-Valiant" [29,35].

Parameter / Variant	BLZZRD-I	BLZZRD-II	BLZZRD-III	BLZZRD-IV
Deviation σ	215	107	250	271
Constant k	11	8	12	12
Split deviation σ'	17.8548	13.2717	20.7614	22.5053
Distance ϵ	$2^{-89.1}$	$2^{-76.8}$	$2^{-85.3}$	$2^{-100.1}$

some degree by using VectorShuffle(VectorSample (n, σ)), which is just a random shuffle of the Gaussian sample vector.

From probability theory we know that the sum of any two discrete Gaussian distributions is a discrete Gaussian distribution. More precisely, variances (which is the square of deviation) are additive. Let X and Y have distributions $\mathcal{N}_{\mathbb{Z}}(\mu_X, \sigma_X^2)$ and $\mathcal{N}_{\mathbb{Z}}(\mu_Y, \sigma_Y^2)$, respectively. Then their sum X + Y has distribution $\mathcal{N}_{\mathbb{Z}}(\mu_X + \mu_Y, \sigma_X^2 + \sigma_Y^2)$. With zero-centered distributions the average does not change, but the resulting deviation will be $\sigma_{X+Y} = \sqrt{\sigma_X^2 + \sigma_Y^2}$. By induction, this generalises to more variables. We use this property to create a more secure Gaussian vector sampler.

Algorithm 2 VectorBlindSample (n, m, σ) returns $\mathcal{N}_{\mathbb{Z}}^{n}(0, \sigma^{2})$ using m iterations of random blinding.

1: **x** ← **0**

Initialise with a vector of zeroes.

2: for $i=1,2,\ldots,m$ do

3: $\mathbf{x} \leftarrow \mathbf{x} + \mathcal{N}_{\mathbb{Z}}^{n} \left(0, (\frac{1}{\sqrt{m}}\sigma)^{2} \right)$ 4: $\mathbf{x} \leftarrow \mathsf{VectorShuffle}(\mathbf{x})$

Deviation of added elements is $\frac{1}{\sqrt{m}}\sigma$.

Shuffle elements of the vector.

5: end for

Output: Sampled vector x.

Algorithm 2 can be used in the signature algorithm as described by Equation 9. Note that the individual samples come from a different distribution with $\sigma' = \frac{1}{\sqrt{m}}\sigma$; one cannot use the original distribution and then "divide" the results by m, as this will not result in the correct distribution.

As was pointed out in [26], one can also choose $\sigma' = \frac{\sigma}{\sqrt{1+k^2}}$ and construct the final distribution as Z = X + kY. The k parameter must be carefully chosen so that the statistical distance to the actual target distribution is not too great. Table 4 gives some appropriate parameters to use. The resulting construction can be written as

$$\mathbf{v} = \mathsf{VectorShuffle}(\mathsf{VectorSample}(n, \sigma')) + k * \mathsf{VectorShuffle}(\mathsf{VectorSample}(n, \sigma')). \tag{10}$$

Here all elements of the second vector are simply multiplied by the integer constant k. The algorithm has a performance impediment factor of m or less. However, fast "leakier" algorithms may be used. More significantly, the required tables are much smaller. For example, a CDF table of only 256 entries gives a secure tailcut at $\tau = 14.34\sigma$ for BLZZRD-I with parameters of Table 4.

The amount of noise entropy introduced by the permutations is technically technically in thousands of bits. Even though this does not directly translate to attack complexity, we expect this countermeasure to make emissions- and cache-based attacks on the sampler significantly harder.

5 Conclusions

We have offered a number of techniques that can be used to improve the security and performance of Ring-LWE public key algorithms. Using these techniques, we constructed BLZZRD, a practical, evolutionary variant of the BLISS-B signature scheme. An experimental implementation that contains many of the features described in this document is available at https://github.com/mjosaarinen/blzzrd.

We have described a direct Grover's attack on the random oracle component of the BLISS signature scheme. In order to counter this attack, a new hash-based random oracle is proposed, with an increased κ variable an "intermediate" hash, which reduces signature size. It is currently not easy to estimate the security of many other components of BLISS/BLZZRD against quantum attacks, but the new random oracle parameters are consistent with suggested quantum-resistant key sizes for symmetric ciphers [23].

Ring-LWE algorithms have discrete Gaussian variables in signatures and ciphertexts. We show that arithmetic coding can be used to compress these quantities in essentially optimal fashion. We describe an efficient Binary Arithmetic Coder (BAC) that produces smaller signatures than previous compressors. A somewhat surprising finding is that arithmetic coders can easily be used to construct Gaussian Samplers that have comparable performance (millions of samples per second) to other dedicated samplers, but require only a (optimally) small amount of true random bits.

Standard "blinding" side-channel countermeasures for public key algorithms introduce randomness (noise) into private key operations, thus making determination of secrets more difficult. In Ring-LWE cryptography, the main components used by private key operations are ring arithmetic and Non-uniform (Gaussian) random sampling.

For ring arithmetic, we introduce "blinded polynomial multiplication", a simple randomisation technique based of constant multiplication and rotation of polynomials. This technique is cheap to implement and utilises the specific isometries of the types of anti-circulant rings often used in Ring-LWE.

For Gaussian sampling, we note that Ring-LWE algorithms typically require vectors rather than individual samples. We show that sampling processes can be blinded by shuffling these vectors and convoluting multiple Gaussian distributions with each other. This may also result in a smaller implementation footprint since the size of required CDF tables becomes smaller.

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A The BLISS-B Signature Algorithm

This section contains pseudocode for original BLISS-B key generation (Algorithm 3, signature (Algorithm 4) and signature verification (Algorithm 5) procedures. See Section 2.2 for further information. The original security parameters are given in Table 5. These are unmodified for BLZZRD apart from κ .

Table 5. Original parameter sets and security targets for BLISS-B from [7,8,26].

Scheme	BLISS-BO	BLISS-BI	BLISS-BII	BLISS-BIII	BLISS-BIV
Optimised for	Fun	Speed	Size	Security	Security
Security target	\leq 60 bits	128 bits	128 bits	160 bits	192 bits
Degree n	256	512	512	512	512
Modulus q	7681	12289	12289	12289	12289
Secret key density δ_1, δ_2	0.55, 0.15	0.3, 0.0	0.3, 0.0	0.42, 0.03	0.45, 0.06
Deviation σ	100	215	107	250	271
Ratio α	0.748	1.610	0.801	1.216	1.027
Repetition rate M	2.44	1.21	2.18	1.40	1.61
Challenge weight κ	12	23	23	30	39
Truncation params d, p	5, 480	10, 24	10, 24	9, 48	8, 96
Verification bound B_2	2492	12878	11074	10206	9901
Verification bound B_{∞}	530	2100	1563	1760	1613

Algorithm 3 BLISS-B [7,8] Key Generation. **f** and **g** are polynomials in cyclotomic ring $\mathbb{Z}_q[x]/(x^n+1)$. Arithmetic for Number Theoretic Transforms is performed mod q

```
1: (\mathbf{f}, \mathbf{g}) \leftarrow \text{Uniform polynomials with exactly } \lceil \delta_1 n \rceil entries in \{\pm 1\} and \lceil \delta_2 n \rceil entries in \{\pm 2\}.

2: \mathbf{g} \leftarrow 2\mathbf{g} + 1 All but g_0 are even (\{\pm 2\} \text{ or } \{\pm 4\})).

3: \mathbf{a} \leftarrow \mathbf{g}/\mathbf{f}. Restart if \mathbf{f} is not invertible. Quick division via Number Theoretic Transform.

Output: Private key (\mathbf{f}, \mathbf{g}) and public key \mathbf{a}.
```

Algorithm 4 BLISS-B [7,8] Signature algorithm. $H(\mathbf{w}, \mathbf{M})$ is a deterministic random oracle that produces a vector with κ ones from input. Note the ring polynomial anti-circulant wraparound; $f_{i+n} = -f_i$.

```
Input: Private key (\mathbf{f}, \mathbf{g}) and public key \mathbf{a} = \mathbf{g}/\mathbf{f}.
Input: Message to be signed M.
 1: (\mathbf{t}, \mathbf{u}) \leftarrow (\mathcal{N}_{\mathbb{Z}}^n(0, \sigma^2), \mathcal{N}_{\mathbb{Z}}^n(0, \sigma^2))
                                                                                                      All coefficients are discrete Gaussians.
 2: \mathbf{v} \leftarrow \mathbf{t} * \mathbf{a}
                                                                                                      Multiplication in the ring using NTT.
  3: for i = 0, 1, \dots, n-1 do
          v_i \leftarrow ((q+1)v_i + u_i) \mod 2q
                                                                                                      Equivalent to adding q to v_i if odd, then u_i.
                                                                                                      Truncation to nearest integer, limit by p = \lfloor 2^{d-1}q \rfloor.
           w_i = \lfloor \frac{v_i}{2^d} \rceil \mod p
 6: end for
 7: \mathbf{c} \leftarrow H(\mathbf{w}, \mathbf{M})
                                                                                                      Using w and M, create a vector with \kappa ones.
 8: (a, b) \leftarrow (0, 0)
                                                                                                      Init the "GreedySC" sign choices algorithm.
 9: for all c_i = 1 do
          if \left(\sum_{j=0}^{n-1} f_j a_{i+j} + g_j b_{i+j}\right) \ge 0 then
10:
               (\mathbf{a}, \mathbf{b}) \leftarrow (\mathbf{a}, \mathbf{b}) - x^i(\mathbf{f}, \mathbf{g})
11:
                                                                                                      Shift both \mathbf{f} and \mathbf{g} by i positions for subtraction.
12:
                (\mathbf{a}, \mathbf{b}) \leftarrow (\mathbf{a}, \mathbf{b}) + x^i(\mathbf{f}, \mathbf{g})
13:
                                                                                                      Add. Anti-circulant shift for norm minimisation.
14:
           end if
15: end for
16: s \leftarrow \mathsf{Random\ bit}.
                                                                                                      Randomise sign.
17: (\mathbf{t}, \mathbf{u}) \leftarrow (\mathbf{t}, \mathbf{u}) + (-1)^s (\mathbf{a}, \mathbf{b})
18: Continue with probability 1/\left(M\exp\left(-\frac{\|\mathbf{a}\|^2+\|\mathbf{b}\|^2}{2\sigma^2}\right)\cosh\left(\frac{\mathbf{t}\cdot\mathbf{a}+\mathbf{u}\cdot\mathbf{b}}{\sigma^2}\right)\right), otherwise restart.
19: for i = 0, 1, \dots, n-1 do
           z_i \leftarrow w_i - \lfloor \frac{v_i - u_i}{2^d} \rfloor \mod p
                                                                                                      Create "rounding correction" for the signature.
21: end for
```

Algorithm 5 BLISS-B [7,8] Signature verification.

Output: Message signature is (t, z, c).

```
Input: Public key a.
Input: Signature (\mathbf{t}, \mathbf{z}, \mathbf{c}) and message \mathbf{M}.
 1: Reject if \sqrt{\|\mathbf{t}\|^2 + 2^{2d}\|\mathbf{z}\|^2} > B_2
                                                                                       Euclidean norm bound check.
 2: Reject if \max(\|\mathbf{t}\|_{\infty}, 2^d \|\mathbf{z}\|_{\infty}) > B_{\infty}
                                                                                       Suprenum norm bound check.
 3: \mathbf{v} \leftarrow \mathbf{t} * \mathbf{a}
                                                                                       Ring arithmetic using NTT \operatorname{mod} q.
 4: for i = 0, 1, \dots, n-1 do
 5: v_i \leftarrow (q+1)v_i + c_i q \mod 2q
                                                                                       Adding q to v_i if v_i is odd, and / or c_i = 1.
      w_i \leftarrow \lfloor \frac{v_i}{2^d} \rceil + z_i \mod p
                                                                                       Truncated value "corrected" with signature z.
 7: end for
 8: \mathbf{c}^{\star} \leftarrow H(\mathbf{w}, \mathbf{M})
                                                                                       Run the oracle on w and M, compare.
Output: Accept signature if c = c^*.
```

B Pseudocode for Binary Arithmetic Coder (BAC) used by BLZZRD

Our implementation was heavily inspired by [30], but modified into a Binary Arithmetic Coder (BAC). Constants used to define the BLZZRD BAC are as follows:

- P Bit precision used by the implementation. For BLZZRD we used P = 64.
- D Alphabet size in bits. For discrete Gaussians, we need $2^D > 2\tau$ (See Section 3.1).

With a BAC, an alphabet of size 2^D can be viewed as a binary tree of depth D. For each one of the D binary decisions in encoding or decoding, a single comparison is required.

The input frequencies can be arbitrarily scaled "counts". The probability of occurrence of x is

$$\Pr(x) = \frac{\operatorname{freq}[x]}{\sum_{i=0}^{2^{D}-1} \operatorname{freq}[i]}.$$
(11)

Given a table of frequencies $freq[2^D]$, function BuildDist (Algorithm 6) creates a corresponding scaled BAC distribution table "tree" $dist[2^D]$ that is used by both encoding and decoding functions.

Algorithm 6 BuildDist. Create a scaled BAC distribution tree from alphabet frequency counts.

```
Input: freq[2^D], a frequency count for alphabet.

1: for i=2^{D-1}, \dots 8, 4, 2, 1 do

2: j \leftarrow 0

3: while j < 2^D do

4: c_0 \leftarrow \sum_{k=j}^{j+i-1} freq[k]

5: c_1 \leftarrow \sum_{k=i+j}^{j+2i-1} freq[k]

6: \operatorname{dist}[i+j] \leftarrow \left\lfloor \frac{2^P c_0}{c_0+c_1} \right\rfloor

7: j \leftarrow j+2i

8: end while
```

Output: $dist[2^D]$, a BAC distribution tree.

9: end for

The internal variables used by main BAC routines as follows. Note that interpretation of "input" and "output" differs for encoding and decoding, as does the size of a word.

- n Size of the decoded vector of words.
- b, l The interval is [b, b+l-1]. Can be implemented with P-bit arithmetic.
- c Scaled binary division point, P bits.
- x Current input byte or word.
- ibit Input bit counter.
- ibuf[] Input vector. Indexed by iptr as ibuf[iptr].
- owrd Stores an output word and carry bits; must have a type large enough for additional carry bits.
- obit Output bit counter.
- $obuf[\] \quad Output\ vector.\ Indexed\ by\ olen\ as\ obuf[olen].$

The encoding routine AriEncode (Algorithm 8) requires a helper function StoreWithCarry (Algorithm 7) to propagate carries. Arithmetic coding routines generally require an output buffer as these carries cannot be reliably predicted. The decoding routine AriDecode (Algorithm 10) utilises a helper function ShiftGetBit (Algorithm 9) to get new bits into the working variable x.

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Algorithm 7 StoreWithCarry(\mathbf{b}). Stores the highest bit of b to variable owrd. If the byte is full, it is stored to obuf[olen], while also adjusting the carry if necessary.

```
1: owrd \leftarrow 2 owrd + \left| \frac{b}{2P-1} \right|
                                                                                               Add highest bit of b to output, shift left
 2: obit \leftarrow obit + 1
                                                                                               Output bit counter.
3: if obit \geq 8 then
         obuf[olen] \leftarrow owrd \mod 2^8
 4:
          i \leftarrow \mathsf{olen}
5:
                                                                                               Index for carry propagation.
          while (owrd \geq 2^8) and (i>0) do
 6:
 7:
                                                                                               Proceed left.
              \mathsf{owrd} \leftarrow \left\lfloor \frac{\mathsf{owrd}}{2^8} \right\rfloor + \mathsf{obuf}[i]
 8:
 9:
              \mathsf{obuf}[i] \leftarrow \mathsf{owrd} \bmod 2^8
                                                                                               Add carry to bytes until done.
10:
          end while
11:
          \mathsf{obit} \leftarrow 0, \mathsf{owrd} \leftarrow 0, \mathsf{olen} \leftarrow \mathsf{olen} + 1
                                                                                               Full byte output.
12: end if
```

Algorithm 8 AriEncode. Perform Arithmetic Coding on a sequence of input words.

Input: ibuf[n]: A sequence of n input words from the alphabet.

Output: obuf[olen], a byte vector with the encoded data.

Input: dist $[2^{\hat{D}}]$: A BAC distribution tree table constructed with BuildDist.

```
1: b \leftarrow 0, l \leftarrow 2^P - 1
                                                                                        Initial base b and range l.
 2: owrd \leftarrow 0, obit \leftarrow 0, olen \leftarrow 0
                                                                                        Initialise StoreWithCarry output variables.
 3: for iptr = 0, 1, 2, \dots n-1 do
 4:
         x = \mathsf{ibuf}[\mathsf{iptr}].
                                                                                        Get a new input word x.
         \begin{aligned} & \textbf{for ibit} = D-1\dots, 2, 1, 0 \ \textbf{do} \\ & c \leftarrow \mathsf{dist}[\left(x \wedge \left(2^P - 2^{\mathsf{ibit}}\right)\right) \vee 2^{\mathsf{ibit}}] \end{aligned}
 5:
 6:
                                                                                        Get centre point from masked BAC frequency table.
 7:
             c \leftarrow \left\lfloor \frac{lc}{2P} \right\rfloor
                                                                                        Scale c to [0, l].
             if (x \wedge 2^{i\vec{b}it}) = 0 then
 8:
 9:
                 l \leftarrow c
                                                                                        Input bit is 0: select lower part.
10:
             else
                 b \leftarrow b + c, \; l \leftarrow l - c
11:
                                                                                        Input bit is 1: select higher part.
                 if b > 2^P then
12:
                     b \leftarrow b - 2^P, owrd \leftarrow owrd + 1
                                                                                        Carry. Note: owrd's type should not overflow.
13:
14:
                 end if
15:
             end if
             while l < 2^{P-1} do
16:
17:
                 StoreWithCarry(b)
                                                                                        Store the highest bit of b.
                 b \leftarrow 2b \bmod 2^P, \ l \leftarrow 2l \bmod 2^P
18:
                                                                                        Shift bounds left.
             end while
19:
20:
          end for
21: end for
22: while b > 0 or owrd \neq 0 do
          {\sf StoreWithCarry}(b)
23:
                                                                                        Purge remaining nonzero bits from b.
         b \leftarrow 2b \bmod 2^I
                                                                                        Shift b bound left.
24:
25: end while
```

Algorithm 9 ShiftGetBit(\mathbf{x}). Shifts x left and fetches a new bit from ibuf byte array.

```
1: \mathsf{ibit} \leftarrow \mathsf{ibit} - 1
                                                                                Decrease number of available bits.
 2: if ibit < 0 then
3:
         if iptr < ilen then
              \mathsf{iwrd} \leftarrow \mathsf{ibuf}[\mathsf{iptr}]
 4:
                                                                                Get a new byte.
 5:
              \mathsf{iptr} \leftarrow \mathsf{iptr} + 1
 6:
          else
 7:
              \mathsf{iwrd} \leftarrow 0
                                                                                Assume zeros.
 8:
          end if
9:
          ibit \leftarrow 7
                                                                                Manage input pointers.
10: end if
11: x \leftarrow (2x \mod 2^P) + (\lfloor \frac{\mathsf{iwrd}}{2^{\mathsf{ibit}}} \rfloor \mod 2)
                                                                                Shift x left, add a bit from iwrd at the bottom.
```

Algorithm 10 AriDecode. Perform Arithmetic Decoding on a sequence of input bytes.

```
Input: ibuf[ilen]: A sequence of input bytes.
Input: dist[2^D]: A BAC distribution tree table constructed with BuildDist.
 1: b \leftarrow 0, l \leftarrow 2^P - 1, x \leftarrow 0
                                                                       Initial base b, range l, and input variable x.
 2: iwrd \leftarrow 0, iptr \leftarrow 0, ibit \leftarrow 0
                                                                       Initialise input pointers.
 3: for i = 1, 2, \dots P do
         \mathsf{ShiftGetBit}(x)
                                                                       Fill up x with P input bits.
 5: end for
 6: for optr = 0, 1, 2, \dots n-1 do
        \mathsf{owrd} \leftarrow 0
                                                                       Zero this output word.
 7:
        \mathbf{for}\ \mathsf{obit} = D-1\dots,2,1,0\ \mathbf{do}
 8:
            c \leftarrow \mathsf{dist}[\left(\mathsf{owrd} \wedge (2^P - 2^{\mathsf{obit}})\right) \vee 2^{\mathsf{obit}}]
 9:
                                                                       Get centre point from masked BAC frequency table.
             c \leftarrow \left\lfloor \frac{lc}{2P} \right\rfloor
10:
                                                                       Scale c to [0, l[.
            if x - \tilde{b} < c then
11:
12:
                l \leftarrow c
                                                                       Output bit 0: Select lower part, reduce range.
13:
             else
14:
                b \leftarrow b + c
                                                                       Output bit 1: Select upper part.
15:
                l \leftarrow l - c
                                                                       Reduce range.
                \mathsf{owrd} \leftarrow \mathsf{owrd} \vee 2^{\mathsf{obit}}
16:
                                                                       Store bit.
17:
             end if
             while l < 2^{P-1} do
18:
19:
                \mathsf{ShiftGetBit}(x)
                                                                       Add an input bit to x.
                b \leftarrow 2b \mod 2^P, \ l \leftarrow 2l
20:
                                                                       Shift bounds left.
21:
             end while
22:
         end for
23:
         obuf[optr] \leftarrow owrd
                                                                       Store the output word.
24: end for
```