

Side-Channel Analysis of Keymill

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Abstract. One prominent countermeasure against side-channel attacks, especially differential power analysis (DPA), is fresh re-keying. In such schemes, the so-called re-keying function takes the burden of protecting a cryptographic primitive against DPA. To ensure the security of the scheme against side-channel analysis, the used re-keying function has to withstand both simple power analysis (SPA) and differential power analysis (DPA). Recently, at SAC 2016, Keymill—a side-channel resilient key generator (or re-keying function)—has been proposed, which is claimed to be inherently secure against side-channel attacks. In this work, however, we present a DPA attack on Keymill, which is based on the dynamic power consumption of a digital circuit that is tied to the $0 \rightarrow 1$ and $1 \rightarrow 0$ switches of its logical gates. Hence, the power consumption of the shift-registers used in Keymill depends on the $0 \rightarrow 1$ and $1 \rightarrow 0$ switches of its internal state. This information is sufficient to obtain the internal differential pattern (up to a small number of bits, which have to be brute-forced) of the 4 shift-registers of Keymill after the nonce (or *IV*) has been absorbed. This leads to a practical key-recovery attack on Keymill.

Keywords: side-channel analysis · fresh re-keying · differential power analysis

1 Introduction

Side-channel attacks like differential power analysis (DPA) pose a serious threat to devices operating in a hostile environment. Such scenarios quite naturally appear in our current information infrastructure whenever an entity has physical access to a device which uses a cryptographic key that must be kept secret to this entity. Hence, it is necessary to protect such devices against the extraction of the secret key by means of side-channel analysis like DPA and SPA. In particular, for resource-constrained or low-cost devices that are used for the Internet of Things, or in RFID applications, the use of protection mechanisms is not straightforward, since applied protection mechanisms have to be cheap and efficient. One protection mechanism that suits such applications very well is fresh re-keying.

Fresh re-keying [8] is an approach for precluding DPA on cryptographic primitives. The resistance against DPA is achieved by a separation-of-duties principle, where a re-keying function takes the burden of protection against DPA away

from the cryptographic primitive. In this construction, the re-keying function processes a nonce and master key to compute a fresh session key. This session key is then used by the cryptographic primitive. Since the cryptographic primitive is only called once per key, DPA attacks are naturally prevented, and only dedicated countermeasures against SPA are needed. However, the used re-keying function has to provide resistance against SPA and DPA attacks, either by its design, or by application of countermeasures like threshold implementations [9], masking [11], hiding [2], re-shuffling [6], etc. The intention behind re-keying schemes is that the re-keying function itself can be protected more easily against DPA than the cryptographic scheme, or that it can even be designed to provide inherent security against DPA. Hereby, it is beneficial for both options that the re-keying function itself does not need to fulfill strong cryptographic requirements [8].

Re-keying functions. Medwed et al. [8] proposed a polynomial multiplication as re-keying function, which has further been extended to the multi-user setting [7]. While such a polynomial multiplication lacks inherent protection against DPA, it is easy to mask and additionally allows simple to implement countermeasures against SPA as for instance reshuffling [8]. However, Pessl and Mangard [10] showed at CT-RSA 2016 that this multiplication is vulnerable to side-channel analysis. In particular, at the point where its masks have to be combined and the session key is used in the cryptographic scheme. Additionally, the original scheme by Medwed et al. is susceptible to time-memory trade-off attacks [3]. Recently at Crypto 2016, Dziembowski et al. [4] proposed a more formal treatment of re-keying functions and propose two schemes, where the first is based on learning parity with leakage and the second on learning with rounding, which are both efficient and easy to mask.

Keymill. In contrast to designs relying on side-channel countermeasures (e.g., masking) for side-channel resistance, Keymill [12] claims to be secure against SCA attacks inherently by design without requiring any redundant circuit. Having a re-keying function which provides inherent security against SCA attacks is beneficial with respect to implementation metrics. Since such schemes do not require masking to withstand DPA, no randomness is needed to create and update masks, and masks do not have to be stored and processed in the first place. A comparison of a modular multiplication and Keymill by Taha et al. [12] shows that a hardware implementation of Keymill requires 775 gate equivalents (GE), while an implementation of a modular multiplication with first-order masking requires 7300 GE [8].

Such small implementation costs for Keymill are possible, since it only consists of 4 shift-registers, where each acts as input for a non-linear function (taken from Achterbahn [5]). The outputs of those non-linear functions act as inputs for the shift-registers. The outputs are connected via a rotating cross-connect to the inputs of the 4 shift-registers. This cross-connect joins function outputs with shift-register inputs cyclically per clock. For this construction and also for

a toy example consisting of two 8-bit registers involving a similar rotating cross-connect, the authors claim that no DPA-attack is feasible without constructing a hypothesis for the whole key, or equivalently for the whole internal state of the four shift-registers.

Our Contribution. In this work, we show that the claim of Keymill to be inherently secure against side-channel attacks without the need of additional circuits does not hold by presenting a DPA attack on Keymill. The proposed attack does not require any hypothesis about concrete values of the internal state, or secret key. Instead, we recover the internal difference of neighboring bits of the shift-registers. As observed in [1,13], the dynamic power consumption of shift-registers depends on the number of internal differences of neighboring bits. The more internal differences we have, the more power the shift-register consumes. We recover those internal differences by comparing the power consumption of a reference nonce (e.g., 0), with power traces of a modified nonce, where a single bit has been flipped.

Outline. In Section 2, we give a brief background on fresh re-keying and restate the specification of Keymill. Then, we describe the side-channel attack on Keymill and on a variant of Toy Model II given in the Keymill specification in Section 3. Section 4 gives experiments for our attack and discusses the influence of different levels of noise. Finally, we conclude in Section 5.

2 Background

In this section, we first give a brief introduction to the concept of fresh re-keying, where we restate the requirements on re-keying functions. Then, we briefly summarize the specification of Keymill and finally, discuss time-memory trade-off attacks on such re-keying schemes.

2.1 Fresh Re-Keying

Fresh re-keying has been proposed by Medwed et al. in [8] as a countermeasure against side-channel and fault attacks for low-cost devices. A typical scenario, where fresh re-keying can be applied is the communication of an RFID tag with an RFID reader. Typically, RFID tags are low-cost mass devices that additionally have strict requirements regarding power consumption, not allowing costly protection mechanisms against side-channel and fault attacks of the implemented cryptographic primitives. This stands in contrast to the more expansive RFID readers, where costly protection mechanisms like masking are usually affordable.

Fig. 1 shows the working principle of fresh re-keying in a communication scenario between an RFID reader and an RFID tag. For sending a message, the tag generates an IV and derives a session key k^* by using a re-keying function g . This session key is then used by the block cipher E to encrypt the message

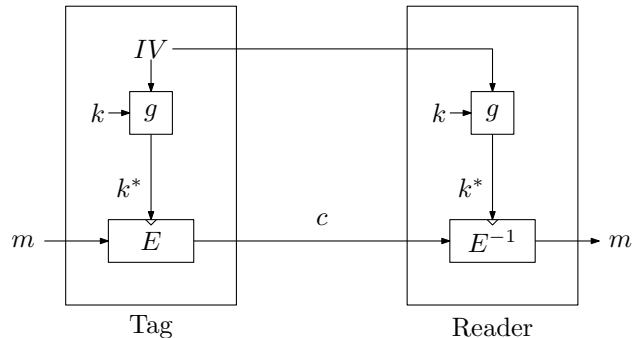


Fig. 1: Fresh re-keying scheme of Medwed et al. [8].

m . The ciphertext c together with the IV is sent to the reader, where it can be decrypted.

Since the IV is generated by the tag, the tag can ensure that the block cipher E is always used with a new session key k^* , which will preclude DPA on the block cipher. However, in the case of the reader, having a unique IV cannot be ensured, because the IV received over the communication channel and thus, might be chosen by an attacker. Therefore, the implementation of the block cipher E of the reader has to be protected against DPA by other means. Apart from that, the implementation of g for both entities has to withstand DPA, because here, the master key k is processed with different IV . On the designer's side, the challenge is to find a suitable re-keying function g which fulfills the following six properties given by Medwed et al. [8]:

1. Good diffusion of the master key k .
2. No synchronization between parties. Hence, g should be stateless.
3. No need for additional key material.
4. Little hardware overhead. Total costs lower than protecting E alone.
5. Easy protection against side-channel attacks.
6. Regularity.

One option for a re-keying function is the polynomial multiplication in $\mathbb{F}_{2^s}[y]$ modulo $p(y)$ proposed by Medwed et al. [8]:

$$g : (\mathbb{F}_{2^s}[y]/p(y))^2 \rightarrow \mathbb{F}_{2^s}[y]/p(y), \quad (k, r) \mapsto k \cdot r.$$

2.2 Brief Description of Keymill

Keymill [12] is a new keystream generator recently proposed by Taha, Reyhani-Masoleh and Schaumont at SAC 2016. In contrast to the fresh re-keying scheme by Medwed et al. discussed in Section 2.1, Keymill does not only provide one session key k^* , instead it provides a keystream. As indicated in Fig. 2, this is particularly useful when encrypting longer messages, requiring several block cipher calls.

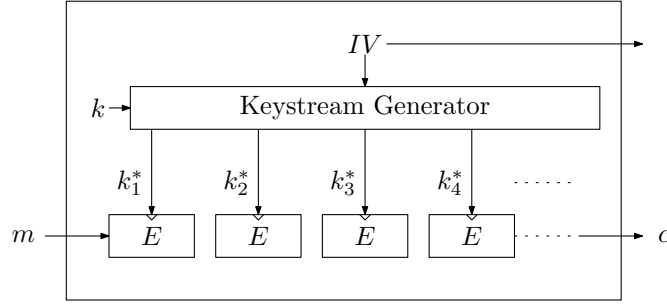


Fig. 2: Re-keying using a keystream generator as shown in [12].

Keymill operates on an internal state of 128 bits, composed of 4 NLFSRs as shown in Fig. 3. Shift-register R_0 has 31 bits, shift-registers R_1 and R_2 have 32 bits, and shift-register R_3 has 33 bits. The feedback functions F_0, F_1, F_2 and F_3 are selected from the set of feedback functions used for the stream cipher Achterbahn [5].

$$\begin{aligned}
F_0(S) = & s_0 + s_2 + s_5 + s_6 + s_{15} + s_{17} + s_{18} + s_{20} + s_{25} + s_8 s_{18} + s_8 s_{20} \\
& + s_{12} s_{21} + s_{14} s_{19} + s_{17} s_{21} + s_{20} s_{22} + s_4 s_{12} s_{22} + s_4 s_{19} s_{22} \\
& + s_7 s_{20} s_{21} + s_8 s_{18} s_{22} + s_8 s_{20} s_{22} + s_{12} s_{19} s_{22} + s_{20} s_{21} s_{22} \\
& + s_4 s_7 s_{12} s_{21} + s_4 s_7 s_{19} s_{21} + s_4 s_{12} s_{21} s_{22} + s_4 s_{19} s_{21} s_{22} \\
& + s_7 s_8 s_{18} s_{21} + s_7 s_8 s_{20} s_{21} + s_7 s_{12} s_{19} s_{21} + s_8 s_{18} s_{21} s_{22} \\
& + s_8 s_{20} s_{21} s_{22} + s_{12} s_{19} s_{21} s_{22}
\end{aligned}$$

$$\begin{aligned}
F_1(S) = F_2(S) = & s_0 + s_3 + s_{17} + s_{22} + s_{28} + s_2 s_{13} + s_5 s_{19} + s_7 s_{19} \\
& + s_8 s_{12} + s_8 s_{13} + s_{13} s_{15} + s_2 s_{12} s_{13} + s_7 s_8 s_{12} + s_7 s_8 s_{14} \\
& + s_8 s_{12} s_{13} + s_2 s_7 s_{12} s_{13} + s_2 s_7 s_{13} s_{14} + s_4 s_{11} s_{12} s_{24} \\
& + s_7 s_8 s_{12} s_{13} + s_7 s_8 s_{13} s_{14} + s_4 s_7 s_{11} s_{12} s_{24} + s_4 s_7 s_{11} s_{14} s_{24}
\end{aligned}$$

$$\begin{aligned}
F_3(S) = & s_0 + s_2 + s_7 + s_9 + s_{10} + s_{15} + s_{23} + s_{25} + s_{30} + s_8 s_{15} + s_{12} s_{16} \\
& + s_{13} s_{15} + s_{13} s_{25} + s_1 s_8 s_{14} + s_1 s_8 s_{18} + s_8 s_{12} s_{16} + s_8 s_{14} s_{18} \\
& + s_8 s_{15} s_{16} + s_8 s_{15} s_{17} + s_{15} s_{17} s_{24} + s_1 s_8 s_{14} s_{17} + s_1 s_8 s_{17} s_{18} \\
& + s_1 s_{14} s_{17} s_{24} + s_1 s_{17} s_{18} s_{24} + s_8 s_{12} s_{16} s_{17} + s_8 s_{14} s_{17} s_{18} \\
& + s_8 s_{15} s_{16} s_{17} + s_{12} s_{16} s_{17} s_{24} + s_{14} s_{17} s_{18} s_{24} + s_{15} s_{16} s_{17} s_{24}
\end{aligned}$$

The feedback functions are mixed via a rotating cross-connect, depending on the current clock cycle index i :

$$F_k \rightarrow R_{k+i \pmod{4}} \quad \text{for } k = 0, 1, 2, 3.$$

After loading the 128-bit secret key into the internal state, 4 bits of the 128-bit IV that can be monitored (or controlled) by the attacker are added to

the feedback functions of the shift-registers in each clock cycle. After absorbing the IV in 16 clock cycles, the internal state is clocked 33 more times before producing any output. Afterwards 4 bits of output are generated (one from each shift-register) in each clock cycle. We refer to the specification of Keymill [12] for a more detailed description.

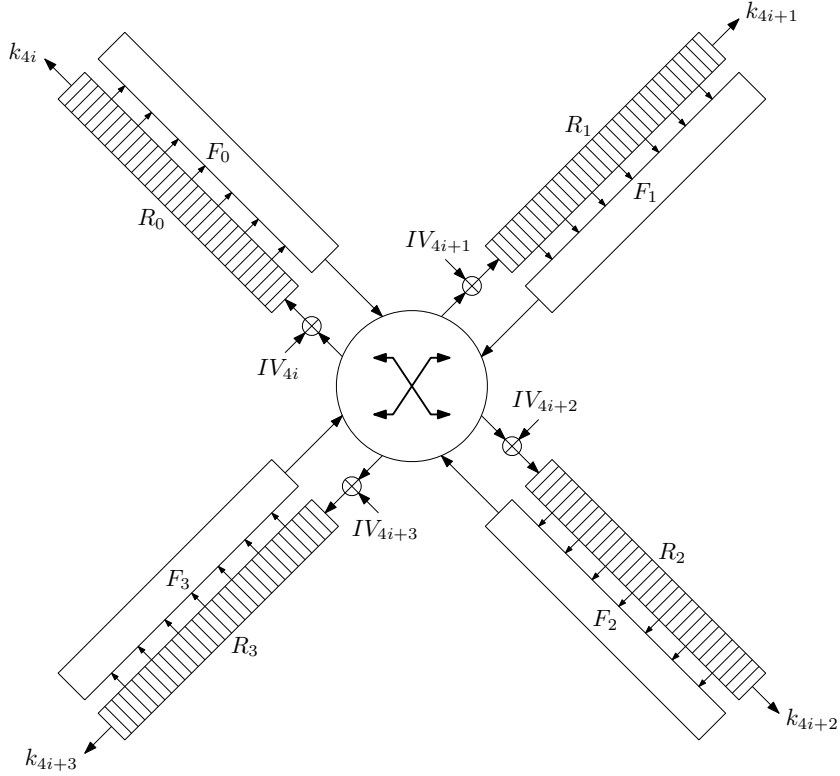


Fig. 3: Structure of Keymill

2.3 Remark on Time-Memory Trade-Off Attacks

As elaborated in [3], the re-keying scheme proposed by Medwed et al. [8] is susceptible to time-memory trade-off attacks dependent on the used re-keying function. For instance, if a polynomial multiplication is used together with AES-128, the used master key can be recovered with a complexity of 2^{65} [3]. Since Keymill has an internal state-size of 128-bits, similar attacks are possible on the scheme shown in Fig. 2.

3 Side-Channel Attack on Keymill

In this section, we will present side-channel attacks on Keymill. First of all, we discuss the power consumption of shift-registers following the work of Zadeh and Heys [13] and show how this power consumption can be used to recover the differences of neighboring shift-register bits. This and the fact that the first bits of the shift-registers are not used in the feedback functions of Keymill allows us to mount a side-channel attack. For simplicity, we first demonstrate the attack on a variant of Toy Model II given in the Keymill specification [12], and afterwards discuss the application to Keymill.

3.1 Power Consumption of a Shift-Register

In all our attacks, we exploit the dynamic power consumption of the shift-registers at the triggering edge of the clock (i.e., positive edge). More specifically, we observe the dynamic power consumption of the building blocks of the shift-registers, the D-flip-flops. As shown by Zadeh and Heys [13], the dynamic power consumption of a D-flip-flop at the triggering edge depends on whether its state changes or not. If the state of the D-flip-flop changes, more power is consumed than if it remains the same. As an example, Zadeh and Heys [13] analyze a D-flip-flop constructed out of 6 NAND gates. For such a flip-flop, 3 gates change if the flip-flop changes its state, whereas only one gate changes if not.

Next, we have a look at the power consumption of a shift-register. For simplicity, consider a 4-bit shift-register consisting of 4 flip-flops D_0 , D_1 , D_2 , and D_3 . In the following, we assume that D_4 is the input of our shift-register, which is shifted towards D_0 . For instance, let us consider the power consumption of the change from state $S_0 = 0110_2$ to state $S_1 = 1101_2$. For this transition, D_0 changes its state, D_1 keeps its state, D_2 changes its state, and D_3 changes its state. Since the power consumption of the flip-flops is higher if they change their state, the power consumption of the shift-register is correlated with the Hamming weight of $S_0 \oplus S_1 (= 1011_2)$. In this example, 3 flip-flops change their state.

Now, we want to consider a state change from S_0 to S'_1 , where we shift in a 0 instead of a 1 as before. So we observe the power consumption for the change from state $S_0 = 0110_2$ to state $S'_1 = 1100_2$. If this transition happens, only two flip-flops change their state. Thus, we observe for the transition $S_0 \rightarrow S'_1$ a smaller power consumption than for $S_0 \rightarrow S_1$. This allows us to derive information about the difference of the bits stored in D_4 and D_3 of S'_1 and S_1 , respectively. In more detail, we know that they are equal for S'_1 and different for S_1 . We will use this observation in our side-channel attack on a variant of Toy Model II and Keymill itself in the following sections.

3.2 Attack on Toy Model II

For the sake of simplicity, we first describe the working principle of our attack on a slightly modified variant of Toy Model II given in the Keymill specification [12],

which has only two 8-bit shift-registers. In the attack, we assume that similar to Keymill, the output of the first flip-flop of each shift-register is not connected to the feedback function, as shown in Fig. 4. This is the only assumption that is necessary to mount our attack. We do not rely on any other specific properties of the used feedback functions. The shift-register is preinitialized with the secret key. After that, the 16-bit IV is absorbed, 2 bits per clock cycle. Our goal is to recover all *internal differences* of both shift-registers after the IV (e.g., $IV = 0000_{16}$) has been absorbed.

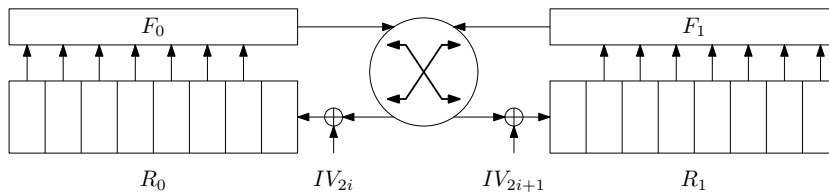


Fig. 4: Structure of modified Toy Model II

First of all, we collect two power traces, one for an IV starting with 00_2 and one for an IV starting with 10_2 . We look at the power consumption when the first two bits of the IV are absorbed in the first cycle. Here, we have a difference in IV_0 for R_0 , but equal values in IV_1 for R_1 . Since the first flip-flop of each shift-register is not connected to the feedback function, the circuit processes the same information for both initial values, except for the first flip-flop of the left shift-register R_0 . As already discussed in Section 3.1, this gives us information about the difference of the first two bits of R_0 after absorbing the first two bits of the IV . If the power consumption when absorbing 00_2 is higher than in the 10_2 case, we know that the first two bits of R_0 are different after 00_2 is absorbed. If the power consumption is lower, then they are equal.

Next, we use two initial values starting with 00_2 and 01_2 . This allows us to learn the internal difference of the first two bits of the shift-register R_1 after 00_2 is absorbed. Then, we use 0000_2 and 0010_2 to learn information of the difference of the first two bits after 0000_2 has been absorbed, still preserving the information of the difference of the now second and third bits of both shift-registers learned in the steps before. By continuing in this way, we can learn the differences of all neighboring bits of R_0 and R_1 after the IV 0000_{16} has been absorbed.

Now, guessing one bit in each shift-register determines the other 7 bits in each shift-register. Hence, we are left with only 4 different possible internal states. From this states on, we can invert Toy Model II step by step until we get 4 key candidates in total.

3.3 Attack on Keymill

Compared to Toy Model II, Keymill is just more of the same. As describe in Section 2.2, we have 4 shift-registers, one 31-bit shift-register, two 32-bit shift-

registers and one 33-bit shift-register. The 128-bit IV is absorbed in 32 cycles, each cycle taking 4 bits. Furthermore, the 4 used feedback functions of Keymill do not consider the outputs of the first flip-flop of each shift-register. As mentioned before, this fact is exploited in our side-channel attack. Again, we want to recover the internal differential pattern of the used shift-registers after a certain IV , e.g., $IV = 0 \dots 0$ has been absorbed.

The attack proceeds in a similar way as described in Section 3.2. First we recorded a power trace for an IV starting with 0000_2 and a second trace for an IV starting with 1000_2 . We compare the power consumption for the two traces at the time the first nibble of the IV is absorbed. At this time, for both traces, the processed values are equal except for the inputs of shift-register R_0 . Since the output of the first flip-flop of R_0 is not fed back into the feedback function, the power consumption differs only because of the state changes of this flip-flop. As discussed in Section 3.1, this is sufficient to recover the difference of the first two bits of shift-register R_0 . The power traces of IV 's starting with 0100_2 , 0010_2 , and 0001_2 can be used to learn the difference of shift-registers R_1 , R_2 and R_3 , respectively.

When the second nibble of the IV is absorbed, those differences are shifted by one position, but are still known, if the first nibble of the IV starts with 0000_2 . Hence, we can use IV 's starting with $0000\ 0000_2$, $0000\ 1000_2$, $0000\ 0100_2$, $0000\ 0010_2$, and $0000\ 0001_2$ and learn the differences of the first two bits of each shift-register, while retaining the knowledge of the differences between the second and the third bits. Proceeding this way, we can at most learn 32 differences of neighboring bits per shift-register.

This means that we can learn all internal differences of all 4 shift-registers, since one shift-register has 31 bits, two have 32 bits and one has 33 bits. So, at most 30, two times 31 and 32 differences have to be learned. Since we know all internal differences of each shift-register, a guess of one state bit in each shift-register determines all others. Thus, guessing 4 bits in total leads to 16 different states we recover. From these states, we can invert Keymill resulting in 16 possible key candidates in total.

3.4 A Note on Filtering the Noise

The success of our attacks crucially depends on the ability to distinguish power consumption changes for a change of the input values. This means that the noise level has to be small enough to reliably identify these changes. If the attacker is allowed to repeat IV s, averaging the trails and filtering the noise is no problem. Even if the IV is required to be unique (as usually the case), this can easily be done, since the state of the shift-registers only depend on bits of the IV that have already been absorbed. Hence, we can use all the remaining IV bits after the relation we want to recover to average the power consumption for this cycle. For Keymill, we can average over up to 16 power traces even if we recover bit relations in the penultimate IV absorbing cycle. Dependent on the noise level, it might happen that the last few internal differences of the state cannot be

recovered anymore, since there are too few trails to filter the noise. So these bits might have to be guessed additionally.

4 Practical Evaluation

In order to show the practicability of the attacks discussed in Section 3. We simulate the described attack targeting a variant of Toy Model II. Therefore, two 8-bit NLFSRs have been modelled in software using two different, randomly chosen feedback-functions¹ F_1 and F_2 . For every clock cycle the simulation returns the Hamming distances produced by the shifts in both shift-registers. The current Hamming distance depends on the values in the shift-registers (before the first clock cycle the values of the two shift-registers equal the secret key), the results of F_1 and F_2 and the IV . Gaussian noise with zero mean ($\mu_{noise} = 0$) and varying standard deviation σ_{noise} can be added to the noise-free Hamming-distance measurements ($HD_{noisefree}$) in order to simulate measurements captured from real hardware, i.e. HD_{meas} (see Equation 1). In order to minimize the influence of the noise it is possible to repeat the simulation with similar IV k -times for calculating the mean of the measurements.

$$HD_{meas} = HD_{noisefree} + noise; \quad noise = \mathcal{N}(0, \sigma_{noise}) \quad (1)$$

For every setting (specific σ_{noise} and specific k) $N_{full} = 500$ experiments with randomly chosen initial states of the two 8-bit shift-registers have been performed to calculate the success rate $SR = \frac{N_{success}}{N_{full}}$ of the attack. $N_{success}$ equals the number of successful state recoveries. Fig. 5 depicts the results of this simulation (solid lines) together with results from a simulation of 4 8-bit shift-registers operating with a rotating cross-connect like the one used in Keymill (dotted lines) to see how the success rate scales for larger structures. Both variants show a similar success rate that decreases with increasing noise. This effect can be compensated by repeating the attack with the same IV k -times and calculate the mean of the measurements. Since the presented attack does not rely on specific feedback functions, nor is limited by the size of the registers, the presented results can be generalized to any register size. The change in the Hamming distance is influenced only by the value entering the shift register (first value) and the value leaving the shift register (last value), making the attack independent of the register size. Therefore, similar attacks work for the original Keymill-structure with the four registers with sizes (31, 32, 32, 33) bits.

Fig. 6 shows the influence of σ_{noise} on the Hamming-distance measurements (HD_{meas}). For this specific plot, $HD_{noisefree} = 8$ has been selected. The black '+' markers represent single HD measurements. In the noise-free scenario, i.e. $\sigma_{noise} = 0$, all HD measurements have the value 8. For a high noise level, i.e. $\sigma_{noise} = 2$, the HD measurements are in the range between 2 and 14.

¹ A list of feedback functions can be found at <https://people.kth.se/~dubrova/nlfsr.html>

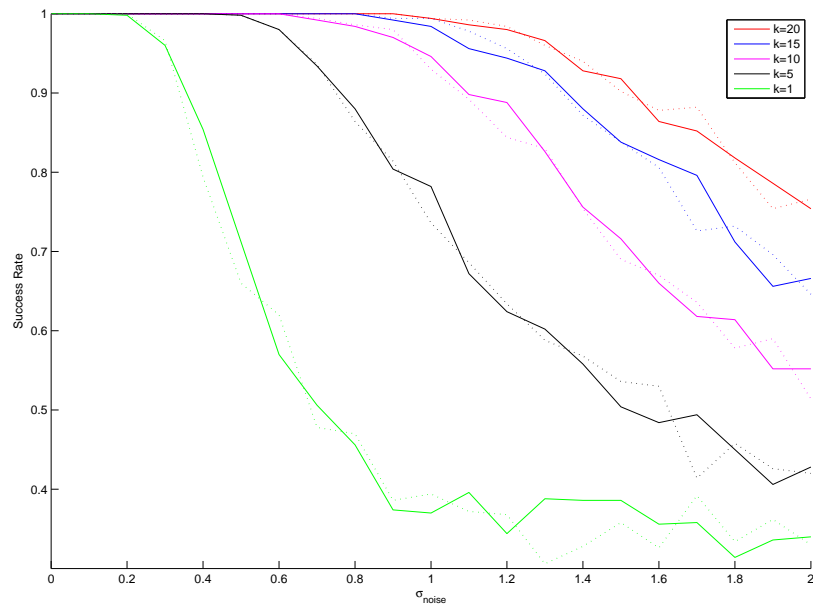


Fig. 5: Success rate for different noise levels. Solid lines represent simulations with two 8-bit shift-registers, dotted lines represent simulations with 4 8-bit shift-registers. For the different graphs different numbers (1–20) of Hamming-distance measurements have been used for calculating the mean Hamming distance.

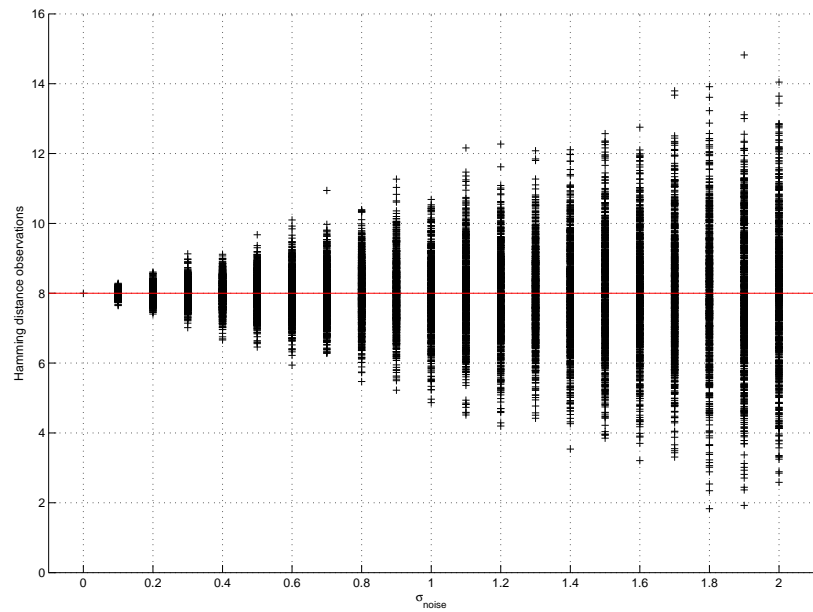


Fig. 6: Hamming distance measurements for increasing σ_{noise} , $HD_{noisefree} = 8$.

5 Conclusion

In this work, we showed that a DPA on Keymill is feasible. In contrast to the DPA attacks that are claimed to be thwarted by the specification of Keymill, we do not recover the actual values of Keymill's internal state. Instead, we recover the differences of neighboring bits. Our attack violates the claim by the designers that Keymill is secure against side-channel attacks inherently by design. Indeed, we show that Keymill needs dedicated countermeasures against DPA attacks exploiting internal differences.

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