# Methods for Masking CRYSTALS-Kyber Against Side-Channel Attacks 

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#### Abstract

In the context of post-quantum secure algorithms like CRYSTALS-Kyber, the importance of protecting sensitive polynomial coefficients from side-channel attacks is increasingly recognized. Our research introduces two alternative masking methods to enhance the security of the compression function in Kyber through masking. Prior to this, the topic had been addressed by only one other research study. The "Double and Check" method integrates arithmetic sharing and symmetry adjustments, introducing a layer of obfuscation by determining coefficient values based on modular overflows. In contrast, the Look-Up-Table (LUT) integration method employs arithmetic-to-Boolean conversions, augmented by a pre-computed table for efficient value verifications. Furthermore, by leveraging the alternative prime 7681, we propose a novel masked compression function. This prime, 7681, is also notable as the smallest prime suitable for fast NTT multiplication. While both algorithms prioritize data protection and streamlined processing, they also underscore the inherent challenges of balancing computational speed with the potential vulnerabilities to side-channel attacks.


Index Terms-Masking, CRYSTALS-Kyber, Post Quantum Cryptography, Side Channel Attacks

## I. Introduction

As we transition into the quantum computing era, the security of many commonly used cryptographic algorithms faces a significant challenge. Quantum computers have the capability to solve complex mathematical problems that threaten the safety of traditional cryptosystems such as RSA, DSA, and elliptic curve cryptosystems. Recognizing this, NIST initiated the Post-Quantum Cryptography (PQC) standardization process [1]. Although Round 3 has recently concluded, many studies regarding to side-channel attacks have emerged, targeting Kyber's implementation methods in both software and hardware [2]-[4].

Drawing from the insights of foundational studies like [5] and [6], which underscore the importance of masking against side-channel attacks, researchers has integrated this protective measure across CRYSTALS-Kyber's components. Prior studies has already masked polynomial operations, especially the NTT multiplication [7], along with basic operations like addition and subtraction detailed in Alg. 5. Additionally, symmetric operations, specifically the hash function $G$ (See Alg. 4 and $P R F$ (See Alg. 5), have been enhanced with the Keccak masking method as outlined in [8]. For sampling tasks involving the central binomial distribution $\left(C B D_{\eta}\right)$ in Alg. 5,
the methodology from [9], which is further elaborated in [10], is employed to ensure masking.

While many components of Kyber are masked, there had been no effort to mask the compression function (see Eq. 3) used in the CPAPKE.Dec() algorithm of Kyber. This was addressed in the work published by [10], which introduced a bit-sliced binary search method on randomly generated sharings of sensitive polynomial coefficients. Building upon this study [10], we propose two alternative approaches for masking the compression function in Kyber: the Double and Check method, adding an additional obfuscation layer, and the classic memory/time trade-off using Look-Up-Table (LUT) integration.

In Section II, we provide essential information about Kyber and masking that will be referenced throughout the paper. In Section III, we discuss existing techniques related to masking compression functions and highlight how conventional methods are ill-suited for Kyber due to its prime modulo design choice. In Section IV, we present the Double and Check (See Alg. 1) and Look-Up-Table integration (See Alg. 2) method as alternatives to masking the compression function in Kyber. Additionally, we demonstrate how choosing 7681 as a prime provides a fast and masked compression function. And finally, in Section V, we present the conclusion, with areas that may rise concern or require further studies.

## II. PRELIMINARIES

In this chapter, we are going to provide the notations and definitions regarding to protection against side-channel attacks.

## A. Ring of Polynomials

CRYSTALS-Kyber is a post-quantum key encapsulation mechanism (KEM) that works in the ring of polynomials over the finite field $\mathbb{Z}_{q}$. This ring is denoted by $R_{q}=\mathbb{Z}_{q}[x] /\left\langle x^{n}+\right.$ $1\rangle$. As given in the Table I, Kyber uses a prime module $q=3329$, causing a non-negligible performance overhead compared to modulo $2^{k}$ as studied in [11], and $n=256$, meaning that Kyber works with polynomials that have 256 coefficients, each of them taken from the finite field $\mathbb{Z}_{3329}$.

## B. Compression and Decompression in Kyber

In CCA-secure CRYSTALS-Kyber, a compression function is defined [12], which takes an element $x \in \mathbb{Z}_{q}$ as input and

| 0 | $\frac{q}{4}$ | $\frac{q}{2}$ | $\frac{3 q}{4}$ |
| :--- | :--- | :--- | :--- |
| 0 | 1 | 1 | $\boldsymbol{q}$ |

Fig. 1. Two Disjoint Interval to Compress a Polynomial Coefficient into a Single Bit [14]
returns an integer within the range $\left\{0, \ldots, 2^{d}-1\right\}$, where $d<\left\lceil\log _{2}(q)\right\rceil$.

$$
\begin{equation*}
\operatorname{Compress}_{q}(x, d)=\left\lceil\left(2^{d} / q\right) \cdot x\right\rfloor \bmod ^{+} 2^{d} \tag{1}
\end{equation*}
$$

The core motivation behind this function, as the name gives a hint, is to omit the low-order bits of ciphertext (i.e., shortening the ciphertext) that do not significantly affect the decryption failure. Additionally, it leverages the LWE error correction advantages during both encryption and decryption [13].

Authors [12] also define a decompressing function such that

$$
\begin{equation*}
x^{\prime}=\operatorname{Decompress}_{q}\left(\operatorname{Compress}_{q}(x, d), d\right), \tag{2}
\end{equation*}
$$

where $x^{\prime}$ is an element in close proximity to $x$, more specifically, $\left|x^{\prime}-x \bmod ^{ \pm} q\right| \leq B:=\left\lceil\frac{q}{2^{d+1}}\right\rfloor$.

In the case of $d=1$, the Compress $_{q}(x, d=1)$ function takes an element $x \in \mathbb{Z}_{q}$ as input and maps it to an integer within the range $\left\{0, \ldots, 2^{d=1}-1\right\}$, which is simply the set $\{0,1\}$. Therefore, result of the Compress $_{q}(x, 1)$ function can be either 0 or 1 .

The Compress ${ }_{q}$ function works by taking the domain of each polynomial coefficient, which is the set of all integers between 0 and $q$ (i.e, $[0, q-1]$ ), and splitting it into two disjoint intervals (See Eq. 3).
Compress $_{q}(x, 1)=\lceil(2 / q) \cdot x\rfloor \bmod 2= \begin{cases}1, & \text { if } \frac{q}{4}<x<\frac{3 q}{4}, \\ 0, & \text { otherwise. }\end{cases}$

The function then assigns a value to each coefficient, depending on which interval it falls into. For coefficients in the interval of $\left[\frac{q}{4}, \frac{3 q}{4}\right]$ (the area coloured in pink in Fig. 1), the compression function assigns the value 1 . For coefficients in the second interval $\left[0, \frac{q}{4}\right]$ and $\left[\frac{3 q}{4}, q\right]$ (blue area in the Fig. 1), the compression function assigns the value 0 .

## C. Kyber Public Key Encryption

Kyber is a module-Learning-with-Error (LWE) scheme [15], which can be parameterized with a given security parameter $k$ (see Table I). When the security parameter $k$ is set to 2 or 3, we are dealing with a module-LWE problem (See the Lines $4-8$ in Alg. 5). In other words, the notation $k>1$ refers to the number of rings used in the module-LWE setting. Thus, when $k=1$, it indicates that there is one ring, and it becomes a ring-LWE problem.

Security of Kyber depends on the difficulty of distinguishing between pairs, $\left(\mathbf{a}_{i}, b_{i}\right) \in \mathbb{R}_{q}^{k} \times \mathbb{R}_{q}$. While some of the pairs are uniformly sampled from the ring of polynomials $\mathbb{R}_{q}$, others are constructed such that $\mathbf{a}_{i}$ selected from a uniform

TABLE I
Parameters of Kyber with three levels of security, $k$.

|  | $n$ | $k$ | $q$ | $\eta_{1}$ | $\eta_{2}$ | $\left(d_{u}, d_{v}\right)$ | $\delta$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| KYBER512 | 256 | 2 | 3329 | 3 | 2 | $(10,4)$ | $2^{-139}$ |
| KYBER768 | 256 | 3 | 3329 | 2 | 2 | $(10,4)$ | $2^{-164}$ |
| KYBER1024 | 256 | 4 | 3329 | 2 | 2 | $(11,5)$ | $2^{-174}$ |

distribution and the secret vector $\mathbf{s}$ is taken from a central binomial distribution (CBD) with given $\eta$ (See Table I) [10], $\mathbf{s} \in \beta_{\eta}^{k}$, and some noise with small coefficients $e_{i} \in \beta_{\eta}$ such that $b_{i}=\mathbf{a}_{i}^{T} \mathbf{s}+e_{i}$.

## D. Masking as a Side-Channel Attack Countermeasure

At its core, masking involves randomly splitting a secret value into multiple shares. These shares are processed independently throughout the algorithm's steps, and the results are then recombined to produce the desired outcome. Working within the masking domain prevents information leakage from a sensitive variable $a$ since it is never used directly. Instead, side-channel leakage arises only from calculations involving $a_{1}$ or $a_{2}$ [16]. Given that both arithmetic and Boolean shares of $a$ are randomly defined each time, any leakage from $a_{1}$ and $a_{2}$ doesn't expose $a$ to attackers even if the attacker runs the algorithm multiple times. As highlighted before, there are two types of masking: Boolean and arithmetic.

Consider a sensitive value $a \in \mathbb{Z}_{q}$. Arithmetical masking randomly splits this sensitive value into $n$ arithmetic shares, which can be denoted as $\mathbf{n}_{s}$. The defining characteristic is that the summation of these shares results in the original value $a$, situated within the set of integers modulo $q$ [10]. For $i \in$ $\left\{0,1 \cdots \mathbf{n}_{s}-1\right\}, a^{(i)}$ denotes the specific share indicated by the number $i$.

$$
a \equiv a^{(0)_{A}}+a^{(1)_{A}}+\cdots+a^{\left(n_{s}-1\right)_{A}} \quad \bmod q
$$

When the sensitive value $\mathbb{Z}_{q}$ is split into $\mathbf{n}_{s}$ shares, we can represent $a$ as $\mathbf{n}_{s}$ tuple. This tuple is given by $a^{(\cdot)_{A}}=$ $\left(a^{(0)_{A}}, \cdots a^{\left(n_{s}-1\right)_{A}}\right)$. In the Boolean masking case,

$$
a=a^{(0)_{B}} \bigoplus a^{(1)_{B}} \bigoplus \cdots \bigoplus a^{\left(n_{s}-1\right)_{B}}
$$

the $\mathbf{n}_{s}$-tuple representation of a secret coefficient $a \in \mathbb{Z}_{q}^{2}$ can be denoted as $a^{(\cdot)_{B}}=\left(a^{(0)_{B}}, \cdots a^{\left(n_{s}-1\right)_{B}}\right)$, which comprises $n_{s}$ arithmetic shares $a^{(i)_{B}}$ with $0 \leq i<n_{s}$.

## III. Existing Methods for Masked Compression

In this section, we discuss the reasons why the compression function introduced in Algorithm 3 in Kyber (Refer to Line 4) requires masking. We will also delve into the workings of the Most Significant Bit (MSB) approach. Furthermore, we'll explore how Kyber's choice of a prime modulo design restricts us from employing this approach to mask the compression function, as illustrated in Eq. 3. Finally, we will introduce a novel bit-sliced binary search method, as proposed by [10].


Fig. 2. 1-Bit Conversion for Polynomial Coefficient in Kyber, Updated from [10]

| 0 | $\frac{q}{4}$ | $\frac{q}{2}$ | $\frac{3 q}{4}$ | $q$ |
| :---: | :---: | :---: | :---: | :---: |
| $\operatorname{Compress}_{q}^{s}(x)=0$ | $\operatorname{Compress}_{q}^{s}(x)=1$ |  |  |  |

Fig. 3. Shifting the Polynomial Coefficient with Certain Offset to Mask It.

## A. Need for a Masked Compression Algorithm in Kyber

To achieve the CCA2 security, CRYSTALS-Kyber [12] algorithm leverages the Fujisaki-Okamoto (FO) transformation [17], [18]. It first performs CPA decryption on the ciphertext (See Line 4 in Alg. 4). Then, the message is "re-encrypted" by CPA encryption (See Line 6 in Alg. 4), resulting in ciphertext $c^{\prime}$. Finally, the algorithm checks whether the public ciphertext $c$ equals $c^{\prime}$. If $c=c^{\prime}$, the algorithm turns True, and False otherwise. Depending on the Boolean result, the session key $K$ is generated (See Lines 7-10 in Alg. 4). The FO-transform is done to see if an adversary performs any alteration during the process.

Any steps that directly processes the secret vector $\mathbf{s} \in \mathbb{R}_{q}^{k}$ should be masked against side-channel attacks. Thus, the steps that are coloured grey in the Fig. 2 should be masked. Up until the study done by [10], there was no research that proposed a masking for the compression function in Kyber.

$$
\begin{equation*}
\operatorname{Encode}_{1}\left(\operatorname{Compress}_{q}\left(v-\operatorname{NTT}^{-1}\left(\hat{\mathbf{s}}^{T} \circ \operatorname{NTT}(\mathbf{u})\right), 1\right)\right) \tag{4}
\end{equation*}
$$

As it is seen in the Eq. 4, the Alg. 3 directly processed the secret key $\mathbf{s} \in \mathbb{R}_{q}^{k}$. As the vector $\mathbf{u} \in \mathbb{R}_{q}^{k}$ and polynomial $v \in \mathbb{R}_{q}$ is publicly known, adversaries can easily do reverseengineering to find the secret vector $\mathbf{s}$. Therefore, the compression function needs to be masked [10].

## B. The Most Significant Bit (MSB) Method

In the most trivial approach for masking in modulo $q=2^{k}$, the coefficient domain is shifted by a specific offset to form a new interval composed of two equal sub-intervals. This is done by adding $\left\lfloor\frac{q}{4}\right\rfloor$ in $\bmod q$ to each coefficient share. Once each interval has been shifted by, $\left\lfloor\frac{q}{4}\right\rfloor$, a symmetry is established around $\left\lfloor\frac{q}{2}\right\rfloor$ as illustrated in Fig. 3. The new Compress ${ }_{q}(x, 1)$ function can be seen in the Eq. 5.

$$
\begin{gather*}
\operatorname{Compress}_{q}^{s}(x):= \begin{cases}0, & \text { if } x<\frac{q}{2} \\
1, & \text { otherwise }\end{cases} \\
\text { Compress }_{q}(x, 1)=\text { Compress }_{q}^{s}\left(x+\left\lfloor\frac{q}{4}\right\rfloor \bmod q\right) \tag{5}
\end{gather*}
$$



Fig. 4. Saber $\left(q=2^{k}\right)$, Shifted Compress ${ }_{q}^{s}$ Function.


$$
2^{\left\lceil\log _{2}(q)\right\rceil-1}
$$

Fig. 5. Offset Between the Most Significant Bit (MSB) and $\left\lfloor\frac{q}{2}\right\rfloor$

Unlike CRYSTAL-Kyber, many other post quantum (PQC) algorithms like Saber uses a modulo $q$ such that $q=2^{k}$. And for algorithms like Saber, this trivial shifting method can be efficiently implemented. This is because, the most significant bit of a sensitive coefficient $x$ will be a great indicator to tell whether the coefficient will fall into the first half of a coefficient domain, such as $\operatorname{MSB}(x)=0$ in the case where $x<\left\lfloor\frac{q}{2}\right\rfloor$. Given that modulo $q=2^{k}$, its most significant bit (MSB) will have a value of $2^{\left\lceil\log _{2}(q)\right\rceil-1}=2^{k-1}$. Moreover, $\frac{q}{2}=\frac{2^{k}}{2}=2^{k-1}$. As a result, the ability of the interval to be divided into two disjoint intervals of equal spacing by the MSB helps us determine whether a share $x \in \mathbb{Z}_{q}$ is greater than or equal to $\left\lfloor\frac{q}{2}\right\rfloor$.
Unfortunately, in Kyber, where the module is prime $q=$ 3329, the interval space is not equally divided by specific bits as in Saber. The MSB in Kyber has a specific offset (348) over $\left\lfloor\frac{q}{2}\right\rfloor$ as it is given Fig. 5. Thus, the most significant bit (MSB) approach does not work for the Kyber. The offset arises because CRYSTAL-Kyber employs a prime number $q=3329$. In this context, the value of the $\operatorname{MSB}\left(2^{11}\right)$ equates to " 2048 " in the coefficient domain, which can be seen in the following: $2^{\left\lceil\log _{2}(q)\right\rceil-1}=2^{\left\lceil\log _{2}(3329)\right\rceil-1}=2^{\lceil 11.7\rceil-1}=2^{12-1}=2^{11}=$ 2048. As $\lfloor q / 2\rfloor=\lfloor 3329 / 2\rfloor=\lfloor 1664.5\rfloor=1664$, the offset is $2048-1664=832$. Because the interval is not divided into equal parts as in Saber, the MSB approach cannot be directly applied to CRYSTALS-Kyber and thus necessitates an update.

## C. Bit-Sliced Binary Search Method

An eye opening study regarding to masking the compression function in Kyber is done by [10], implementing a bit-sliced binary search over the 12-bits length Boolean version of an arithmetically shifted $\left(\left\lfloor\frac{q}{4}\right\rfloor\right)$ share to decide whether the share is greater than equal to $\left\lfloor\frac{q}{2}\right\rfloor$. The algorithm is provided to be $t-\mathrm{SNI}$ secure with $\mathbf{n}_{s}=t+1$ shares [10].

This algorithm takes each 256 coefficient of a polynomial $a \in \mathbb{Z}_{q}[X]$ and splits each coefficient into its $\mathbf{n}_{s}$ shares. Each share is shifted by $\left\lfloor\frac{q}{4}\right\rfloor$ to make the interval symmetric around
$\left\lfloor\frac{q}{2}\right\rfloor$ (See Fig. 3), making the decision of whether a share is greater than equal to $\left\lfloor\frac{q}{2}\right\rfloor$ much more easier. Then, each share is cast to its 12-bit Boolean version. $k=\left\lceil\log _{2}(q)\right\rceil=$ $\left\lceil\log _{2}(3329)\right\rceil \approx\lceil 11.7067\rceil=12$

Now that we have the Boolean-shares, the algorithm performs a Bitslicing on the $n_{s}$-tuple of a Boolean-shared $a_{i}^{(\cdot)_{B}}$ bits. In other words, since $a_{i}^{(\cdot)_{B}}$ consists of 12-bit length $n_{s}$ shares, the algorithm slices $a_{i}^{(\cdot)_{B}}$ into 12 bits. Then, the algorithm uses the following bit-wise Compress $_{q}^{s}(x)$ function to decide whether the share is greater than equal to $\left\lfloor\frac{q}{2}\right\rfloor$.

$$
\begin{equation*}
\operatorname{Compress}_{q}^{s}(x)=x_{11} \oplus\left(\neg x_{11} \cdot x_{10} \cdot x_{9} \cdot\left(x_{8} \oplus\left(\neg x_{8} \cdot x_{7}\right)\right)\right) \tag{6}
\end{equation*}
$$

If $x_{11}=1$, then $2^{11}=2048>1664$, and the coefficient should be compressed to 1 . If $x_{11}=0$, we need to consider the remaining bits. If $x_{10}=1$ and $x_{9}=1$, then the share is in the range $[1536,2048)$. Further analysis of $x_{8}$ and $x_{7}$ is required. If $x_{8}=1$, the coefficient is in the range [1664, 2048) and should be compressed to 1 , regardless of the value of $x_{7}$. If $x_{8}=0$ and $x_{7}=1$, the coefficient is exactly 1664 , and it should be compressed to 1 . In all other cases (where $x_{10}=0, x_{9}=0$ ), the coefficient is less than 1664 , and it should be compressed to 0 . The output of this algorithm looks like a vector depending on the number of shares, and is used by other masked components in Kyber.

## IV. Contribution

In this section, we are going to present two alternative algorithms to masking the compression function in Kyber: Double and Check (See Alg. 1) and Integration of Look-UpTable (LUT).

## A. Double and Check Algorithm

The Double and Check algorithm provides a method to handle arithmetic shares, introducing a level of obfuscation. In this approach, everything up to and including the shifting procedure aligns with [10]. Each coefficient of a polynomial in $\mathbb{Z}_{q}[X]$ is divided into several "shares", with the property that the sum of the shares modulo $q$ equals the original value. Each share is then shifted by adding $\lfloor q / 4\rfloor$, which makes the interval symmetric around $\lfloor q / 2\rfloor$ (See Fig. 3). Retaining the symmetric interval property of the coefficient domain ensures consistent overflow behavior beyond $q=3329$. This allows the algorithm to yield a Boolean output, indicating if the original share exceeded $\lfloor q / 2\rfloor$.

After the symmetry is achieved, the algorithm doubles each shifted share. This doubling acts as an obfuscated method for indirectly gauging if the shifted share, when multiplied by two, surpasses the modulo $q$. If it does, it implies that the original share was greater than $\lfloor q / 2\rfloor$. This overflow, or surpassing of the modulo, is detected by checking if the doubled share after taking modulo $q$ is different from the raw doubled share (before taking modulo). If these two values are not the same, an overflow has occurred.

Hence, the "Check" in Double and Check (See Alg. 1) is the procedure of observing whether this overflow took place.

```
Algorithm 1 Double and Check algorithm
Require: An arithmetic sharing \(a^{(\cdot)_{A}}\) of a polynomial \(a \in\)
    \(\mathbb{Z}_{q}[X]\), with each coefficient is masked by \(n_{s}\) shares such
    that \(a_{i}^{(\cdot)_{A}}=a_{i}^{(0)_{A}}+a_{i}^{(1)_{A}}+\cdots+a_{i}^{\left(n_{s}-1\right)_{A}} \bmod q\).
Ensure: A Boolean sharing \(m^{\prime(\cdot)_{B}}\) indicating if each share is
    greater than \(q / 2\) with \(m^{\prime}=\operatorname{Compress}_{q}(a, 1) \in \mathbb{Z}_{2^{256}}\).
    for \(i=0\) to 255 do
        for \(k=0\) to \(n_{s}-1\) do
            \(a_{i}^{(k)_{A}} \leftarrow a_{i}^{(k)_{A}}+\left\lfloor\frac{q}{4}\right\rfloor \bmod q \quad \triangleright\)
    Shift each share by round.down \(\left(\frac{q}{4}\right)\) to make the domain
    interval \(([0, q-1])\) symmetric around \(\frac{q}{2}\).
        raw_double_share \(\leftarrow 2 * a_{i}^{(k)_{A}{ }^{2}} \quad \triangleright\) Calculate the
    doubled share without modulo \(q\) once
        double_share \(\leftarrow\) raw_double_share \(\bmod q\)
        \(m_{i}^{(k)_{B}} \stackrel{\text { - }}{ } \stackrel{\text { double_share }}{ } \neq\) raw_double_share \(\triangleright\)
    Check if doubling the shifted share overflows \(q\)
        end for
    end for
    return \(m^{(\cdot)_{B}}\)
```

When overflow is detected, the algorithm outputs True with value 1 , indicating the original share was above the midpoint $\lfloor q / 2\rfloor$. If no overflow occurred, the output is False with value 0 . The result will be an array of $\mathbf{n}_{s}$ bits for each coefficient.

## B. Integration of Look-Up-Table (LUT)

In this section, we are going to introduce a Look-Up-Table (LUT) integration, which represents a classic trade-off between time (computational speed) and space (memory/storage). In many scenarios, especially in cryptography, where speed is paramount, this trade-off is acceptable. The LUT-based method uses one Arithmetic-to-Boolean conversion per each share (See Alg. 2).

The algorithm takes each sensitive coefficient of a polynomial $x \in \mathbb{Z}_{3329}[X]$ as input and splits it into $\mathbf{n}_{s}$ shares. Each share is shifted by $\left\lfloor\frac{q}{4}\right\rfloor=832$. This is again done to my the interval symmetric around $\left\lfloor\frac{q}{2}\right\rfloor=1664$. Subsequently, every share is converted into its 12 -bit Boolean representation (Arithmetic-to-Boolean). This representation is then used to determine if the share is greater than or equal to 1664 , a decision facilitated by a Look-Up-Table (LUT).

Therefore, since only the 5 most significant bits of a number are needed to check whether it is greater than or equal to $\left\lfloor\frac{q}{2}\right\rfloor=1664_{10}=(\mathbf{0 1 1 0 1 0 0 0 0 0 0 0})_{2}$, the algorithm requires $2^{5}=32$ entities (See Table II).

To illustrate the process, consider a coefficient share $2035 \in$ $\mathbb{Z}_{3329}$, which is already shifted by $\left\lfloor\frac{q}{4}\right\rfloor$ in $\bmod q=3329$. First, we convert 2035 into its binary representation, resulting in $(01111111011)_{2}$. From this binary form, we extract its 5 most significant bits, which are 01111 (equivalent to 15 in decimal notation). Then, by checking the value in the Look-Up-Table at index 15, we find it's 1 . Consequently, the Boolean representation for the share 2035 is determined to be 1 .

TABLE II
Bit Representation, Index, and Values.

| 5-bit | Index | Value |
| :---: | :---: | :---: |
| 00000 | 0 | 0 |
| 00001 | 1 | 0 |
| $\ldots$ |  |  |
| 01100 | 12 | 0 |
| 01101 | 13 | 1 |
| $\ldots$ |  |  |
| 11110 | 30 | 1 |
| 11111 | 31 | 1 |

```
Algorithm 2 LUT-based Compression Algorithm
Require: An arithmetic sharing \(a^{(\cdot)_{A}}\) of a polynomial \(a \in\)
    \(\mathbb{Z}_{q}[X], a_{i}^{(\cdot)_{A}}=a_{i}^{(0)_{A}}+a_{i}^{(1)_{A}}+\cdots+a_{i}^{\left(n_{s}-1\right)_{A}} \bmod q\).
Ensure: A Boolean sharing \(m^{(\cdot)_{B}}\) indicating if each offset
    share is greater than \(q / 2\).
    Pre-compute LUT for indices \(0-31\), each being a binary
    representation of an integer \(i\) with bits \(x_{11}, x_{10}, x_{9}, x_{8}, x_{7}\).
    for \(i=0\) to 255 do \(\triangleright\) Loop through all 256 coefficients
        for \(k=0\) to \(n_{s}-1\) do \(\triangleright\) Loop through all shares for
    each coefficient
            \(a_{i}^{(k)_{A}} \leftarrow\left(a_{i}^{(k)_{A}}+\left\lfloor\frac{q}{4}\right\rfloor\right) \bmod q\)
            \(a_{i}^{(k)_{B}} \leftarrow \mathrm{~A} 2 \mathrm{~B}\left(a_{i}^{(k)_{A}}\right)\)
            Extract a 5-bit index from the most significant bits
    of \(a_{i}^{(k)_{B}}\)
            \(m_{i}^{(k)_{B}} \leftarrow\) LUT[index]
        end for
    end for
    return \(m^{(\cdot)_{B}}\)
```


## C. Alternative Prime Number for Fast Compression Function

In this session, we examined the case where the prime number used in Kyber is set to 7681, instead of 3329. If we consider the scenario with $q=3329$, we observe that it utilizes the 5 most significant bits of $\left\lfloor\frac{3329}{2}\right\rfloor$ with 4 AND, 2 NEG, and 1 XOR operations. However, when $q=7681$, the new masked compression function (See Eq. (7)) becomes 1 NEG and 1 XOR operation less expensive than the function in Eq. (6), which can be seen from the Table III.

$$
\begin{align*}
\left\lfloor\frac{7681}{2}\right\rfloor & =3840 \\
3840 & =(0 \mathbf{1 1 1 1 0 0 0 0 0 0 0 0})_{2} \\
\text { Compress }_{q}^{s}(x): & =x_{13} \oplus\left(\neg x_{13} \cdot x_{12} \cdot x_{11} \cdot x_{10} \cdot x_{9}\right) \tag{7}
\end{align*}
$$

In fact, the prime 7681 is the smallest prime that satisfies $q \equiv 1(\bmod 2 n)$ such that $n=256$. Polynomial multiplication is one of the most computationally intensive parts of lattice-based cryptographic schemes. The NTT algorithm relies on the existence of a $2 n$-th primitive root of unity in the finite field of integers modulo $q$. Hence, the prime number 7681 is not only suitable for fast NTT multiplication but also provides a fast masked compression function.

TABLE III
ANALYSIS FOR THE PRIME NUMBER 7681

| Attribute | Value |
| :--- | :--- |
| Number | 7681 |
| k | 14 |
| $\left\lfloor\frac{q}{2}\right\rfloor$ | $3840.5 \simeq 3840$ |
| Binary representation of 3840 | $(0111100000000)_{2}$ |
| Compress $_{q}^{s}(x)$ | $x_{13} \oplus\left(\neg x_{13} \cdot x_{12} \cdot x_{11} \cdot x_{10} \cdot x_{9}\right)$ |
| Number of XOR operations | 1 |
| Number of AND operations | 4 |
| Number of NEG operations | 1 |

## V. Conclusion

In conclusion, our study delves into alternative methods for ensuring the security of polynomial coefficients during compression in CRYSTALS-Kyber, focusing on safeguarding against side-channel attacks.

The presented "Double and Check" algorithm, along with the LUT-based method, provide effective means of coefficient masking, each with its distinct advantages and disadvantages. Additionally, we have proposed a fast masked compression function for the CPAPKE.Dec() algorithm using the prime number 7681, which also facilitates fast NTT multiplication.

While the Double and Check method preserves integrity and promotes computational efficiency, it does necessitate modular addition, which is more computationally intensive than a bitwise operation proposed in [10]. But more efficient algorithms exist (like Barrett reduction [19]) that can reduce this complexity. However, these reductions may present additional risk for side-channel leakage, as shown in the work [20].

In terms of Look-Up-Table integration, which utilizes modular arithmetic and pre-computed tables, these methods not only ensure enhanced data protection but also provide computational efficiency. However, they do require a trade-off between memory and computational efficiency. Even though the table is quite small, in scenarios involving hardware implementations with limited memory, this may pose a concern.

While both algorithms significantly enhance the security paradigm, it's crucial to weigh the potential trade-offs in specific use-case scenarios. As we navigate an ever-evolving landscape of cybersecurity threats, this research highlights the pressing need for continuous innovation in data protection techniques.

## Acknowledgment

The first author gratefully acknowledges the support of TÜBİTAK's "2210-A National MSc/MA Scholarship Program." We extend our heartfelt appreciation to TÜBİTAK for their invaluable contribution to academic research.

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## VI. Appendix

```
Algorithm 3 Kyber.CPAPKE.Dec \((c, s k)\) : decryption [13]
Require: Secret key \(s k \in B^{12 \cdot k \cdot n / 8}\)
Require: Ciphertext \(c \in B^{d_{u} \cdot k \cdot n / 8+d_{v} \cdot n / 8}\)
Ensure: Message \(m \in B^{32}\)
    \(\mathbf{u} \leftarrow \operatorname{Decompress}_{q}\left(\operatorname{Decode}_{d_{u}}(c), d_{u}\right)\)
    \(v \leftarrow\) Decompress \(_{q}\left(\operatorname{Decode}_{d_{v}}\left(c+d_{u} \cdot k \cdot n / 8\right), d_{v}\right)\)
    \(\hat{\mathbf{s}} \leftarrow \operatorname{Decode}_{12}(s k)\)
    \(m \leftarrow \operatorname{Encode}_{1}\left(\operatorname{Compress}_{q}\left(v-\operatorname{NTT}^{-1}\left(\hat{\mathbf{s}}^{T} \circ \operatorname{NTT}(\mathbf{u})\right), 1\right)\right) \quad \triangleright\)
    Final Result: \(m \leftarrow\) Compress \(_{q}\left(v-\mathbf{s}^{T} \mathbf{u}, 1\right)\)
    return \(m\)
```

```
Algorithm 4 Kyber.CCAKEM.Decaps \((c, s k)\) : decryption [13]
Require: Ciphertext \(c \in B^{d_{u} \cdot k \cdot n / 8+d_{v} \cdot n / 8}\)
Require: Secret key \(s k \in B^{24 \cdot k \cdot n / 8+96}\)
Ensure: Shared ephemeral key \(K \in B^{*}\)
    \(p k \leftarrow s k+12 \cdot k \cdot n / 8\)
    \(h \leftarrow s k+24 \cdot k \cdot n / 8+32 \in B^{32}\)
    \(z \leftarrow s k+24 \cdot k \cdot n / 8+64\)
    \(m^{\prime} \leftarrow\) Kyber.CPAPKE.Dec \((\mathbf{s},(\mathbf{u}, v))\)
    \(\left(\bar{K}^{\prime}, r^{\prime}\right) \leftarrow G\left(m^{\prime} \| h\right)\)
    \(c^{\prime} \leftarrow \operatorname{Kyber} . C P A P K E . E n c\left(p k, m^{\prime}, r^{\prime}\right)\)
    if \(c=c^{\prime}\) then
        \(K \leftarrow \operatorname{KDF}\left(\bar{K}^{\prime} \| H(c)\right)\)
    else
        \(K \leftarrow \operatorname{KDF}(z \| H(c))\)
    end if
    return \(K\)
```

```
Algorithm 5 Kyber.CPAPKE.Enc \((p k, m, r)\) : encryption [13]
Require: Public key \(p k \in \mathcal{B}^{12 \cdot k \cdot n / 8+32}\)
Require: Message \(m \in \mathcal{B}^{32}\)
Require: Random coins \(r \in \mathcal{B}^{32}\)
Ensure: Ciphertext \(c \in \mathcal{B}^{d_{u}} \cdot k \cdot n / 8+d_{v} \cdot n / 8\)
    \(N \leftarrow 0\)
    \(\hat{\mathbf{t}} \leftarrow \operatorname{Decode}_{12}(p k)\)
    \(\rho \leftarrow p k+12 \cdot k \cdot(n / 8)\)
    for \(i\) from 0 to \(k-1\) do \(\triangleright\) Generate the matrix \(A^{T} \in \mathbb{R}_{q}^{k \times k}\) in NTT
    domain
        for \(j=0\) to \(k-1\) do
            \(\hat{\mathbf{A}}^{T}[i][j] \leftarrow \operatorname{Parse}(\operatorname{XOF}(\rho, i, j))\)
        end for
    end for
    for \(i=0\) to \(k-1\) do
        \(\mathbf{r}[i] \leftarrow \operatorname{CBD}_{\eta_{1}}(\operatorname{PRF}(r, N)) \quad \triangleright\) Sample \(\mathbf{r} \in \mathbb{R}_{q}^{k}\) from \(B_{\eta_{1}}\)
        \(N \leftarrow N+1\)
    end for
    for \(i=0\) to \(k-1\) do
        \(\mathbf{e}_{1}[i] \leftarrow \operatorname{CBD}_{\eta_{2}}(\operatorname{PRF}(r, N)) \triangleright\) Generate the error vector \(\mathbf{e}_{1} \in \mathbb{R}_{q}^{k}\)
    from \(B_{\eta_{2}}\)
        \(N \leftarrow N+1\)
    end for
    \(e_{2} \leftarrow \operatorname{CBD}_{\eta_{2}}(\operatorname{PRF}(r, N)) \quad \triangleright\) Generate the error polynomial \(e_{2} \mathbb{R}_{q}\)
    \(\hat{\mathbf{r}} \leftarrow \operatorname{NTT}(r) \quad \triangleright\) Perform NTT
    \(\mathbf{u} \leftarrow \operatorname{NTT}^{-1}\left(\hat{\mathbf{A}}^{T} \circ \hat{\mathbf{r}}\right)+\mathbf{e}_{1} \quad \triangleright \mathbf{u}:=\mathbf{A}^{T} \cdot \mathbf{r}+\mathbf{e}_{1}\)
    \(v \leftarrow \mathrm{NTT}^{-1}\left(\hat{\mathbf{t}}^{T} \circ \hat{\mathbf{r}}\right)+e_{2}+\) Decompress \(_{q}\left(\operatorname{Decode}_{1}(m), 1\right) \quad \triangleright\)
    \(v:=\mathbf{t}^{T} \cdot \mathbf{r}+e_{2}+\operatorname{Decompress}_{q}(m, 1)\)
    \(c_{1} \leftarrow \operatorname{Encode}_{d_{u}}\left(\operatorname{Compress}_{q}\left(\mathbf{u}, d_{u}\right)\right) \quad \triangleright\) Compress \(\mathbf{u}\)
    \(c_{2} \leftarrow \operatorname{Encode}_{d_{v}}\left(\operatorname{Compress}_{q}\left(v, d_{v}\right)\right) \quad \triangleright\) Compress \(v\)
    \(c \leftarrow\left(c_{1} \| c_{2}\right)\)
    \(\triangleright\) Concatenate \(c_{1}\) and \(c_{2}\)
    return \(c \quad \triangleright c \leftarrow\left(\operatorname{Compress}_{q}\left(\mathbf{u}, d_{u}\right), \operatorname{Compress}_{q}\left(v, d_{v}\right)\right)\)
```

