Pushing the Limit of Vectorized Polynomial Multiplication for NTRU Prime

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Abstract. This paper implements a vectorized polynomial multiplication for the NTRU Prime parameter sets ntrulpr761/sntrup761 with AVX2. We explore various fast Fourier transformations (FFTs) for multiplying polynomials in $\mathbb{Z}_{4591}[x]/\langle x^{761}-x-1\rangle$. The polynomial ring $\mathbb{Z}_{4591}[x]/\langle x^{761}-x-1\rangle$ is a finite field and does *not* enjoy common beliefs on friendliness measure for implementations. Commonly, people believe that radix-2 Cooley-Tukey FFT and other FFTs with the same definibility $(2^k|(q-1))$ for the coefficient ring \mathbb{Z}_q with prime q) are fast and easy to vectorization. We show that this belief should be extended to include the following: (i) Bruun's FFT exploiting the power-of-two factor of q+1 if $q\equiv 3 \mod 4$; (ii) truncated Rader's FFT exploiting the prime factor of q-1. We qualify the prime 4591 as FFT and vectorization-friendly and find that most NTRU Prime parameter sets enjoy friendliness measures. Compared to the state-of-the-art AVX2-optimized implementation by [Bernstein, Brumley, Chen, and Tuveri, USENIX Security 2022, our big-by-big polynomial multiplication is $1.99 \times$ and $2.16 \times$ faster on Haswell and Skylake, respectively. We port our implementation to the scheme sntrup761. For the batch key generation with batch size 32, we reduce the amortized cost by 12% on Haswell and 8% on Skylake. For encapsulation, we reduce the performance cycles by 7% on Haswell and 10% on Skylake. Finally, for the decapsulation, we reduce the performance cycles by 10% on Haswell and 13% on Skylake.

Keywords: NTRU Prime · AVX2 · Good–Thomas FFT · Rader's FFT · Bruun's FFT · Truncation

1 Introduction

OpenSSH 9.0 currently uses the hybrid sntrup761x25519-sha512 key exchange by default¹. This paper explores various insights on designing transformations that are suitable for vectorization. We implement AVX2-optimized polynomial multiplication for NTRU Prime parameter sets ntrulpr761/sntrup761 as a proof of concept on identifying algebraic structures that are friendly for implementations. Our target is the polynomial multiplication in $\mathbb{Z}_{4591}[x]/\langle x^{761}-x-1\rangle$ used by ntrulpr761/sntrup761. We refer to [BBC+20] for the specification of NTRU Prime. For ntrulpr761/sntrup761, maintaining the vectorization–friendliness while working over \mathbb{Z}_{4591} was challenging. While computing the product of two polynomials, if one of the polynomials has coefficients within a small range, we call the computing task a big-by-small polynomial multiplication. Otherwise, we call it a big-by-big polynomial multiplication are big by small. Nevertheless, big-by-big polynomial multiplications are used for improving the key generation of sntrup [BY19, BBCT22] and can replace big-by-small polynomial multiplications if the performance is improved.

 $^{^1} See \ "New features" \ in \ \texttt{https://marc.info/?l=openssh-unix-dev\&m=164939371201404\&w=2}.$

In the context of FFT-based polynomial multiplication over coefficient ring \mathbb{Z}_q with prime factorization $q = \prod_h p_h^{d_h}$, one can show that a size-n cyclic discrete Fourier transform (DFT) $\mathbb{Z}_q[x]/\langle x^n-1\rangle\cong\prod_i\mathbb{Z}_q[x]/\langle x-\omega_n^i\rangle$ is definable if and only if n divides $\gcd(p_h-1)$ [Pol71, AB74]. See [Pol76, DV78, Für09] for conditions in the ring setting. For ntrulpr761/sntrup761, since $4591=2\cdot 3^3\cdot 5\cdot 17+1$ is a prime, we can only define a cyclic DFT of size n for $n|(2\cdot 3^3\cdot 5\cdot 17)$. [ACC+21] computed size-1530 and size-1620 cyclic convolutions by applying size-153 and size-270 transformations while working over \mathbb{Z}_{4591} for big-by-big polynomial multiplication on Cortex-M4. Both implementations are obviously not suitable for vectorization – the largest power-of-two factors are 2 for 1530 and 4 for 1620. They also proposed to compute size-1536 cyclic convolution for big-by-small polynomial multiplication by switching the coefficient ring to a large ring $\mathbb{Z}_{q'}$ bounding the product's maximum value in \mathbb{Z} for a prime q' with 512|(q'-1). [ACC+21] showed that big-by-big polynomial multiplication is slightly faster than big-by-small one on Cortex-M4 implementing limited SIMD support.

When moving to an architecture implementing a powerful vector instruction set, big-by-small polynomial multiplication can be vectorized in an obvious way. [BBCT22] implemented a AVX2-optimized big-by-small polynomial multiplication by operating over $\mathbb{Z}_{7681}[x]/\langle x^{1536}-1\rangle$ and $\mathbb{Z}_{10753}[x]/\langle x^{1536}-1\rangle$. However, big-by-small polynomial multiplications require the smallness of coefficients.

For big-by-big polynomial multiplications (no smallness assumption), [BBCT22, Section 3.3, Paragraph "Problem description and related multiplication."] considered three possible options: (i) In addition to the moduli 7681 and 10753, we also compute the radix-2 FFT over 12289. (ii) Applying a radix-17 algorithm with Rader's FFT as did in [ACC⁺21]. (iii) Applying radix-2 Schönhage and Nussbaumer.

For (i), this amounts to at least a factor of 1.5 blow up. The actual overhead might be larger since 12289, the smallest 16-bit prime candidate supporting a size-512 cyclic FFT, requires more modular reductions [BBCT22].

For (ii), [BBCT22] argued that it is unfriendly for radix-2 NTT. We quote the corresponding paragraphs below.

Secondly, q from the NTRU Prime parameter set is not a radix-2 NTT friendly prime. For example, q=4591 in sntrup761, and since $4591-1=2\cdot 3^3\cdot 5\cdot 17$, no simple root of unity is available for recursive radix-2 FFT tricks. Alkim, Cheng, Chung, Evkan, Huang, Hwang, Li, Niederhagen, Shih, Wälde, and Yang [ACC+21] presented a non-radix-2 NTT implementation on $(\mathbb{Z}/4591)[x]/(x^{1530}-1)$ for embedded systems. They performed radix-3, radix-5, and radix-17 NTT stages in their NTT. We instead use a radix-2 algorithm that efficiently utilizes the full ymm registers in the Haswell architecture.

[BBCT22] eventually implemented (iii) – applying radix-2 Schönhage and Nussbaumer and computing in $\mathbb{Z}_{4591}[x]/\langle (x^{1024}+1)(x^{512}-1)\rangle$. Since Schönhage and Nussbaumer double the number of coefficients and interprate the movement of coefficients as multiplications by roots of unity, [BBCT22] eventually resulted in $\frac{4\cdot1536}{8}=768$ size-8 base multiplications (small-dimensional polynomial multiplications). Their big-by-big polynomial multiplication is roughly $1.5\times$ slower than their big-by-small one.

Recently, [CCH+23] explored various vectorization ideas for NTRU and NTRU Prime on a Cortex-A72 with Neon. Among the various vectorized transformations, we are interested in their Good-Rader-Bruun. Good-Rader-Bruun multiplies polynomials in $\mathbb{Z}_{4591}[x]/\langle x^{1632}-1\rangle$, and introduces no doubling of coefficients while maintaining the vectorization-friendliness. This results in 6 size-16 base multiplications and 192 size-8 base multiplications.

Contributions. We summarize our contributions as follows.

• Let $\Phi_{17}(x^{96}) = 1 + x^{96} + \cdots x^{96 \cdot 16}$. We improve [CCH+23]'s transformation choice, and propose an AVX2 implementation multiplying polynomials in $\mathbb{Z}_{4591}[x]/\langle \Phi_{17}(x^{96})\rangle$ with truncated Rader's, Good–Thomas, and Bruun's FFT. Compared to the state-of-the-art AVX2-optimized implementation by [BBCT22], our big-by-big polynomial multiplication is $1.99 \times$ and $2.16 \times$ faster on Haswell and Skylake, respectively.

- We formalize two friendliness measures vectorization—friendliness and permutation—friendliness to rigorously argue about the architectural aspect of algebra monomorphisms with vector arithmetic.
- We provide the benchmark of the full Streamlined NTRU Prime with batch key generation and our improved polynomial multiplication. Comparing to the state-of-the-art [BBCT22], the performance cycles of batch key generation with batch size 32 are reduced by 12% on Haswell and 8% on Skylake. For the encapsulation, the performance cycles are reduced by 7% on Haswell and 10% on Skylake. As for the decapsulation, the performance cycles are reduced by 10% on Haswell and 13% on Skylake.

Code. Our source code can be found at https://github.com/vincentvbh/NTRU_Prime_polymul_AVX2 under CC0 license.

Structure of this paper. This paper is structured as follows: Section 2 describes modular arithmetic, Section 3 describes various algebraic techniques, Section 4 formalizes the need of vectorization, and Section 5 describes our choice of transformation and compares the design choice to existing vectorized polynomial multiplications for NTRU Prime. Section 6 shows the performance of our polynomial multiplication, and Section 7 drafts several possible future works, including the deployment of FFT-based constant-time GCD [BY19] and applications to other NTRU Prime parameter sets.

2 Modular Multiplication and Reduction

2.1 Definitions and Notations

Let q be a positive integer. We call two integers a, b equivalent modulo q if their difference is a multiple of q, and denote with $a \equiv b \pmod{q}$. For the set \mathbb{Z} of integers, we can partition \mathbb{Z} into q sets such that integers in the same partition are equivalent modulo q. The set formed by collecting these q sets is denoted by \mathbb{Z}_q . In each partition P, we call integers in P representatives of P. A common way for enumerating the members of \mathbb{Z}_q is to select one representative from each partition. If we define $\mathbb{Z}_q \coloneqq [0,q) \cap \mathbb{Z}$, we call the corresponding arithmetic unsigned, and define $\mathrm{mod}^+q:\mathbb{Z} \to \mathbb{Z}_q$ as the function satisfying $\forall z \in \mathbb{Z}, z \equiv z \mod^+q \pmod{q}$. On the other hand, if we define $\mathbb{Z}_q \coloneqq \left[-\frac{q}{2}, \frac{q}{2}\right] \cap \mathbb{Z}$, we call the corresponding arithmetic signed, and define $\mathrm{mod}^\pm q:\mathbb{Z} \to \mathbb{Z}_q$ as the function satisfying $\forall z \in \mathbb{Z}, z \equiv z \mod^\pm q \pmod{q}$. Additionally, we define floor function $\lfloor \rfloor : \mathbb{R} \to \mathbb{Z}$ as the function mapping a real number r to the largest integer lower-bounding r, and rounding function $\lfloor \rceil : \mathbb{R} \to \mathbb{Z}$ as $r \mapsto \lfloor r + 0.5 \rfloor$.

2.2 AVX2 Implementations

In AVX2, each vector register holds 256-bit of data. Since we are only dealing with 16-bit coefficients, each vector register holds 16 coefficients. We recall the vectorized Montgomery multiplication [Mon85] and Barrett reduction [Bar86] from [Sei18]. vpmullw multiplies corresponding signed 16-bit values and places the lower 16-bit values of the products to

the destination register. vpmulhw places the upper 16-bit values to the destination instead. **vpmulhrsw** effectively computes $\left\lfloor \frac{ab}{2^{15}} \right\rfloor$ from the signed 16-bit values a and b.

For signed 16-bit values a and b, Montgomery multiplication [Mon85, Sei18] computes a representative of $ab2^{-16} \mod {}^{\pm}q$ with

$$\left\lfloor \frac{ab}{2^{16}} \right\rfloor - \left| \frac{\left(abq' \bmod {}^{\pm} 2^{16} \right) q}{2^{16}} \right| \equiv ab2^{-16} \pmod{q}$$

where $q' = q^{-1} \mod {}^{\pm}2^{16}$. Algorithm 1 is an illustration. If b is known in prior, we replace $(b, bq' \mod^{\pm} 2^{16})$ with $(b2^{16} \mod^{\pm} q, (b2^{16} \mod^{\pm} q) q' \mod^{\pm} 2^{16})$ to save one multiplication and mitigate the scaling by 2^{-16} . Algorithm 2 is an illustration.

Barrett reduction [Bar86, Sei18] reduces a signed 16-bit value a by computing

$$a - \left\lfloor \frac{a \left\lfloor \frac{2^{15}}{q} \right\rfloor}{2^{15}} \right\rfloor q \equiv a \pmod{q}.$$

Algorithm 3 is an illustration. In the case of q = 4591, one can show (by brute-force testing) that for $a \in [-32768, 32767]$, the results lies in [-2881, 2881].

Algorithm 1 Montgomery multiplication [Sei18].

Inputs: a = a, b = b.

Constants:
$$q = 4591$$
, $q' = q^{-1} \mod^{\pm} 2^{16} = 15631$.
Output: $c = \lfloor \frac{ab}{2^{16}} \rfloor - \lfloor \frac{(abq' \mod^{\pm} 2^{16})q}{2^{16}} \rfloor \equiv ab2^{-16} \mod^{\pm} q \pmod{q}$.

1: vpmullw b, q', lo

2: vpmullw lo, a, lo

3: vpmulhw b, a, hi

4: vpmulhw lo, q, lo

5: vpsubw lo, hi,

Algorithm 2 Montgomery multiplication with precomputation [Sei18].

Input: a = a.

Constants:
$$q = 4591$$
, $b = b2^{16} \mod {}^{\pm}q$, $b' = (b2^{16} \mod {}^{\pm}q) q^{-1} \mod {}^{\pm}2^{16}$.
Output: $c = \left\lfloor \frac{a(b2^{16} \mod {}^{\pm}q)}{2^{16}} \right\rfloor - \left\lfloor \frac{(a((b2^{16} \mod {}^{\pm}q)q^{-1} \mod {}^{\pm}2^{16}))q}{2^{16}} \right\rfloor \equiv ab \mod {}^{\pm}q \pmod q$.

1: vpmullw b', a, lo

2: vpmulhw b, a, hi

3: vpmulhw lo, q, lo

4: vpsubw lo, hi,

Algorithm 3 Barrett reduction [Sei18].

Input: a = a.

Constants: q = 4591, $\bar{q} = \left\lfloor \frac{2^{15}}{q} \right\rfloor = 7$.

Output: $a = a - \lfloor \frac{a\bar{q}}{2^{15}} \rfloor q, -2881 \le a - \lfloor \frac{a\bar{q}}{2^{15}} \rfloor q \le 2881.$

1: vpmulhrsw a, $ar{q}$, hi

2: vpmullw hi, q, hi

3: vpsubw hi, a,

3 Transformations

This section reviews various algebraic techniques, including Chinese remainder theorem in Section 3.2, truncation in Section 3.3, Cooley–Tukey FFT in Section 3.4, Good–Thomas FFT in Section 3.5, Rader's FFT in Section 3.6, Bruun's FFT in Section 3.7, twisting and composed multiplication in Section 3.8, and Karatsuba in Section 3.9.

3.1 Definitions and Notations

In this paper, we assume all readers are familiar with some basic algebraic structures, including, monoids, groups, rings, modulues, and associative algebras. We go through an informal introduction of them in this section.

Monoids, groups, rings. A monoid M is a set equipped with a binary associative operator \cdot_M admitting an identity element 1_M . If \cdot_M is commutative, we write it $+_M$, denote 0_M for the identity element, and call M a commutative monoid. If each elements in M admits an inverse element, we call M a group. Let G be a group. If there is an element $g \in G$ such that $G = \{g^i\}$, we call G a cyclic group. Furthermore, if the binary associative operator is commutative, we call G an abelian group. Obviously, cyclic groups are abelian. For an abelian group G, if we additionally find a binary associative operator \cdot_G with 1_G satisfying the left and right distributivity over the commutative operator $+_{G}$, we call $(G, \cdot_G, 1_G)$ a ring and G its underlying additive group. For a ring R, if \cdot_R is commutative, we call R a commutative ring. In this paper, all rings are commutative. For a subgroup Iof the underlying additive group of a ring R, we call I an ideal if $\forall r \in R, \forall a \in I, ra \in I$. If $I=rR\coloneqq\{ra|a\in R\}$ for an $r\in R$, we denote $I=\langle r\rangle$. For an ideal I of a ring R, the set $R/I := \{\{r+a|a\in I\} | r\in R\}$ is a ring called quotient ring. A straightforward example of rings is \mathbb{Z} with the usual addition and multiplication as the binary associative operators. For a positive integer q, the set of q-multiples $q\mathbb{Z} = \{qz | z \in \mathbb{Z}\}$ is an ideal. We denote the quotient ring $\mathbb{Z}/q\mathbb{Z}$ as \mathbb{Z}_q in this paper. Notice that \mathbb{Z}_q is in fact a cyclic group of qelements. Since cyclic groups containing the same number of elements are isomorphic in an obvious way, \mathbb{Z}_q is also used for denoting a cyclic group of q elements.

Modules. For an abelian group M, a ring R, and a map $\cdot_{R\times M}: R\times M\to M$, we call $(M,\cdot_{R\times M})$ a left R-module if $\forall r,s\in R, \forall a,b\in M$, (i) $r\cdot_{R\times M}(a+_Mb)=r\cdot_{R\times M}a+_Mr\cdot_{R\times M}b$ (distributivity over addition in M), (ii) $(r+_Rs)\cdot_{R\times M}a=r\cdot_{R\times M}a+_Mr\cdot_{R\times M}b$ (distributivity over addition in R), (iii) $(r\cdot_Rs)\cdot_{R\times M}a=r\cdot_{R\times M}(s\cdot_{R\times M}a)$ (associativity of \cdot_R and $\cdot_{R\times M}$), and (iv) $1_R\cdot_{R\times M}a=a$ (compatibility of the multiplicative identity 1_R). For a ring R, an immediate example of an R-module is the set R^n : for an $r\in R$ and an n-tuple $(a_i)\in R^n$, we define $r(a_i)$ as (ra_i) .

Associative algebras. For an R-module M, if we adjoin a ring structure to M by introducing a binary associative operator with an identity compatible with 1_R to the underlying additive group M, we call M an associative R-algebra. For simplicity, we call an associative R-algebra an R-algebra or an algebra when the context is clear. For a degree-n polynomial $\mathbf{g} \in R[x]$, the quotient ring $R[x]/\langle \mathbf{g} \rangle$ is an R-algebra: (i) $R[x]/\langle \mathbf{g} \rangle = R^n$ as R-modules, and (ii) $R[x]/\langle \mathbf{g} \rangle$ is a ring. We give more examples that are relevant to this paper. For a group G of n elements, we define R[G] as the set of elements of the form

$$\sum_{i} r_i g_i$$

for $r_i \in R$ and $g_i \in G$. Obviously, we have $R[G] = R^n$ as R-modules. It remains to verify the ring structure of R[G]. We turn R[G] into a ring by defining

$$\left(\sum_i a_i g_i\right) \left(\sum_i b_i g_i\right) \coloneqq \sum_i \left(\sum_{g_j g_k = g_i} a_j b_k\right) g_i.$$

Immediately, if $G = \mathbb{Z}_n$ is a cyclic group of n elements, we have $R[\mathbb{Z}_n] \cong R[x]/\langle x^n - 1 \rangle$ as R-algebras. In this paper, $R[\mathbb{Z}_n]$ and $R[x]/\langle x^n - 1 \rangle$ are used interchangeably. For two R-algebras \mathcal{A}, \mathcal{B} , we can form an R-algebra $\mathcal{A} \otimes \mathcal{B}$ called tensor product of \mathcal{A} and \mathcal{B} . We refer to [Bou89, Section 3, Chapter II] and [Jac12, Section 3.9] for definition. If we have a group isomorphism $G \cong G_0 \times G_1$, then $R[G] \cong R[G_0] \otimes R[G_1]$ as R-algebras and isomorphisms defined on R[G] might be much faster in terms of computational complexity if we first move to $R[G_0] \otimes R[G_1]$. For example, for coprime positive integers q_0, q_1 , the group isomorphism $\mathbb{Z}_{q_0q_1} \cong \mathbb{Z}_{q_0} \times \mathbb{Z}_{q_1}$ implies the algebra isomorphism $R[\mathbb{Z}_{q_0q_1}] \cong R[\mathbb{Z}_{q_0}] \otimes R[\mathbb{Z}_{q_1}]$, and for an arbitrary isomorphism defined on $R[\mathbb{Z}_{q_0}] \otimes R[\mathbb{Z}_{q_1}]$, there is a corresponding tensor product of isomorphisms defined on $R[\mathbb{Z}_{q_0}] \otimes R[\mathbb{Z}_{q_1}]$ by tensor-hom adjunction [Jac12, Exercise 3, Section 3.8]. Such a tensor product of isomorphisms usually admits faster computation. We will give more details in Section 3.5.

3.2 Chinese Remainder Theorem

Let R be a ring. For elements $e_0, e_1 \in R$, we call them orthogonal if $e_0e_1 = 0$. An element $e \in R$ is called idempotent if $e^2 = e$. For orthogonal idempotent elements e_0 and e_1 in R satisfying $e_0 + e_1 = 1$, we have the ring isomorphism $R \cong R/(1-e_0)R \times R/(1-e_1)R$. This easily generalizes to finitely many orthogonal idempotent elements (e_0, \ldots, e_{d-1}) with $\sum_i e_i = 1$ realizing $R \cong \prod_i R/(1-e_i)R$. Explicitly, we have the isomorphism $R \to \prod_i R/(1-e_i)R$ mapping a to the n-tuple $(a \mod (1-e_i)R)$ with the inverse $(\hat{a}_i) \mapsto \sum_i \hat{a}_i e_i$ [Bou89]. This is the idempotent-element-based approach to the Chinese remainder theorem for rings.

We are interested in two cases: $R[x] / \langle \prod_{i_0, \dots, i_{h-1}} \mathbf{g}_{i_0, \dots, i_{h-1}} \rangle$ for coprime polynomials $\mathbf{g}_{i_0, \dots, i_{h-1}}$'s in R[x] and $\mathbb{Z}_{q_0 \dots q_{d-1}}$ for coprime integers q_0, \dots, q_{d-1} . For the former, since $\mathbf{g}_{i_0, \dots, i_{h-1}}$'s are coprime, we can split in a "layer-by-layer" fashion by moving indices i_j 's from the ideal part to the product-ring part as follows:

$$\frac{R[x]}{\left\langle \prod_{i_0,\dots,i_{h-1}} \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle} \cong \prod_{i_0} \frac{R[x]}{\left\langle \prod_{i_1,\dots,i_{h-1}} \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle} \cong \dots \cong \prod_{i_0,\dots,i_{h-1}} \frac{R[x]}{\left\langle \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle}.$$

If all the isomorphisms are fast in terms of computational complexity, the overall isomorphism $R[x] / \left\langle \prod_{i_0, \dots, i_{h-1}} \boldsymbol{g}_{i_0, \dots, i_{h-1}} \right\rangle \cong \prod_{i_0, \dots, i_{h-1}} R[x] / \left\langle \boldsymbol{g}_{i_0, \dots, i_{h-1}} \right\rangle$ will be fast. For the later case $\mathbb{Z}_{q_0 \dots q_{d-1}} \cong \prod_i \mathbb{Z}_{q_i}$, a similar complexity argument holds via tensor product. See Section 3.5 for details.

3.3 Truncation

Let \mathcal{I} be a finite index set. Suppose we have an isomorphism $\eta_{\mathcal{I}}: R[x]/\langle \prod_{i\in\mathcal{I}} \boldsymbol{g}_i \rangle \to \prod_{i\in\mathcal{I}} R[x]/\langle \boldsymbol{g}_i \rangle$ for coprime ideals $(\boldsymbol{g}_i)_{i\in\mathcal{I}}$. Truncation [CF94, vdH04, Ber08] is a systematic approach for computing a product of size $n < \deg(\prod_{i\in\mathcal{I}} \boldsymbol{g}_i)$ while requiring the defining conditions of $\eta_{\mathcal{I}}$.

For an index set $\mathcal{J} \subset \mathcal{I}$ with $n+1 = \deg\left(\prod_{j \in \mathcal{J}} \boldsymbol{g}_j\right)$, we have the isomorphism $\eta_{\mathcal{J}} : R[x] / \left\langle \prod_{j \in \mathcal{J}} \boldsymbol{g}_j \right\rangle \to \prod_{j \in \mathcal{J}} R[x] / \left\langle \boldsymbol{g}_j \right\rangle$ for computing a size-n product. Obviously, $\eta_{\mathcal{J}}$ is defined whenever $\eta_{\mathcal{I}}$ is defined, and any algorithm implementing $\eta_{\mathcal{I}}$ can be converted into

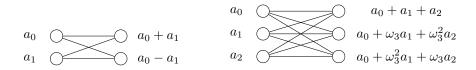
an algorithm implementing $\eta_{\mathcal{J}}$. In the simplest case $\prod_{i\in\mathcal{I}} g_i = x^{2^k} - 1$, the computational complexity of $\eta_{\mathcal{J}}$ is roughly $\frac{n}{2^k}$ of $\eta_{\mathcal{I}}$.

3.4 Cooley-Tukey FFT

Let $n = \prod_j n_j$, and i_j run over $0, \dots, n_j - 1$ for each j. The Cooley-Tukey FFT [CT65] computes with the following isomorphisms:

$$\frac{R[x]}{\left\langle \prod_{i_0,\dots,i_{h-1}} \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle} \cong \prod_{i_0} \frac{R[x]}{\left\langle \prod_{i_1,\dots,i_{h-1}} \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle} \cong \dots \cong \prod_{i_0,\dots,i_{h-1}} \frac{R[x]}{\left\langle \boldsymbol{g}_{i_0,\dots,i_{h-1}} \right\rangle}$$

by choosing $\mathbf{g}_{i_0,\dots,i_{h-1}} = x - \zeta \omega_n^{\sum_l i_l} \prod_{j < l} n_j$ where ω_n is a principal n-th root of unity². The Cooley–Tukey FFT is invertible if ζ and n are invertible in R. Since $\prod_{i_0,\dots,i_{h-1}} \mathbf{g}_{i_0,\dots,i_{h-1}} = x^n - \zeta^n$, we can multiply polynomials in $R[x]/\langle x^n - \zeta^n \rangle$ via $\prod_{i_0,\dots,i_{h-1}} R[x]/\langle \mathbf{g}_{i_0,\dots,i_{h-1}} \rangle$. See Figure 1a for the radix-2 cyclic case $R[x]/\langle x^2 - 1 \rangle$ and Figure 1b for the radix-3 cyclic case $R[x]/\langle x^3 - 1 \rangle$.



(a) Radix-2 CT butterfly for $R[x]/\langle x^2-1\rangle$. (b) Radix-3 CT butterfly for $R[x]/\langle x^3-1\rangle$.

Figure 1: Radix-2 and Radix-3 Cooley–Tukey butterflies (CT butterflies).

3.5 Good-Thomas FFT

Let $n = \prod_j q_j$ for coprime integers q_0, \ldots, q_{d-1} . There are two ways for stating Good–Thomas FFT [Goo58]: (i) as an isomorphism from a group algebra to a tensor product of associative algebras; and (ii) as a correspondence between one-dimensional FFT and multi-dimensional FFT. (ii) was stated in [Goo58], and (i) is a more general statement in the modern algebra language and is apparent from [Goo58].

Recall that we have a group isomorphism $\mathbb{Z}_n \cong \prod_j \mathbb{Z}_{q_j}$. This implies an isomorphism between the group algebras $R[\mathbb{Z}_n]$ and $\bigotimes_j R[\mathbb{Z}_{q_j}]$. Suppose n is invertible in R, and there is a principal n-th root of unity $\omega_n \in R$ realizing the isomorphism $\eta: R[x]/\langle x^n-1\rangle \cong \prod_i R[x]/\langle x-\omega_n^i\rangle$. By definition, we also have a principal n_j -th root of unity ω_{n_j} for each j. Let e_j be the unique tuple of orthogonal idempotents implementing $\forall a \in \mathbb{Z}_n, a \equiv \sum_j (a \bmod q_j)e_j \pmod n$. We choose $\omega_{n_j} \coloneqq \omega_n^{e_j}$ so $\prod_j \omega_{n_j} = \omega_n^{\sum_j e_j} = \omega_n$. We define η_j as the isomorphism $R[x_j]/\langle x_j^{n_j}-1\rangle \cong \prod_{i_j} R[x_j]/\langle x_j-\omega_{n_j}^{i_j}\rangle$ for each j, and relate the tensor product $\bigotimes_j \eta_j$ to η via the equivalence $x \sim \prod_j x_j$. Figure 2 is an illustration. This is how Good–Thomas FFT converts the computation η into $\bigotimes_j \eta_j$.

 $^{{}^{2}\}forall j=1,\ldots,n-1,\sum_{i=0}^{n-1}\omega_{n}^{ij}=0.$

$$\frac{R[x]}{\langle x^{n}-1\rangle} \xrightarrow{x \mapsto \prod_{j} x_{j}} \bigotimes_{j} \frac{R[x_{j}]}{\langle x_{j}^{n_{j}}-1\rangle} \\
\downarrow \qquad \qquad \qquad \downarrow \bigotimes_{j} \eta_{j} \\
\prod_{i} \frac{R[x]}{\langle x-\omega_{n}^{i}\rangle} \longleftarrow \prod_{j} \omega_{n_{j}} \mapsto \omega_{n} \qquad \bigotimes_{j} \prod_{i_{j}} \frac{R[x_{j}]}{\langle x_{j}-\omega_{n_{j}}^{i_{j}}\rangle}$$

Figure 2: Commutative diagram of Good-Thomas FFT.

We give a small example for n=6. By defining $\omega_3 \coloneqq \omega_6^4$ and $\omega_2 \coloneqq \omega_6^3$, we rewrite the transformation matrix implementing $R[x]/\langle x^6-1\rangle \cong \prod_i R[x]/\langle x-\omega_6^i\rangle$ as:

$$P_{(14)}\begin{pmatrix} 1 & 1 & 1 & 1 & 1 & 1 \\ 1 & \omega_{6} & \omega_{6}^{2} & \omega_{6}^{3} & \omega_{6}^{4} & \omega_{6}^{5} \\ 1 & \omega_{6}^{2} & \omega_{6}^{4} & 1 & \omega_{6}^{2} & \omega_{6}^{4} \\ 1 & \omega_{6}^{3} & 1 & \omega_{6}^{3} & 1 & \omega_{6}^{3} \\ 1 & \omega_{6}^{4} & \omega_{6}^{2} & 1 & \omega_{6}^{4} & \omega_{6}^{2} \\ 1 & \omega_{6}^{5} & \omega_{6}^{4} & \omega_{6}^{3} & \omega_{6}^{2} & \omega_{6} \end{pmatrix} P_{(14)}$$

$$= \begin{pmatrix} 1 & 1 & 1 & 1 & 1 & 1 \\ 1 & \omega_{6}^{4} & \omega_{6}^{2} & 1 & \omega_{6}^{4} & \omega_{6}^{2} \\ 1 & \omega_{6}^{2} & \omega_{6}^{4} & 1 & \omega_{6}^{2} & \omega_{6}^{4} \\ 1 & 1 & 1 & \omega_{6}^{3} & \omega_{6}^{3} & \omega_{6}^{3} \\ 1 & \omega_{6}^{4} & \omega_{6}^{2} & \omega_{6}^{3} & \omega_{6} & \omega_{6}^{5} \\ 1 & \omega_{6}^{2} & \omega_{6}^{4} & \omega_{6}^{3} & \omega_{6}^{3} & \omega_{6}^{3} \\ 1 & \omega_{6}^{2} & \omega_{6}^{4} & \omega_{6}^{3} & \omega_{6}^{3} & \omega_{6}^{5} \\ 1 & \omega_{6}^{2} & \omega_{6}^{4} & \omega_{6}^{3} & \omega_{6}^{5} & \omega_{6}^{5} \end{pmatrix} = \begin{pmatrix} 1 & 1 \\ 1 & \omega_{2} \end{pmatrix} \otimes \begin{pmatrix} 1 & 1 & 1 \\ 1 & \omega_{3} & \omega_{3}^{2} \\ 1 & \omega_{3}^{2} & \omega_{3} \end{pmatrix}$$

for $P_{(14)}$ the permutation matrix swapping the 1st and 4th elements.

3.6 Rader's FFT

Let p be an odd prime, and $\mathcal{I}=\{0,\dots,p-1\}$, $\mathcal{I}^*=\{1,\dots,p-1\}$ be index sets. Rader's FFT [Rad68] computes the map $R[x]/\langle x^p-1\rangle\cong\prod_i R[x]/\langle x-\omega_p^i\rangle$ with a size- $\lambda(p)$ cyclic convolution where λ is the Carmichael's lambda function. See [Win78] for the odd-prime-power case.

Since p is a prime, there is a $g \in \mathcal{I}$ with $\mathcal{I}^* = \{1, g, \dots, g^{\lambda(p)-1}\}$. We define $\log_g : \mathcal{I}^* \to \mathbb{Z}_{\lambda(p)}$ as the discrete logarithm. This allows us to introduce the following reindexing for $(\hat{a}_j)_{j \in \mathcal{I}} = \left(\sum_{i \in \mathcal{I}} a_i \omega_p^{ij}\right)_{j \in \mathcal{I}}$: (i) $i \in \mathcal{I}^* \mapsto -\log_g i \in \mathbb{Z}_{\lambda(n)}$ and (ii) $j \in \mathcal{I}^* \mapsto \log_g j \in \mathbb{Z}_{\lambda(n)}$. For $j \in \mathcal{I}^*$, this gives us

$$\hat{a}_{g^{\log_g j}} - a_0 = \sum_{i \in \mathcal{I}^*} a_i \omega_p^{ij} = \sum_{-\log_g i \in \mathbb{Z}_{\lambda(p)}} a_{g^{\log_g i}} \omega_p^{g^{\log_g i + \log_g j}}.$$

Therefore, we find that $(\hat{a}_{g^k} - a_0)_{k \in \mathbb{Z}_{\lambda(p)}}$ is the convolution of $(a_{g^{-k}})_{k \in \mathbb{Z}_{\lambda(p)}}$ and $(\omega_p^{g^k})_{k \in \mathbb{Z}_{\lambda(p)}}$. We give an example for p = 5 and g = 2:

$$(\hat{a}_{2^k} - a_0)_{k \in \mathbb{Z}_4} = \begin{pmatrix} a_1 \omega_5^1 + a_2 \omega_5^2 + a_3 \omega_5^3 + a_4 \omega_5^4 \\ a_1 \omega_5^2 + a_2 \omega_5^4 + a_3 \omega_5^1 + a_4 \omega_5^3 \\ a_1 \omega_5^4 + a_2 \omega_5^3 + a_3 \omega_5^2 + a_4 \omega_5^1 \\ a_1 \omega_5^3 + a_2 \omega_5^1 + a_3 \omega_5^4 + a_4 \omega_5^2 \end{pmatrix} = \begin{pmatrix} a_{2^0} \omega_5^{2^0} + a_{2^3} \omega_5^{2^3} + a_{2^2} \omega_5^{2^2} + a_{2^1} \omega_5^{2^1} \\ a_{2^0} \omega_5^{2^1} + a_{2^3} \omega_5^{2^0} + a_{2^2} \omega_5^{2^3} + a_{2^1} \omega_5^{2^2} \\ a_{2^0} \omega_5^{2^2} + a_{2^3} \omega_5^{2^1} + a_{2^2} \omega_5^{2^0} + a_{2^1} \omega_5^{2^3} \\ a_{2^0} \omega_5^{2^3} + a_{2^3} \omega_5^{2^2} + a_{2^2} \omega_5^{2^0} + a_{2^1} \omega_5^{2^0} \end{pmatrix}.$$

Obviously, for the inversion, we apply Rader's FFT with inverted roots.

3.6.1 Truncated Rader's FFT and its Inverse

Let Φ_p be the *p*-th cyclotomic polynomial. Since *p* is a prime, we have $\Phi_p(x) = \sum_{i=0,\dots,p-1} x^i$ and $\Phi_p(x)|(x^p-1)$. A natural question is how to truncate Rader's FFT defined over x^p-1 to $\Phi_p(x)$. We first find the following isomorphism

$$\eta: \begin{cases} R[x]/\langle \Phi_p(x) \rangle & \cong \prod_{j \in \mathcal{I}^*} R[x]/\langle x - \omega_p^j \rangle \\ \sum_{i \in \mathcal{I}^*} a_{i-1} x^{i-1} & \mapsto \left(\hat{a}_j = \sum_{i \in \mathcal{I}^*} a_{i-1} \omega_p^{(i-1)j} \right)_{j \in \mathcal{I}^*} \end{cases}$$

by truncation. With the same reindexing $i \mapsto -\log_g i$ and $j \mapsto \log_g j$, we have

$$\hat{a}_{g^{\log_g j}} = \sum_{i \in \mathcal{I}^*} a_{i-1} \omega_p^{(i-1)j} = \omega_p^{-j} \sum_{i \in \mathcal{I}^*} a_{i-1} \omega_p^{ij} = \omega_p^{-j} \sum_{-\log_g i \in \mathbb{Z}_{\lambda(p)}} a_{g^{\log_g i} - 1} \omega_p^{g^{\log_g i + \log_g j}}$$

and find that $(\omega_p^k \hat{a}_{g^k})_{k \in \mathbb{Z}_{\lambda(p)}}$ is the convolution of $(a_{g^{-k}-1})_{k \in \mathbb{Z}_{\lambda(p)}}$ and $(\omega_p^{g^k})_{k \in \mathbb{Z}_{\lambda(p)}}$. Below is an illustration for p=5 and g=2:

$$\left(\omega_p^k \hat{a}_{2^k}\right)_{k \in \mathbb{Z}_4} = \begin{pmatrix} a_0 \omega_5^1 + a_1 \omega_5^2 + a_2 \omega_5^3 + a_3 \omega_5^4 \\ a_0 \omega_5^2 + a_1 \omega_5^4 + a_2 \omega_5^1 + a_3 \omega_5^3 \\ a_0 \omega_5^4 + a_1 \omega_5^3 + a_2 \omega_5^2 + a_3 \omega_5^1 \\ a_0 \omega_5^3 + a_1 \omega_5^1 + a_2 \omega_5^4 + a_3 \omega_5^2 \end{pmatrix} = \begin{pmatrix} a_0 \omega_5^{2^0} + a_1 \omega_5^{2^1} + a_2 \omega_5^{2^3} + a_3 \omega_5^{2^2} \\ a_0 \omega_5^{2^1} + a_1 \omega_5^{2^2} + a_2 \omega_5^{2^0} + a_3 \omega_5^2 \\ a_0 \omega_5^2 + a_1 \omega_5^{2^3} + a_2 \omega_5^{2^1} + a_3 \omega_5^2 \\ a_0 \omega_5^{2^2} + a_1 \omega_5^{2^3} + a_2 \omega_5^{2^1} + a_3 \omega_5^2 \end{pmatrix} = \begin{pmatrix} a_{2^0 - 1} \omega_5^{2^0} + a_{2^3 - 1} \omega_5^{2^0} + a_{2^$$

For the inverse η^{-1} , [Ber22, Section 4.8.2] showed how to implement it with a size- $\lambda(p)$ cyclic convolution. Since convoluting with $\left(\omega_p^{g^k}\right)_{k\in\mathbb{Z}_{\lambda(p)}}$ is exactly the same as multiplying by $\left(\omega_p^{g^k}\right)_{k\in\mathbb{Z}_{\lambda(p)}}$ in the group algebra $R[\mathbb{Z}_{\lambda(p)}]$ by definition, it suffices to identify the multiplicative inverse of $\left(\omega_p^{g^k}\right)_{k\in\mathbb{Z}_{\lambda(p)}}$ in $R[\mathbb{Z}_{\lambda(p)}]$. [Ber22] found $\left(\omega_p^{g^k}\right)_{k\in\mathbb{Z}_{\lambda(p)}}^{-1} = \frac{1}{p}\left(\omega_p^{-g^{-k}}-1\right)_{k\in\mathbb{Z}_{\lambda(p)}}$. We illustrate below for p=5 and g=2:

$$\begin{pmatrix} \omega_5^{2^0} & \omega_5^{2^3} & \omega_5^{2^2} & \omega_5^{2^1} \\ \omega_5^{2^1} & \omega_5^{2^0} & \omega_5^{2^3} & \omega_5^{2^3} \\ \omega_5^{2^2} & \omega_5^{2^1} & \omega_5^{2^0} & \omega_5^{2^3} \\ \omega_5^{2^3} & \omega_5^{2^1} & \omega_5^{2^0} & \omega_5^{2^3} \\ \omega_5^{2^3} & \omega_5^{2^2} & \omega_5^{2^1} & \omega_5^{2^0} \end{pmatrix} \begin{pmatrix} \omega_5^{-2^{-1}} - 1 \\ \omega_5^{-2^{-1}} - 1 \\ \omega_5^{-2^{-2}} - 1 \\ \omega_5^{-2^{-3}} - 1 \end{pmatrix}$$

$$= \begin{pmatrix} \omega_5^1 & \omega_5^3 & \omega_5^4 & \omega_5^2 \\ \omega_5^2 & \omega_5^1 & \omega_5^3 & \omega_5^4 \\ \omega_5^2 & \omega_5^1 & \omega_5^3 & \omega_5^4 \\ \omega_5^3 & \omega_5^4 & \omega_5^2 & \omega_5^1 \end{pmatrix} \begin{pmatrix} \omega_5^{-1} - 1 \\ \omega_5^{-3} - 1 \\ \omega_5^{-4} - 1 \\ \omega_5^{-2} - 1 \end{pmatrix} = \begin{pmatrix} 5 \\ 0 \\ 0 \\ 0 \end{pmatrix}.$$

In summary, we can implement η^{-1} by mapping $(\hat{a}_{g^k})_{k \in \mathbb{Z}_{\lambda(p)}}$ to $(\omega_p^k \hat{a}_{g^k})_{k \in \mathbb{Z}_{\lambda(p)}}$ and convoluting with $(\omega_p^{-g^{-k}} - 1)_{k \in \mathbb{Z}_{\lambda(p)}}$. Scaling by $\frac{1}{p}$ is postponed to the end. See [Ber22, Sections 4.12.3 and 4.12.4] for the generalization to arbitrary p.

3.7 Bruun's FFT

Let q be a prime with $q \equiv 3 \mod 4$ and $q+1=r2^w$ for an odd r. Bruun's FFT allows us to split $\mathbb{Z}_q[x]/\langle x^{2^w}+1\rangle$ as follows:

$$\frac{\mathbb{Z}_q[x]}{\langle x^{2^w} + 1 \rangle} \cong \prod_i \frac{\mathbb{Z}_q[x]}{\langle x^2 \pm \alpha_i x - 1 \rangle}.$$

See [BGM93] for a proof. The benefit is that the number of coefficients is the same after the transformation. Earlier work [BBCT22] with Nussbaumer for the same scenario resulted in $2\times$ many coefficients after transforming. For q=4591, we can split $\mathbb{Z}_q[x]/\langle x^{16}+1\rangle$ into size-2 polynomial rings with moduli of the form $x^2\pm\alpha_ix-1$ since $4591+1=287\cdot 2^4$. In this paper, we are interested in the case $\mathbb{Z}_q[x]/\langle x^{16}+1\rangle\cong\prod\mathbb{Z}_q[x]/\langle x^8\pm\sqrt{2}x^4+1\rangle$. For simplicity, we illustrate with the case $\mathbb{Z}_q[x]/\langle x^4+1\rangle\cong\prod\mathbb{Z}_q[x]/\langle x^2\pm\sqrt{2}x+1\rangle$. We compute $(a_0-a_2,a_1+a_3,\sqrt{2}a_2,\sqrt{2}a_3)$, swap the last two values implicitly, and apply add-sub pairs [CCH⁺23]. See Figure 3 for illustration.



Figure 3: Bruun's butterfly computing $\hat{a}_0 + \hat{a}_1 x = \boldsymbol{a}(x) \mod (x^2 + \sqrt{2}x + 1)$, $\hat{a}_2 + \hat{a}_3 x = \boldsymbol{a}(x) \mod (x^2 - \sqrt{2}x + 1)$ for $\boldsymbol{a}(x) = \sum_{i=0,\dots,3} a_i x^i$ [CCH⁺23].

Bruun's FFT was originally proposed with \mathbb{C} as the coefficient ring. See [Bru78] for the power-of-two case and [Mur96] for the even case. The finite field case is closely related to [BGM93, Mey96, TW13, BMGVdO15, WYF18, WY21].

3.8 Twisting

Let R be a ring, $\zeta \in R$ be an invertible element, n be an integer, and $\xi \in R$ be an element. We have the isomorphism $R[x]/\langle x^n - \xi \zeta^n \rangle \cong R[y]/\langle y^n - \xi \rangle$ by sending x to ζy . This is called twisting. In the literature, twising is commonly specialized to $\xi = 1$. In this paper, we need the cases $\xi = \pm 1$.

3.9 Karatsuba

Karatsuba [KO62] computes the product $(a_0 + a_1x)(b_0 + b_1x)$ by evaluating at the point set $\{0, 1, \infty\}$. We compute $(a_0 + a_1x)(b_0 + b_1x) = a_0b_0 + (a_0b_1 + a_1b_0)x + a_1b_1x^2$ with three multiplications a_0b_0 , a_1b_1 , and $(a_0 + a_1)(b_0 + b_1)$ by observing $a_0b_1 + a_1b_0 = (a_0 + a_1)(b_0 + b_1) - a_0b_0 - a_1b_1$.

4 The Need of Vectorization

Let v be the number of elements contained in a vector register. In this section, we formalize vectorization and permutation–friendliness of an algebra monomorphism. Since algebra monomorphisms can be characterized as matrix multiplications, our formulation is based on manipulations of matrices with standard bases unless specified otherwise. Recent work SPIRAL [FLP+18] had attempted to formalize the vectorization of FFTs for code generation. However, it seems to fall short to cover the transformations considered in this paper. We believe this section will give more insights on extending SPIRAL.

4.1 Vectorization-Friendliness

We first identify a set SD of matrices that can be implemented efficiently with vector instructions. Although SD is definitely platform-dependent, we fix SD to be a union of certain matrices and explain why they are usually suitable for vectorization. We define SD as a set of all possible block diagonal matrices with each block a $v' \times v'$ matrix of the following form for a v-multiple v':

- 1. Diagonal matrix: a matrix with non-diagonal entries all zeros.
- 2. Cyclic/negacyclic shift matrix: a matrix implementing $(a_i)_{0 \le i < v'} \mapsto (a_{(i+c) \mod v'})_{0 \le i < v'}$ (cyclic) or $(a_i)_{0 \le i < v'} \mapsto ((-1)^{\llbracket i + c \ge v' \rrbracket} a_{(i+c) \mod v'})_{0 \le i < v'}$ (negacyclic) for a nonnegative integer c.

Diagonal matrices are suitable for vectorization since we can load v coefficients, multiply them by v constants, and store them back to memory with vector instructions. For cyclic/negacyclic shift matrices, we discuss how to implement them for the following vector instruction sets:

- Armv7/8-A Neon: For cyclic shifts, we use the instruction ext extracting consecutive elements from a pair of vector registers. We negate one of the registers before applying ext for negacyclic shifts [CCH⁺23].
- AVX2: For cyclic shifts, we perform unaligned loads, shuffle the last vector register, and store the vectors to memory. Again, the last vector register is negated for negacyclic shifts [BBCT22].

Let f be an algebra monomorphism, and M_f be the matrix form of f. We call f vectorization—friendly if

$$M_f = \prod_i \left(M_{f_i} \otimes I_v \right) S_{f_i}$$

for some M_{f_i} and $S_{f_i} \in S$. We first observe that vector instruction sets usually provide instructions loading/storing consecutive v coefficients from/to memory. The tensor product $M_{f_i} \otimes I_v$ ensures that each v-chunk is regarded as a whole while applying $M_{f_i} \otimes I_v$. Additionally, f is vectorization–friendly if and only if f^{-1} is vectorization–friendly.

4.2 Permutation–Friendliness

We introduce the notion "permutation–friendliness". Conceptually, permutation–friendliness stands for vectorization–friendliness after applying a special type of permutation – interleaving. Again, let v' be a multiple of v. We define the transposition matrix $T_{v'^2}$ as the $v'^2 \times v'^2$ matrix permuting the elements as if transposing a $v' \times v'$ matrix. We illustrate the case v'=2 with Algorithm 4 for Neon and Algorithm 5 for AVX2. Now we are ready to specify the set SI of interleaving matrices. We call a matrix M interleaving matrix with step v' if it takes the form

$$M = (\pi' \otimes I_{v'}) (I_m \otimes T_{v'^2}) (\pi \otimes I_{v'})$$

for a positive integer m and permutation matrices π, π' permuting mv' elements. The set SI consists of interleaving matrices of all possible steps and is closed under inversion.

We now define permutation–friendliness formally as follows. We call an algebra monomorphism g permutation–friendly if we can factor its matrix form M_g as

$$M_q = M'_{SI} M_f M_{SI}$$

for a vectorization–friendly M_f and $M_{SI}, M'_{SI} \in SI$ an interleaving matrix. Immediately, we know that g is permutation–friendly if and only if g^{-1} is permutation–friendly.

Algorithm 4 trn{1, 2} permuting double words in Armv8.0-A Neon registers.

Inputs: $(v0, v1) = (a_0 \mid \mid a_1, b_0 \mid \mid b_1)$ Outputs: $(v2, v3) = (a_0 \mid \mid b_0, a_1 \mid \mid b_1)$

1: trn1 v2.2D, v0.2D, v1.2D 2: trn2 v3.2D, v0.2D, v1.2D

Algorithm 5 vperm2i128 permuting double words in AVX2 %ymm registers.

Inputs: (%ymm0, %ymm1) = $(a_0 \mid \mid a_1, b_0 \mid \mid b_1)$ Outputs: (%ymm2, %ymm3) = $(a_0 \mid \mid b_0, a_1 \mid \mid b_1)$ 1: vperm2i128 %ymm2, %ymm0, %ymm1, 0x20 2: vperm2i128 %ymm3, %ymm0, %ymm1, 0x31

Generally, while computing with vector instructions, we choose algebra monomorphisms f and g such that f is vectorization–friendly and g is permutation–friendly. Their composition $g \circ f$ then admits a suitable mapping to our target vector instruction set. Concretely, we vectorize f, transpose the coefficients, and vectorize the vectorization–friendly part of g.

5 Implementation

This section goes through the implementation with truncated Rader's FFT, Good–Thomas FFT, and Bruun's FFT. For simplicity, we assume $R = \mathbb{F}_{4591}$ and multiply polynomials in

$$\frac{R[x]}{\langle \Phi_{17}(x^{96})\rangle}.$$

5.1 Large-Dimensional Transformations

We first split $R[x]/\langle \Phi_{17}(x^{96}) \rangle$ into $\prod_{i=1,\dots,16} R[x]/\langle x^{96}-\omega_{17}^i \rangle$ with truncated size-17 Rader's FFT. We then twist all $R[x]/\langle x^{96}-\omega_{17}^i \rangle$ into $R[x]/\langle x^{96}-1 \rangle$ by observing $\omega_{17}=\omega_{17}^{1344}=\left(\omega_{17}^{14}\right)^{96}$. See Algorithms 6 and 7 for illustrations. In practice, twisting is merged with the scaling by ω_{17}^{-i} at the end of truncated Rader's FFT. For each $R[x]/\langle x^{96}-1 \rangle$, we split it into $\prod_j R[x]/\langle x^{16}-\omega_6^j \rangle$ with Good–Thomas FFT. This amounts to applying the tensor product of size-2 and size-3 cyclic FFT. Algorithm 8 is an illustration. Since everything so far is defined over chunks of 16-tuples, all of the above are vectorization-friendly.

```
Algorithm 6 Big picture of R[x]/\langle \Phi_{17}(x^{96})\rangle \cong \left(R[x]/\langle x^{96}-1\rangle\right)^{16}.

Input(s): poly[0-15] [0-95] = a(x) = \sum_{i=0,...,1535} a_i x^i.

Output(s): \left(a\left(\omega_{17}^{14(i_0+1)}y\right) \bmod \left(y^{96}-1\right)\right)_{i_0=0,...,15}.

1: for i_1 \in \{0,\ldots,95\} do

2: poly_NTT[0-15] [i_1] = trunc_Rader(poly[0-15] [i_1]).

3: end for

4: \forall i_0 \in \{0,\ldots,15\}, poly_NTT[i_0] [0-95] = \omega_{17}^{i_0+1}a(x) \bmod \left(x^{96}-\omega_{17}^{i_0+1}\right).

5: for i_0 \in \{0,\ldots,15\} do

6: poly_NTT[i_0] [0-95] = twist96 (poly_NTT[i_0] [0-95], \omega_{17}^{14(i_0+1)}).

7: end for

8: \forall i_0 \in \{0,\ldots,15\}, poly_NTT[i_0] [0-95] = a\left(\omega_{17}^{14(i_0+1)}y\right) \bmod \left(y^{96}-1\right).
```

Algorithm 7 Implementation of trunc_Rader.

```
Input(s): a(x) = \sum_{i=0,...,15} a_i x^i.

Output(s): (c_i)_{i=0,...,15} = (\omega_{17}^{i+1} a(\omega_{17}^{i+1}))_{i=0,...,15}.

1: for i=0,...,15 do

2: \operatorname{src}[(16 - \log_3(i+1)) \bmod^+16] = a_i.

3: \operatorname{twiddle}[\log_3(i+1)] = \omega_{17}^{i+1}.

4: end for

5: \operatorname{buff}[0\text{-}15] = \operatorname{src}[0\text{-}15] \cdot \operatorname{twiddle}[0\text{-}15] \bmod (x^{16} - 1).

6: for i=0,...,15 do

7: c_i = \operatorname{buff}[\log_3(i+1)].

8: end for

9: (c_i)_{i=0,...,15} = (\omega_{17}^{i+1} a(\omega_{17}^{i+1}))_{i=0,...,15}.
```

Algorithm 8 NTT_{$R[x]/\langle x^6-1\rangle = \pi \circ \left(\mathsf{NTT}_{R[y]/\langle y^2-1\rangle} \otimes \mathsf{NTT}_{R[z]/\langle z^3-1\rangle} \right) \circ \pi$ via Good-Thomas FFT where $\pi = P_{(14)}$ is the permutation matrix swapping the 1st and 4th element.}

```
Input(s): a(x) = \sum_{i=0,...,5} a_i x^i.

Output(s): (a0, a4, a2, a3, a1, a5) = (a(\omega_6^i))_{i=0,...,5}.

1: (a0,...,a5) = (a_0, a_4, a_2, a_3, a_1, a_5).

2: (a0, a1, a2) = \text{NTT}_{R[z]/\langle z^3 - 1 \rangle} (a0, a1, a2).

3: (a3, a4, a5) = \text{NTT}_{R[y]/\langle y^2 - 1 \rangle} (a3, a4, a5).

4: (a0, a3) = \text{NTT}_{R[y]/\langle y^2 - 1 \rangle} (a0, a3).

5: (a1, a4) = \text{NTT}_{R[y]/\langle y^2 - 1 \rangle} (a1, a4).

6: (a2, a5) = \text{NTT}_{R[y]/\langle y^2 - 1 \rangle} (a2, a5).

7: \Rightarrow (a0, a4, a2, a3, a1, a5) = (a(\omega_6^i))_{i=0,...,5} \text{ for } \omega_6 = \omega_2 \omega_3.
```

The remaining problems are multiplying in $R[x] / \langle x^{16} - \omega_6^j \rangle$ – we have 16 instances for each of $R[x] / \langle x^{16} - \omega_6^j \rangle$. Obviously, this implies permutation-friendliness – we simply partition $6 \cdot 16$ size-16 polynomial multiplications into 6 partitions where each partition contains 16 instances, and apply transposition matrices to all the partitions. By definition, we have $\omega_6^j = \omega_2^{j \mod +2} \omega_3^{j \mod +3}$. Let's rewrite $(j \mod +2, j \mod +3)$ as (j_0, j_1) for all j.

After interleaving, we twist as follows:

$$\frac{R[x]}{\left\langle x^{16} - \omega_6^j \right\rangle} = \frac{R[x]}{\left\langle x^{16} - \omega_2^{j_0} \omega_3^{j_1} \right\rangle} = \frac{R[x]}{\left\langle x^{16} - \omega_2^{j_0} \left(\omega_3^{j_1} \right)^{16} \right\rangle} \cong \frac{R[x]}{\left\langle x^{16} - \omega_2^{j_0} \right\rangle}.$$

In practice, we merge the twisting $R[x]/\langle x^{16} - \omega_6^j \rangle \cong R[x]/\langle x^{16} - \omega_2^{j_0} \rangle$ and interleaving.

5.2 Small-Dimensional Polynomial Multiplications

The remaining problems are multiplying in $R[x]/\langle x^{16} \pm 1 \rangle$.

 $R[x]/\langle x^{16}-1\rangle$. For $R[x]/\langle x^{16}-1\rangle$, if one of multiplicands is known in prior (as in the case of truncated Rader's FFT), we split $R[x]/\langle x^{16}-1\rangle$ with Cooley–Tukey butterflies whenever the ring takes the form $R[x]/\langle x^{2^k}-1\rangle$ for a k>0:

$$\frac{R[x]}{\langle x^{16}-1\rangle}\cong\frac{R[x]}{\langle x-1\rangle}\times\frac{R[x]}{\langle x+1\rangle}\times\frac{R[x]}{\langle x^2+1\rangle}\times\frac{R[x]}{\langle x^4+1\rangle}\times\frac{R[x]}{\langle x^8+1\rangle}.$$

For a size-16 polynomial c(x) with coefficients known in prior, we precompute its image with proper scaling. In other words, we compute $8c(x=1), 8c(x=-1), 4c(x^2=-1), 2c(x^4=-1), c(x^8=-1)$ where c(x=1) is obtained by replacing x with 1 and similarly for $c(x=-1), c(x^2=-1), c(x^4=-1), c(x^8=-1)$. The scaling of the coefficients is then the same while inverting Cooley–Tukey butterflies. On the other hand, if both of the multiplicands are unknown, we split $R[x]/\langle x^{16}-1\rangle$ into

$$\frac{R[x]}{\langle x^{16}-1\rangle}\cong\frac{R[x]}{\langle x^4-1\rangle}\times\frac{R[x]}{\langle x^4+1\rangle}\times\frac{R[x]}{\langle x^8-1\rangle}$$

and perform additional additions doubling half of the coefficients in order to have consistent scaling for all the coefficients.

 $R[x]/\langle x^{16}+1\rangle$. For $R[x]/\langle x^{16}+1\rangle$, we split with Bruun's butterfly as follows

$$\frac{R[x]}{\langle x^{16}+1\rangle} \cong \frac{R[x]}{\langle x^8+\sqrt{2}x^4+1\rangle} \times \frac{R[x]}{\langle x^8-\sqrt{2}x^4+1\rangle}.$$

Finally, we perform Karatsuba for size-8 polynomial multiplications and schoolbook otherwise. The criteria for choosing between Cooley–Tukey butterflies, Bruun's butterflies, Karatsuba, and schoolbook is based on our experiments.

5.3 Comparisons to Prior Vectorized Implementation

We briefly compare our vectorized implementation to prior AVX2 work [BBCT22] and Neon work [CCH⁺23].

5.3.1 Comparison to [BBCT22]

[BBCT22] reduced the computing task to

$$\frac{R[x]}{\langle (x^{1024}+1)(x^{512}-1)\rangle}.$$

They first introduced $x^{32} - y$ and rewrite $R[x]/\langle (x^{1024} + 1)(x^{512} - 1)\rangle$ as

$$\frac{\frac{R[x]}{\langle x^{32}-y\rangle}[y]}{\langle (y^{32}+1)(y^{16}-1)\rangle}.$$

They then applied Schönhage [Sch77] by replacing $x^{32}-y$ with $x^{64}+1^3$. Since $x^{64}\sim -1$ in $R[x]/\langle x^{64}+1\rangle$, x is a principal 128-th root of unity for radix-2 Cooley–Tukey FFT in y. We argue that Schönhage is vectoriation-friendly: the replacement of $x^{32}-y$ by $x^{64}+1$ can be described as $\binom{1}{0}\otimes I_{32}$ and multiplication by powers of x over $R[x]/\langle x^{64}+1\rangle$ are negacyclic shifts. Therefore, the overall transformation can be written as:

$$\left(\prod (\mathtt{AddSub}_i \otimes I_{16}) \mathtt{BlockNegShift}_i\right) \left(I_{48} \otimes \begin{pmatrix} 1 \\ 0 \end{pmatrix} \otimes I_{32}\right)$$

for BlockNegShift_i $\in SD$ and AddSub_i's implementing add-sub pairs of various sizes. For $R[x]/\langle x^{64}+1\rangle$, they applied Nussbaumer [Nus80] as follows:

$$\frac{R[x]}{\langle x^{64}+1\rangle}\cong\frac{\frac{R[z]}{\langle z^8+1\rangle}[x]}{\langle x^8-z\rangle}\hookrightarrow\frac{\frac{R[z]}{\langle z^8+1\rangle}[x]}{\langle x^{16}-1\rangle}.$$

Again, z is a principal 16-th root of unity for radix-2 Cooley–Tukey FFT in x. We claim that Nussbaumer is permutation friendly with an informal justification: Observe that Nussbaumer can be seen as a composition of Schönhage and interleaving, it remains to specify clearly the interleaving step. We leave the formal justification to readers.

The remaining problem is to compute $48 \cdot 16 = 768$ polynomial multiplications of the form $R[z]/\langle z^8 + 1 \rangle$. [BBCT22] applied recursive Karatsuba to $R[z]/\langle z^8 + 1 \rangle$.

On the other hand, after the FFT computation, our transformation results in only 48 size-4 cyclic convolution, 48 size-4 negacyclic convolution, 48 size-8 negacyclic convolution, 48 polynomial multiplications in $R[x]/\langle x^8 + \sqrt{2}x^4 + 1 \rangle$, and 48 polynomial multiplications in $R[x]/\langle x^8 - \sqrt{2}x^4 + 1 \rangle$. A rough measure is to count the number of coefficients involved. $768 \cdot 8 = 6144$ coefficients are involved in [BBCT22]'s implementation while only $48 \cdot 4 + 48 \cdot 8 + 48 \cdot 8 + 48 \cdot 8 = 1536$, a quater of 6144, are involved in our implementation. This explains the significant overal performance improvement.

5.3.2 Comparison to $[CCH^+23]$

In this section, we compare our transformation to the Good-Rader-Bruun approach used in [CCH⁺23]. We will explain the benefit of truncated Rader's FFT over Rader's FFT in terms of permutation-friendliness.

Although we rely on several techniques used in [CCH⁺23], our implementation is actually quite different from them in terms of transformation choices. They computed in $R[x]/\langle x^{1632}-1\rangle$. [CCH⁺23] first permuted with the equivalences $x^{16}\sim uvw,u^{17}\sim v^3\sim w^2\sim 1$. This gives them $\bar{R}[u]/\langle u^{17}-1\rangle\otimes \bar{R}[v]/\langle v^3-1\rangle\otimes \bar{R}[w]/\langle w^2-1\rangle$ where $\bar{R}=R[x]/\langle x^{16}-uvw\rangle$. They then applied size-17 Rader's FFT without truncation to $\bar{R}[u]/\langle u^{17}-1\rangle$, size-3 FFT to $\bar{R}[v]/\langle v^3-1\rangle$, and size-2 FFT to $\bar{R}[w]/\langle w^2-1\rangle$. Obviously, since all transformations operate over \bar{R} , they are vectorization-friendly.

Substituting uvw with various ω_{102}^i , [CCH⁺23] splitted $R[x]/\langle x^{16} - \omega_{102}^i \rangle$ with Cooley–Tukey and Bruun's FFTs without twisting. This step is not permutation-friendly: Observe that permutation-friendliness is defined only for monomorphisms with rank over R a

³This map is a monomorphism in the sense that products in $R[x]/\langle x^{32}-y\rangle$ can be uniquely identified while computing in $R[x]/\langle x^{64}+1\rangle$

multiple of $v^2=16^2=256$, we can't find a suitable interleaving matrix for permutation-friendliness. They applied Cooley–Tukey to 48 instances of $R[x]/\langle x^{16}-\omega_{102}^i\rangle$, $i\equiv 0\pmod 2$ and Bruun to 48 instances of $R[x]/\langle x^{16}-\omega_{102}^i\rangle$, $i\equiv 1\pmod 2$. For the remaining 6 instances, they interleaved with don't-cares and applied Karatsuba.

In our implementation, we compute in $R[x]/\langle \Phi_{17}(x^{96})\rangle$ by first applying truncated size-17 Rader's FFT which results in $\frac{16}{17}$ number of subproblems of size-17 Rader's FFT. Since 16|96, the transformation can be written as a tensor product of a matrix and I_{16} . After twisting and Good–Thomas FFT, we have 48 instances of $R[x]/\langle x^{16}-1\rangle$ and 48 instances of $R[x]/\langle x^{16}+1\rangle$ with no leftovers – hence permutation-friendly.

For the size-16 polynomial multiplications of the form $R[x]/\langle x^{16} - \zeta \rangle$, [CCH⁺23] applied the Cooley–Tukey and Bruun's FFTs defined on $R[x]/\langle x^{16} - \zeta \rangle$. We apply twisting instead as implemented in common AVX2-optimized implementations. The benefit of twisting is twofold: (i) twisting effectively reduces the range of the coefficients (this is widely known, but somehow [CCH⁺23] didn't implement it), and (ii) Bruun's FFT is more favorable when the modulus takes the form $x^8 \pm \sqrt{2}x^4 + 1$ in terms of arithmetic and register scheduling since $\sqrt{2}^2 - 1 = 1$ and the dependency chain is shorter than $x^8 \pm \alpha x^4 + \beta$ in general.

6 Results

6.1 Benchmarking Environment

We benchmark on Intel(R) Core(TM) i7-4770K (Haswell) processor with frequency 3.5 GHz, and Intel(R) Xeon(R) CPU E3-1275 v5 (Skylake) with frequency 3.6 GHz. For benchmarking polynomial multiplications, we compile with GCC 10.4.0 on Haswell and GCC 11.3.0 on Skylake using the optimization flag -03. For the batch key generation, we reuse the libsntrup761-20210608 package from [BBCT22]. For the encapsulation and decapsulation, we benchmark with the benchmarking framework SUPERCOP, version supercop-20230530. TurboBoost and hyperthreading are disabled throughout the entire benchmarking.

6.2 Performance of Polynomial Multiplication

We provide the performance cycles of functions mulcore and polymul in Table 1. mulcore computes the product in $\mathbb{Z}_{4591}[x]$ with potential scaling by a predefined constant, and polymul additionally reduces the product modulo $x^{761}-x-1$ and mitigates the potential scaling. Compared to [BBCT22], our mulcore is $1.90\times$ and $2.05\times$ faster on Haswell and Skylake. For polymul, our implementation is $1.99\times$ and $2.16\times$ faster on Haswell and Skylake. In Appendix A, we provide the detailed performance numbers. Additionally, Table 2 summarizes the Haswell and Skylake cycles of existing x86 implementations.

Table 1: Performance cycles of big-by-big polynomial multiplications for ntrulprs761/sntrup761 on Haswell and Skylake with AVX2.

	[BBCT22]*	This work	
Haswell			
$\texttt{mulcore}\;(\mathbb{Z}_{4591}[x])$	23 460	12336	
$ ext{polymul}\left(rac{\mathbb{Z}_{4591}[x]}{\langle x^{761}-x-1 angle} ight)$	25356	12 760	
Skylake			
$\texttt{mulcore}\;(\mathbb{Z}_{4591}[x])$	20 070	9778	
$ \hspace{1cm} \texttt{polymul} \left(\frac{\mathbb{Z}_{4591}[x]}{\langle x^{761} - x - 1 \rangle} \right) $	21 364	9 876	

^{*} Our own benchmarks.

Table 2: Performance cycles of existing polynomial multiplications for ntrulpr761/sntrup761.

	Haswell cycles	Skylake cycles	Applicability
round1***	29 013	26 401	Big-by-small
round2***	17 546	14 448	Big-by-small
avx800***	16514	13 222	Big-by-small
[BBCT22]**	25356	21 364	Big-by-big
This work	12 760	9 876	Big-by-big

^{**} Our own benchmarks.

6.3 Performance of Scheme

Finally, we compare the overall performance of $\mathtt{sntrup761}$ on Haswell and Skylake. For the batch key generation with batch size 32, we reduce the amortized cost by 12% on Haswell and 8% on Skylake. For encapsulation, we reduce the cost by 7% on Haswell and 10% on Skylake. For decapsulation, we reduce the cost by 10% on Haswell and 13% on Skylake. We summarize the performance in Table 3.

Table 3: Performance cycles of sntrup761 with batch key generation using Montgomery's trick. For the batch key generation, we benchmark with batch size 32.

	[BBCT22]****	SUPERCOP	This work
Haswell			
Batch key generation (amortized)	154552	-	136003
Encapsulation	-	47 464	44 108
Decapsulation	-	56 064	50 080
Skylake			
Batch key generation (amortized)	129 159	-	118939
Encapsulation	-	40 653	36 486
Decapsulation	-	47 387	41 070

^{****} Our own benchmark.

^{***} Implementations in SUPERCOP (version supercop-20230530). See https://bench.cr.yp.to/impl-core/multsntrup761.html for numbers on other processors.

7 Future Works

This work shows that polynomial multiplication in a ring lacking common beliefs of friendliness measures for implementations, truncated Rader's, Good–Thomas, and Bruun's FFT are more favorable than Schönhage's and Nussbaumer's FFTs.

There are several future works for parameter set sntrup761. An immediate one is to generate several multipliers of sizes $2^{i_0}3^{i_1}5^{i_2}$ based on this work for FFT-based fast constant-time GCD computation [BY19]. Additionally, an ambitious goal is to explore various possible vectorized multipliers for other NTRU Prime parameter sets. We briefly draft below for the parameter sets ntrulpr857/sntrup857, ntrulpr1013/sntrup1013, and ntrulpr1277/sntrup1277.

In ntrulpr857/sntrup857, we want to multiply polynomials in $\mathbb{Z}_{\mathbb{Z}_{5167}[x]}/\langle x^{857}-x-1\rangle$. We propose to multiply in $\mathbb{Z}_{5167}[x]/\langle \Phi_7(x^{288})\rangle$ with truncated Rader's, Good–Thomas, and Bruun's FFTs. Since $5167-1=2\cdot 3^2\cdot 7\cdot 41$ and $5167+1=2^4\cdot 17\cdot 19$, we can define principal roots ω_7 , ω_2 , ω_9 , and $\mathbb{Z}_{5167}[x]/\langle x^{16}+1\rangle$ splits into eight trinomials. We first compute the isomorphism

$$\frac{\mathbb{Z}_{5167}[x]}{\langle \Phi_7(x^{288}) \rangle} \cong \prod_{i=1,\dots,6} \frac{\mathbb{Z}_{5167}[x]}{\langle x^{288} - \omega_7^i \rangle} \cong \left(\frac{\mathbb{Z}_{5167}[x]}{\langle x^{288} - 1 \rangle} \right)^6$$

with truncated size-7 Rader's FFT and twisting. We then apply Good–Thomas FFT turning a size-18 cyclic DFT into a tensor product of a size-2 cyclic DFT and a size-9 cyclic DFT. The size-9 cyclic DFT is then implemented with Cooley–Tukey FFT using radix-3 butterflies. After applying the size-18 cyclic DFT, we twist all the polynomial rings into cyclic and negacyclic ones. Below is an illustration:

$$\frac{\mathbb{Z}_{5167}[x]}{\langle x^{288} - 1 \rangle} \cong \prod_{i_0, i_1} \frac{\mathbb{Z}_{5167}[x]}{\langle x^{16} - \omega_2^{i_0} \omega_9^{i_1} \rangle} \cong \prod_{i_0} \left(\frac{\mathbb{Z}_{5167}[x]}{\langle x^{16} - \omega_2^{i_0} \rangle} \right)^9.$$

The remaining problems are 54 polynomial multiplications in each of $\mathbb{Z}_{5167}[x]/\langle x^{16}-1\rangle$ and $\mathbb{Z}_{5167}[x]/\langle x^{16}+1\rangle$. We interleave 48 polynomials in $\mathbb{Z}_{5167}[x]/\langle x^{16}-1\rangle$ and 48 polynomials in $\mathbb{Z}_{5167}[x]/\langle x^{16}+1\rangle$, and apply AVX2-optimized Cooley–Tukey and Bruun's FFT as shown in Section 5.2. For the remaining 12 polynomial multiplications in $\mathbb{Z}_{5167}[x]/\langle x^{16}\pm 1\rangle$, we interleave them with four don't-care polynomials and apply AVX2-optimized Karatsuba. As a side note, if the number of elements is smaller than 16, one can possibly choose other approaches. Let's take Neon as an example. Since each vector register in Neon holds 128-bit of data, or equivalently, 8 elements in \mathbb{Z}_{5167} , we can instead interleave 6 polynomials in $\mathbb{Z}_{5167}[x]/\langle x^{16}-1\rangle$ with don't-cares and apply Cooley–Tukey FFT. Similar arguement applies to $\mathbb{Z}_{5167}[x]/\langle x^{16}+1\rangle$ with Bruun's FFT.

We estimate the performance of $\mathbb{Z}_{5167}[x]/\langle \Phi_7(x^{288})\rangle$ as follows. For the truncated Rader-7 FFT, the performance is the same as a size-6 cyclic convolution. We overestimate the size-6 cyclic convolution with two size-6 cyclic FFTs followed by twisting since one of the operands contains only publicly known constants where precomputation is allowed. We regard this as an overestimation since there could be faster way for the size-6 cyclic convolution. For the follow up size-18 cyclic DFT via Good–Thomas and Cooley–Tukey FFTs, since the performance is no worsee than a size-36 cyclic DFT and one size-36 cyclic DFT can be implemented as two layers of size-6 cyclic FFTs followed by twisting, we again overestimate the performance of size-18 cyclic DFT with two size-6 cyclic FFTs followed by twisting. Based on the performance numbers in Table 5, we overestimate the performance cycles of

$$\frac{\mathbb{Z}_{5167}[x]}{\langle \Phi_7(x^{288}) \rangle} \cong \left(\prod \frac{\mathbb{Z}_{5167}[x]}{\langle x^{16} \pm 1 \rangle} \right)^{54}$$

as $5112 \cdot \frac{1}{256 \cdot 8} \cdot \frac{1728}{6} \cdot 4 = 2875.5$ cycles. For the performance estimation of size-16 cyclic/negacyclic polynomial multiplications, we estimate it as the sum of performance numbers for 48 Cooley–Tukey FFT for $\mathbb{Z}_{5167}[x]/\langle x^{16}-1\rangle$, 48 Bruun's FFT for $\mathbb{Z}_{5167}[x]/\langle x^{16}+1\rangle$, and 16 Karatsuba for $\mathbb{Z}_{5167}[x]/\langle x^{16}-\zeta\rangle$ where ζ could be different among the 16 polynomials. Therefore, the performance of size-16 polynomial multiplication is $3168 \cdot \frac{48}{128} + 4720 \cdot \frac{48}{128} + 5588 \cdot \frac{16}{128} = 3656.5$ cycles. Finally, the remaining estimation is about interleaving polynomials. Since $1728 < 256 \cdot 7$, we overestimate the performance as $768 \cdot \frac{7}{8} = 672$. In summary, the overall overestimation of the performance is $2875.5 \cdot 3 + 672 \cdot 3 + 3656.5 = 14299$ cycles.

We briefly draft below some promising approaches for larger parameter sets. For ntrulpr1013/sntrup1013, we reduce polynomial multiplication in $\mathbb{Z}_{7177}[x]/\langle x^{1013}-x-1\rangle$ to $\mathbb{Z}_{7177}[x]/\langle \frac{x^{2496}-1}{x^{312}+1}\rangle$ with Rader's, truncated Good–Thomas [HVDH22, Section 3.5], and Cooley–Tukey FFTs. For ntrulpr1277/sntrup1277, we reduce the computing task in $\mathbb{Z}_{7879}[x]/\langle x^{1277}-x-1\rangle$ to $\mathbb{Z}_{7879}[x]/\langle (x^{2496}-1)(x^{64}+1)\rangle$ with Rader's, Good–Thomas, and Cooley–Tukey FFTs.

A Profiling of Polynomial Multiplication

Table 4: Performance of permutations with twisting. Numbers are medians of 100,000 iterations where each iteration computes with the indicated repetions of algebraic operations.

	Haswell	Skylake
Twist with pre-transpose (8×, 256 coeff. each)	768	632
Twist wit post-transpose (8×, 256 coeff. each)	720	618

Table 5: Performance cycles of butterfly operations. Numbers are medians of 100,000 iterations where each iteration computes with the indicated repetions of algebraic operations.

	Haswell	Skylake
Radix-(3,2)		
Radix- $(3,2)$ with pre-twist (256×8)	5 112	3794
Radix- $(3,2)$ with post-twist (256×8)	4 904	3550
Radix-17		
Truncated Rader-17 for $R[x]/\langle \Phi_{17}(x)\rangle$ (128×)	2504	1776
Inverse of truncated Rader-17 for $R[x]/\langle \Phi_{17}(x) \rangle$ (128×)	2528	1752

Table 6: Performance cycles of power-of-two base multiplications. Numbers are medians of 100,000 iterations where each iteration computes with the indicated repetions of algebraic operations. Karatsuba for $R[x]/\langle x^{16}-\zeta\rangle$ is only involved in our development and not used in our implementation.

	Approach	Haswell	Skylake
$R[x]/\langle x^{16}-1\rangle$ (128×)	Cooley-Tukey	3 168	2576
$R[x]/\langle x^{16}+1\rangle$ (128×)	Bruun's	4720	3 6 1 6
$R[x]/\langle x^{16} - \zeta \rangle $ (128×)	Karatsuba	5 588	4428

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